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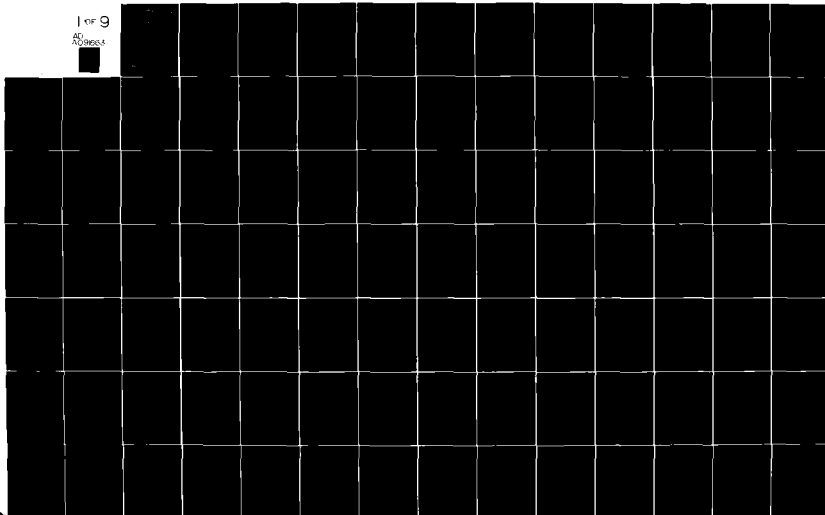
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

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FINAL REPORT

SPEECH OPTIMIZATION AT 9600 BITS/SECOND

VOLUME 2

**REAL-TIME
SOFTWARE AND HARDWARE**

**SUBMITTED TO
DEFENSE COMMUNICATIONS AGENCY**

SEPTEMBER 30, 1980

Sylvania Systems Group
Communication Systems Division
GTE Products Corporation
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FINAL REPORT, DTIC 71-117-111

Speech Optimization at 9600 Bits/Second.

Volume 2.

Real-Time Software and Hardware.



11/13/80 SEP 12

13/DTIC 71-117-111

Submitted to

Defense Communications Agency

September 30, 1980

101 A.S. / Solid State L. / 11 S. / T. W. L. / E. / 11
L. / 11

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SECURITY CLASSIFICATION OF THIS PAGE (When Data Entered)

REPORT DOCUMENTATION PAGE		READ INSTRUCTIONS BEFORE COMPLETING FORM
1. REPORT NUMBER	2. GOVT ACCESSION NO.	3. RECIPIENT'S CATALOG NUMBER
	AD-A074	664
4. TITLE (and Subtitle)		5. TYPE OF REPORT & PERIOD COVERED
9600 BPS Speech Optimization Study Volume 2		Final Report October 1978 - September 1980
6. AUTHOR(s)		7. PERFORMING ORG. REPORT NUMBER
A. J. Goldberg, L. Cosell, S. Kwon, L. Bergeron, and R. Cheung		
8. PERFORMING ORGANIZATION NAME AND ADDRESS		9. CONTRACT OR GRANT NUMBER(s)
GTE Sylvania 77 "A" Street Needham Heights, MA 02194		DCA 100-78-C-0064
10. CONTROLLING OFFICE NAME AND ADDRESS		11. PROGRAM ELEMENT, PROJECT, TASK AREA & WORK UNIT NUMBERS
Defense Communications Agency Contract Management Division, Code 260 Washington, D.C. 20305		
12. MONITORING AGENCY NAME & ADDRESS (if different from Controlling Office)		13. REPORT DATE
		September 30, 1980
		14. NUMBER OF PAGES
		Volume 2 - 821 pages
		15. SECURITY CLASS. (of this report)
		Unclassified
		16. DECLASSIFICATION/DOWNGRADING SCHEDULE
17. DISTRIBUTION STATEMENT (of this Report)		
Distribution of this document is unlimited. It may be released to the Clearinghouse, Department of Commerce, for sale to the general public.		
18. DISTRIBUTION STATEMENT (of the abstract entered in Block 20, if different from Report)		
19. SUPPLEMENTARY NOTES		
20. KEY WORDS (Continue on reverse side if necessary and identify by block number)		
speech coding, 9600 bps speech transmission, adaptive transform coder, adaptive bit allocation, discrete cosine transform, digital voice ter- minal, real-time speech coder		
21. ABSTRACT (Continue on reverse side if necessary and identify by block number)		
This report describes the design and development of a real-time adaptive transform coder that transmits high-quality speech over a 9600 bps channel with bit-error rates of up to 1% without significant loss of speech fidelity. The report presents the results of our FORTRAN simulations on the adaptive transform coder which maximized the quality of the trans- mitted speech. Important aspects of the ATC algorithm which are optimized (cont'd)		

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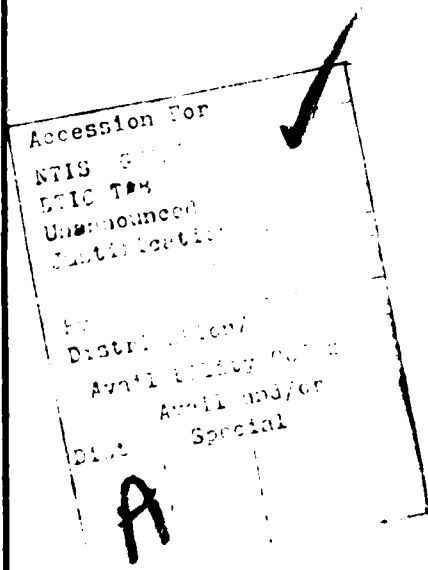
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20. Abstract (cont'd)

were specification and transmission of the side-band information, accuracy of the pitch and voicing decisions, and error-protection of the important transmission parameters. Also included is the system design, detailed documentation, and program listings of the MAP-300 real-time implementation of the optimized ATC speech coder. Finally, the report includes a description of analog equipment GTE built to interface the MAP-300 to telephone handsets and tape recorders and a description of digital circuits (RS 423 compatible) to interface the MAP-300 to a modem.

This report is bound in two volumes. Volume I contains a description of the ATC system and the results of the FORTRAN simulations. Volume II contains all the information on the real-time system including documentation for implementing the ATC system on the MAP, listing of the MAP software, and documentation for the hardware built by GTE.



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Chapter 3

Real-Time Implementation

The GTE Sylvania Speech Processing System functions as a real-time, full duplex terminal of a digital voice communications system. As such, the GTE system accepts and digitizes analog voice input, processes it using an Adaptive Transform Coding (ATC) algorithm, and transmits the resulting serial bit stream at 9600 bits/second across a digital channel to a similar terminal. Because of the full duplex requirement, the system must simultaneously accept a similar bit stream, decode it to produce synthetic speech, and finally convert this synthetic speech to an analog waveform which it then plays out.

This chapter describes the design and operation of the GTE system and the hardware and software components used to construct it.

3.1 System Components

The GTE ATC system is implemented on a MAP-300 processor manufactured by CSP, Inc., Billerica, Mass. The MAP architecture consists of several independent processors, capable of parallel operation. Some of these processors are the Arithmetic Processor, used for high speed floating point operations; the Input/Output Scroll processors, of which there are three in the system, used for the Analog In, Analog Out, and Digital In/Out functions; and the CSPU, which is used to control the other processors and also to perform general computation chores.

Attached to the MAP is a GTE-manufactured Speech Processing Interface (SPI). The SPI includes a handset with earphone and high-quality microphone, amplifiers, and band-limiting filters. The SPI interfaces to the ADAM and AOM scrolls. The Full Duplex Interface (FDI) is also

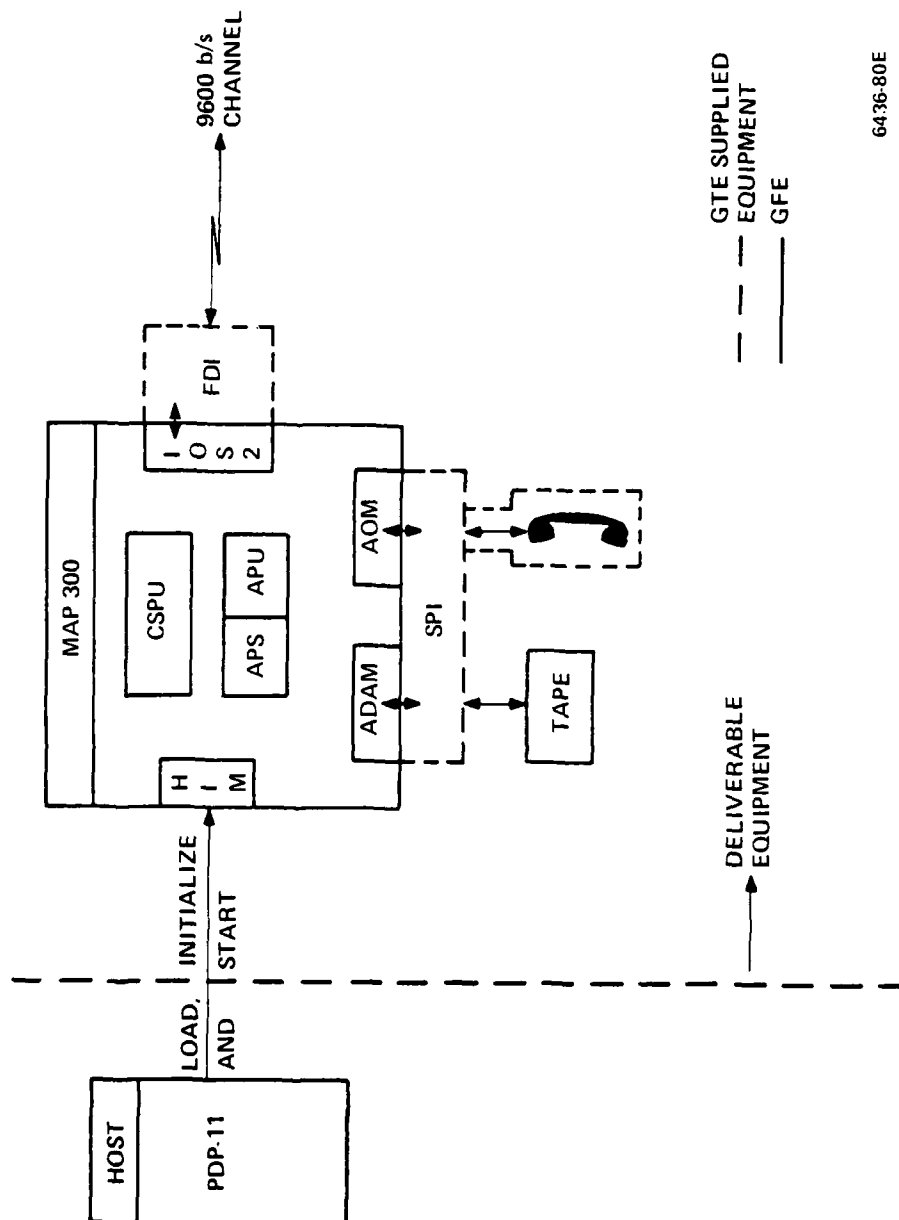
attached to the MAP. The FDI resides physically on the I/O scroll board and provides 9600 bps digital ports and their interfaces to the I/O scroll. Two additional SPI's and FDI's to be used on other MAP systems have been supplied by GTE.

The MAP is attached to a host PDP-11 which is used to load the MAP programs and to initialize and start them. Once the MAP has been started, it runs independently of the host. A block diagram of the system is shown in Figure 3-1.

The software components of the ATC system include modules supplied by CSP, Inc., as well as those written by GTE. Three categories of software are used in the ATC system. The first, the Fortran Control and Support programs, resides in the PDP-11 and is used for communication between the user and the MAP. The second category, Executive software, consists of the body of code that runs in the CSPU. This software is used to control and schedule all the processes in the MAP. The third category, MAP Functions, consists of the program modules that are loaded into the Arithmetic Processor and I/O Scroll processors. The scrolls require only one program each, but the Arithmetic Processor, because of limited memory, must be loaded with successive program modules and started for each. The CSPU Executive software performs this task.

3.2 System Design and Operation

The ATC system is composed of seven distinct processes. These are the Analog In process, the Analog Out process, the Digital In process, the Digital Out process, the Analysis process, the Synthesis process, and the Executive process. Figure 3-2 shows the function of and the relationship between the various processes. Conceptually, all of these

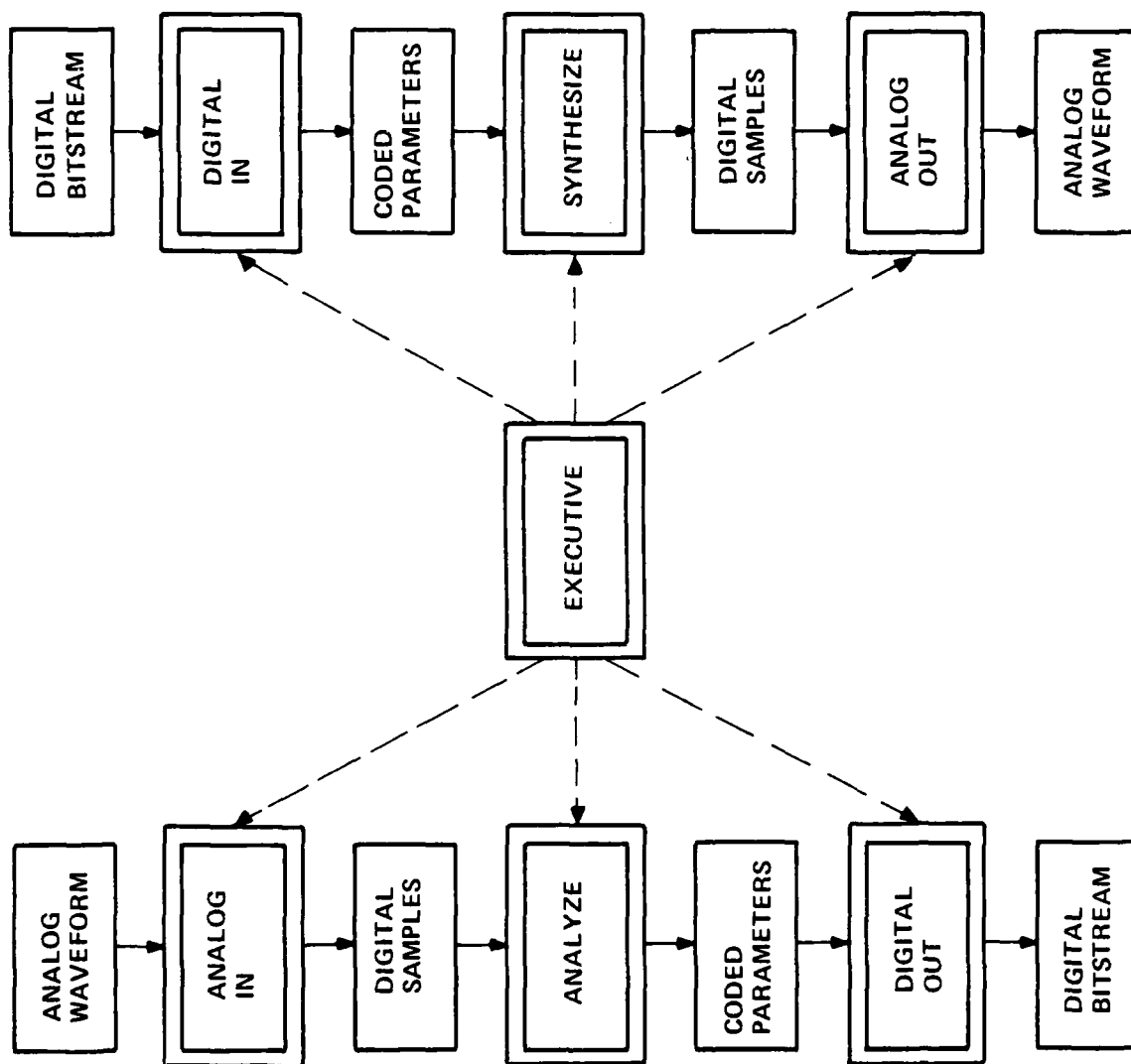


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FIGURE 3-1: ATC SYSTEM COMPONENTS



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FIGURE 3-2: BASIC PROCESSES OF ATC SYSTEM

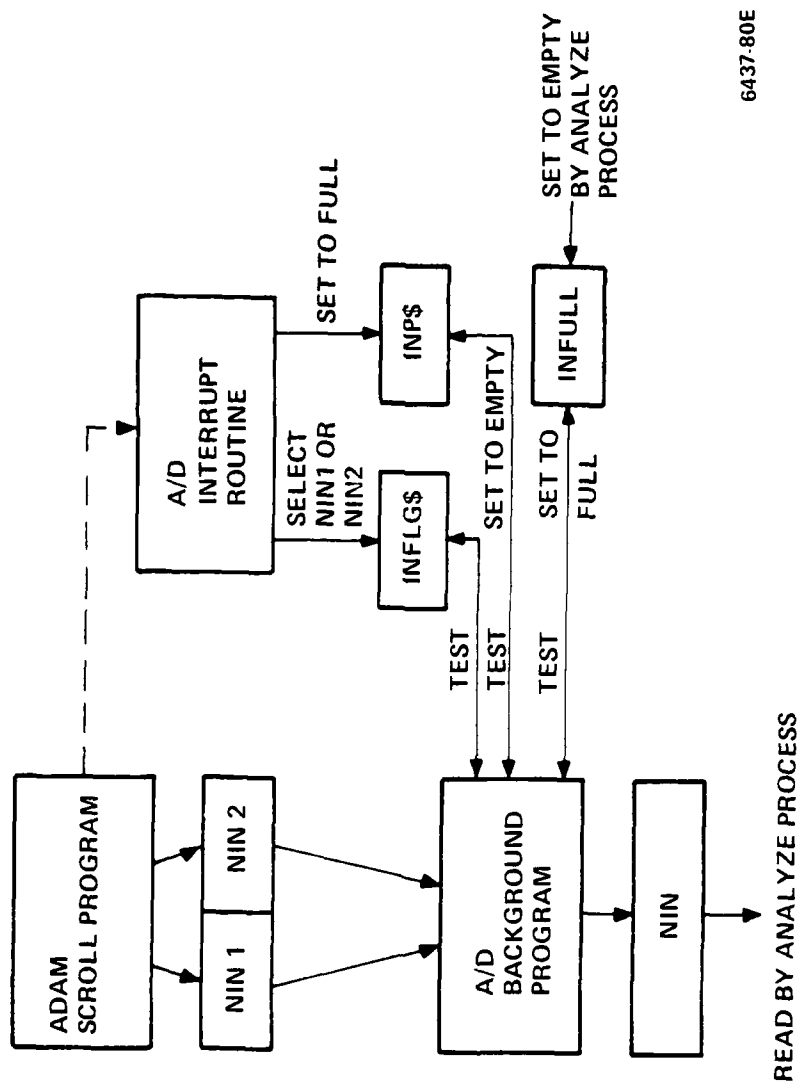
processes operate simultaneously, however, since at least portions of all of them must be executed by the CSPU or the Arithmetic Processor, both of which are sequential machines, the scheduling and synchronization of these processes is at the heart of the system design. Since the four I/O processes are essentially similar, they will be discussed first. The Analyze and Synthesize processes, which depend on their inputs on the Analog and Digital In processes, will be discussed next. Finally, the Executive process and the scheduling of the other processes will be presented.

3.2.1 Analog In Process

The Analog In process controls the A/D converter and makes the digitized samples available to the Analysis process. The Analog In process consists of three separate elements: the scroll program, the interrupt routine, and the background program. Figure 3-3 shows the components of the Analog In process.

3.2.1.1 A/D Scroll Program

This program runs in the ADAM scroll. It causes the A/D converter to operate at a pre-selectable sampling rate, which is 6400 samples/second for the purposes of the ATC system. As each sample is digitized, it is placed in the next (empty) location in the current buffer. Two buffers are used so that one can be filling while the data already in the other is used. Each buffer is 246 samples, or 37.5 milliseconds long. When a buffer is filled, an interrupt to the CSPU is generated by the scroll program, and the other buffer becomes the current buffer.



6437-80E

FIGURE 3-3: ANALOG IN PROCESS

This program is used as supplied by CSP, Inc., and as described in their documentation. The program used is ADMSD. The parameters supplied to it are ADAMPM (=63), the BID of the buffer containing the ADAM program; two identical sampling rates, each 6400 Hz; a control word which selects the internal clock, fixed point sampling, internal triggering, and no slow sampling; channel 1 select; and the double buffering pair where the input will be stored, NIN1 and NIN2.

3.2.1.2 A/D Interrupt Routine

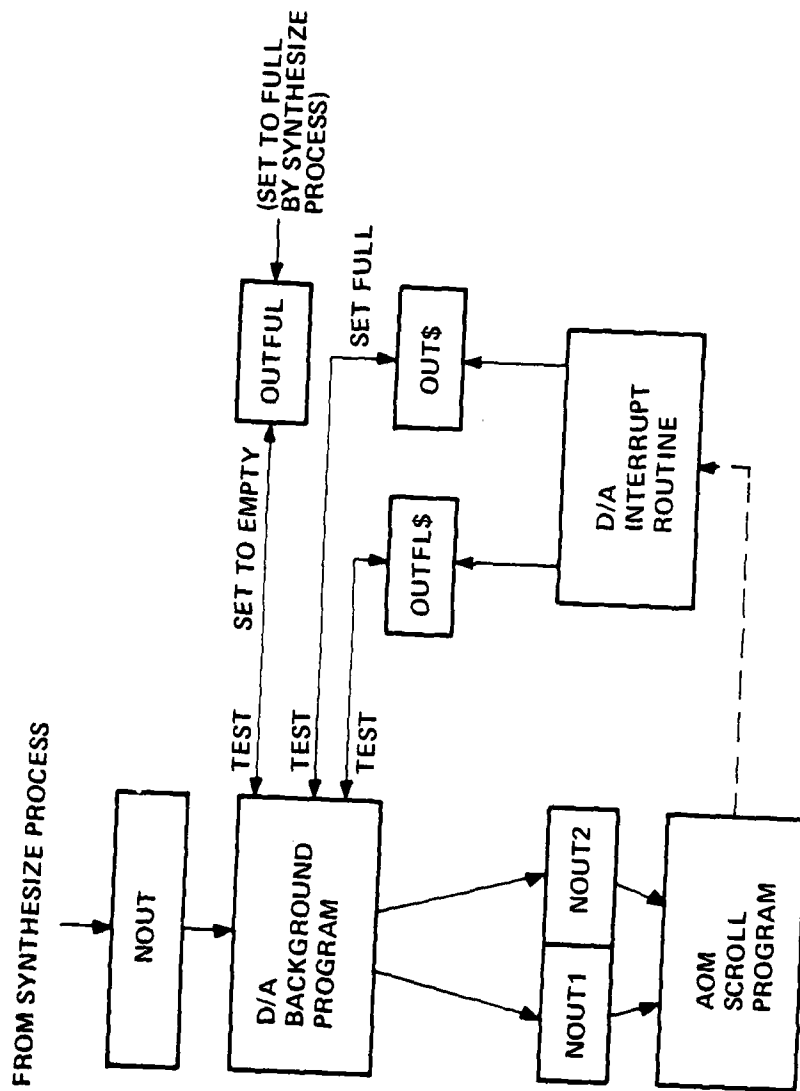
The A/D interrupt routine runs in the CSPU at interrupt level 10. It clears INPS (integer scalar 106) to indicate that a new frame of samples is available, and it also clears or sets INFLGS (integer scalar 107) to indicate that buffer NIN1 or buffer NIN2, respectively, contains the newest data.

3.2.1.3 A/D Background Program

The A/D background program runs in the CSPU. It is enabled by the combination of INPS clear and INFULL (integer scalar 110) clear, which indicates that the previous data has been used by the Analysis process. The background process copies the new data from NIN1 or NIN2, depending on the state of INFLGS, into the analysis buffer, NIN. Finally, it sets INFULL and exists.

3.2.2 Analog Out Process

The Analog Out process is similar to the Analog In process except that the data flow is reversed. The Analog Out process consists of the D/A Scroll program, the D/A interrupt routine, and the D/A background program. Figure 3-4 shows the components of the Analog Out process.



6439-80E

FIGURE 3-4: ANALOG OUT PROCESS

3.2.2.1 D/A Scroll Program

This program runs in the AOM scroll. It controls the D/A converter and uses a double buffering scheme similar to that used by the A/D scroll program. As each of the two buffers is emptied, the program generates a Device 22 interrupt to the CSPU. This program is used as supplied by CSP, Inc., and as described in their documentation. The program module used is AOMID. The parameters supplied to it are AOMPM, (=62), the BID where the AOM program is stored; a sampling frequency divider which is 1; a control word which selects fixed point output, single channel mode, and external triggering; the double buffering pair which stores the output, NOUT1 and NOUT2; and an offset, which is 2. The AOM program produces two channels of output, one of which is the data from the MAP and one of which is a ramp from 0 to 491.

3.2.2.2 D/A Interrupt Routine

The D/A interrupt routine runs in the CSPU at level 9. It clears OUTS (integer scalar 104) to indicate that a buffer has been emptied. It also clears and sets the flag OUTFLS (integer scalar 105) to indicate that buffer NOUT1 or buffer NOUT2, respectively, is the buffer most recently emptied and, hence, available.

3.2.2.3 D/A Background Program

The D/A background program module, called LOBUF, runs in the CSPU and is enabled by the combination of OUTS clear and OUTFUL (integer scalar 111) set, indicating that the Synthesis process has made new data available for output. If synchronization has been acquired, the

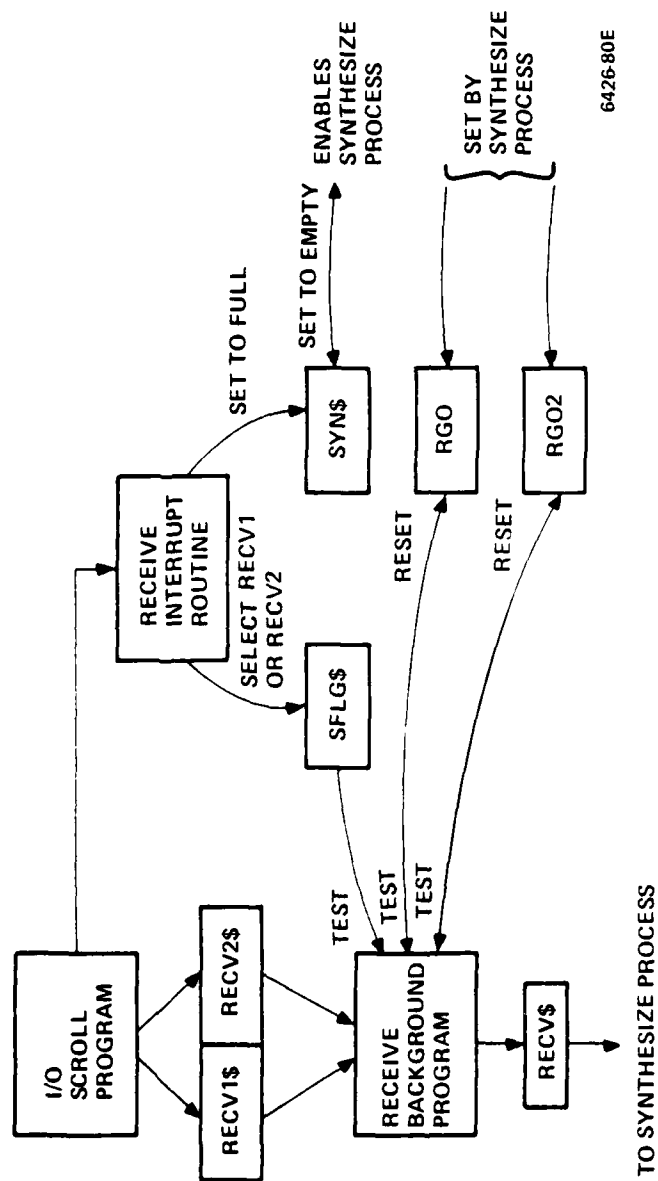
background program copies the new data from buffer NOUT into either buffer NOUT1 or buffer NOUT2, depending on the state of OUTFLS. If the system is not in synchronization, the relevant output buffer is set to zero. Finally, the background program clears OUTFUL, indicating that NOUT is available for filling by the Synthesis process.

3.2.3 Digital In Process

The Digital In process controls the digital input from the I/O scroll, examines the digital data to determine or confirm synchronization, and makes a frame of digital data available to the Synthesis process. The Digital In process consists of three components: the I/O scroll program, the Receiver interrupt routine, and the Receiver background program. Figure 3-5 shows the components of the Digital In process.

3.2.3.1 I/O Scroll Program

This program, which runs in the IOS2 Scroll, both transmits and receives digital data, and, thus, is a component of both the Digital In and the Digital Out process. This GTE written program is called LINE. It uses two sets of double buffers. One set, RECV1 and RECV2, stores the incoming bits from the modem. The other set, SEN1 and SEN2, store the bits to be output to the modem. As either of the receive buffers is filled, an interrupt from Device 16, line 2 to the CSPU is generated, and the other receive buffer begins to fill. Similarly, as either of the send buffers is emptied, an interrupt from Device 16, line 1 to the CSPU is generated, and the next bit to be output is taken from the other send buffer. All four of these buffers are 369 bits long and 16 bits wide. However, only the low order bit from each 16-bit word contains



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FIGURE 3-5: DIGITAL IN PROCESS

the digital data. The reader is referred to section 3.4 for the meanings of the other bits.

3.2.3.2 Receive Interrupt Routine

The Receive Interrupt routine runs in the CSPU at level 7. It has two functions, first, to keep track of which receive buffer has the newest data and to transmit this information to other processes, and, second, to confirm that synchronization is still valid.

When an interrupt from the receive portion of the I/O Scroll program occurs, the Receive interrupt routine clears SYNS (integer scalar 102) and clears or sets flag SFLGS (integer scalar 103) to indicate that buffer RECV1 or buffer RECV2, respectively, has been filled.

When the ATC system has achieved synchronization (integer scalar 118, RSYN, greater than zero), the bit assumed to be the sync bit for the new data is compared to the sync bit from the previous buffer. If these bits are opposite, synchronization is assumed to be still good. If they are the same, and it is the fourth non-alternating sync bit without two consecutive correctly alternating sync bits intervening, synchronization is declared lost (RSYN is cleared). The location of the sync bit in the data buffer is stored in SYNCS (integer scalar 117).

3.2.3.3 Receive Background Program

The Receive background program runs in the CSPU and is enabled by flag RGO (integer scalar 114) being set. This flag is set by the Synthesis process which is in turn enabled by SYNS being clear. The Receive background program sets SYNS to one on entry.

The Receive background program performs several functions. First, it must collect a new frame of data. This consists of finding the newest sync bit location, and using the 369 previous received bits as the frame. This is implemented by having the background program copy the new data into another set of contiguous buffers, BF1, BF2, and BF3. If RECV2 contains the new data, it is copied into BF2. If RECV1 contains the new data, it is copied into both BF1 and BF3. Then, the 369 data bits prior to the sync bit location are copied into buffer RECV. This double copying of RECV1 assures that, regardless of the position of the synchronization bit in the new frame, the 369 data bits previous to it are contained (contiguously) in the triple length buffer.

Second, the background program must acquire synchronization. That is, it must determine the position of the sync bit in the frame of data. The background program builds a histogram containing 369 bins, one for each bit in the frame. When it receives a new frame of data, the new frame is compared bit for bit against the previous frame. If a given bit alternates in the two frames, the corresponding bin in the histogram is incremented. If it does not alternate, the bin is cleared. This process continues until one bin both exceeds the acquired threshold (10.) and is a unique maximum in the histogram. When this occurs, the bit position corresponding to the maximum bin is declared the Sync position and is stored in SYNC5 (integer scalar 117) and RSYN (integer scalar 118) is set to one to indicate that synchronization has been acquired. Until synchronization is acquired, the Receive background process terminates at this point.

Once synchronization has been acquired, the background process must present an actual frame of data, one that has the sync bit at the beginning of the frame, for further processing. To do this, it copies the 369 bits previous to the sync bit of the newest frame from the triple length buffer (BF1, BF2, and BF3) into the receive processing buffer RECVS. This procedure is shown in Figure 3-6. Reasons of efficiency require that the new actual frame be present as a contiguous block within the triple length buffer, in order that a "block move" instruction can be used for the copying and no "corner turning" test is needed.

Once a new frame of data has been copied into RECVS, transmission bit errors are detected and corrected if possible. A (63, 45) BCH code is used to protect 45 bits of the sideband parameters with 18 protection bits. These 63 bits are decoded using a table-look-up procedure for efficiency. For each of the 63 transmitted bits, there are three associated values (Table 3-1). Three residues S_1 , S_3 , and S_5 are computed by exclusive-or'ing the appropriate value for each bit that is set into each sum. If each of these residues is zero, no transmission errors are detected, and the 45 data bits are used as received.

If the residues are not all zero, the number of errors is determined by computing three syndromes σ_1 , σ_2 , and σ_3 , and a determinant, which is

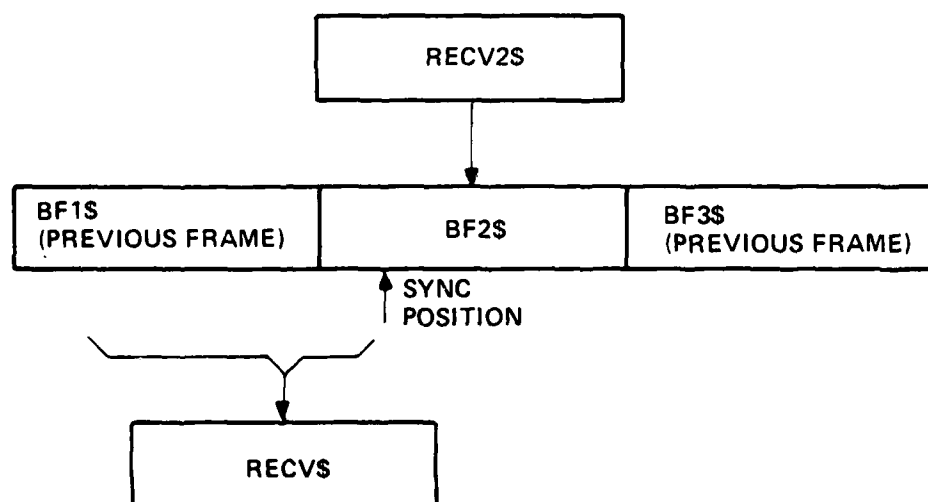
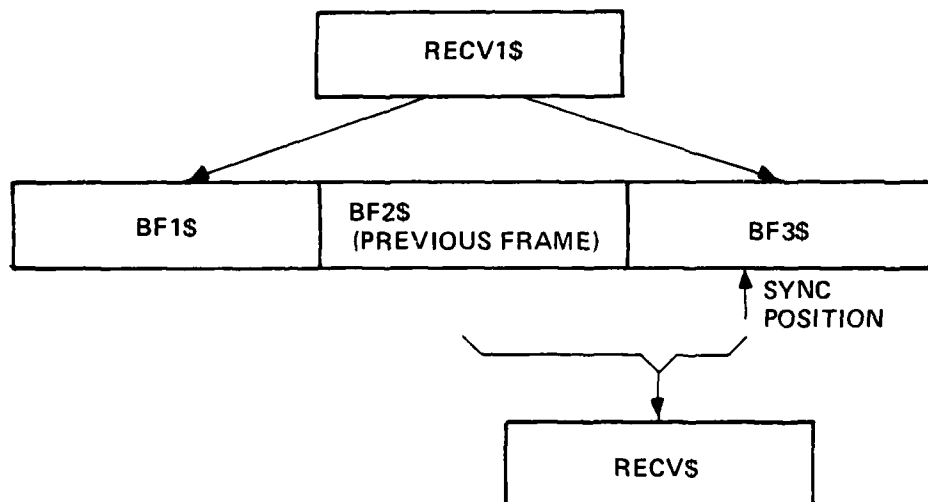
$$S_1^3 + S_3$$

If this determinant is zero, one error has occurred. σ_1 is set to one and σ_2 and σ_3 are zeroed. Otherwise,

$$\sigma_1 = S_1$$

$$\sigma_2 = S_1^2 S_3 + S_5 / (S_1^3 + S_3)$$

$$\sigma_3 = (S_1^3 + S_3) + S_1 \sigma_2$$



6427-80E

FIGURE3-6: FRAME COLLECTION

Bit Position	S ₁ (decimal)	S ₃	S ₅	Bit Position	S ₁ (decimal)	S ₃	S ₅
0	33	57	63	33	56	17	30
1	49	62	42	34	28	59	20
2	57	23	13	35	14	15	24
3	61	43	55	36	7	40	16
4	63	13	27	37	34	5	33
5	62	25	18	38	17	24	62
6	31	58	28	39	41	3	21
7	46	54	41	40	53	8	39
8	23	22	15	41	59	1	58
9	42	18	10	42	60	57	44
10	21	51	12	43	30	62	9
11	43	14	8	44	15	23	14
12	52	17	49	45	38	43	53
13	26	59	31	46	19	13	38
14	13	15	43	47	40	25	5
15	39	40	50	48	20	58	6
16	50	5	29	49	10	54	4
17	25	24	22	50	5	22	57
18	45	3	37	51	35	18	46
19	55	8	7	52	48	51	52
20	58	1	59	53	24	14	25
21	29	57	19	54	12	17	47
22	47	62	35	55	6	59	11
23	54	23	3	56	3	15	51
24	27	43	2	57	32	40	34
25	44	13	61	58	16	5	60
26	22	25	23	59	8	24	40
27	11	58	26	60	4	3	48
28	36	54	45	61	2	8	32
29	18	22	54	62	1	1	1
30	9	18	36				
31	37	51	56				
32	51	14	17				

TABLE 3-1: RESIDUE TABLE TBPSM

The powers are formed using two tables TBRPC and TBPRC, shown in Table 3-2. These are essentially logarithm and anti-logarithm tables for Galois field arithmetic. For example, S_1^2 is found by using S_1 as an index to TBRPC, doubling the value found in the table (modulo 63) and using the result as an index into TBPRC. The division is performed similarly.

The Chien search procedure (refer to Section 2.4) is used to determine the position of the errors. This procedure uses three additional tables, TBAL1, TBAL2, and TBAL3, shown in Table 3-3, to look up the products $\sigma_1\alpha$, $\sigma_2\alpha^2$, and $\sigma_3\alpha^3$ which replace σ_1 , σ_2 , and σ_3 . For each bit position, σ_1 , σ_2 , and σ_3 are exclusive or'ed together. If the result is unity, that bit position is in error and is complemented.

When transmission errors have been corrected, the background process must deserialize the bitstream into code words of various lengths. This deserialization procedure is performed in two parts. The first part deserializes the sideband parameters and makes them available to the Synthesis process. Figure 3-7 shows a map of the data frame and the bits allocated to each of the sideband parameters. The sideband code words are stored in buffer RQPBS, which is later read by the Synthesize process.

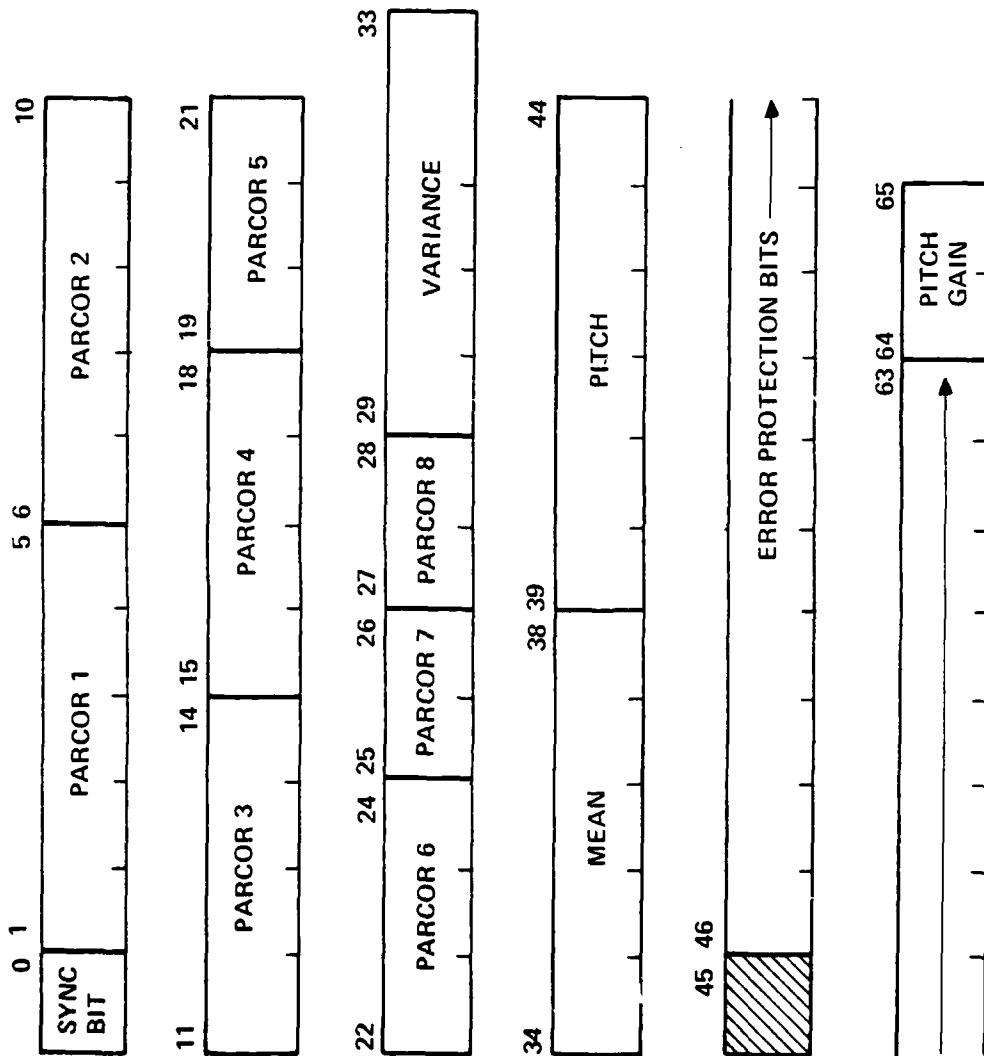
Before the second part of the deserialization procedure, deserialization of the Discrete Cosine Transform (DCT) coefficients can be executed, the word lengths of the DCT's must be obtained from the Synthesize process, which has determined these word lengths from the sideband parameters. To avoid delays and the consequent buffering, the transmitted data consists of the current frame of sideband information and the

TBRPC				TBPRC			
S	Index	S	Index	Index	S	Index	S
0	0	32	5	0	1	32	9
1	0	33	62	1	2	33	18
2	1	34	25	2	4	34	36
3	6	35	11	3	8	35	11
4	2	36	34	4	16	36	22
5	12	37	31	5	32	37	44
6	7	38	17	6	3	38	27
7	26	39	47	7	6	39	54
8	3	40	15	8	12	40	47
9	32	41	23	9	24	41	29
10	13	42	53	10	48	42	58
11	35	43	51	11	35	43	55
12	8	44	37	12	5	44	45
13	48	45	44	13	10	45	25
14	27	46	55	14	20	46	50
15	18	47	40	15	40	47	39
16	4	48	10	16	19	48	13
17	24	49	61	17	38	49	26
18	33	50	46	18	15	50	52
19	16	51	30	19	30	51	43
20	14	52	50	20	60	52	21
21	52	53	22	21	59	53	42
22	36	54	39	22	53	54	23
23	54	55	43	23	41	55	46
24	9	56	29	24	17	56	31
25	45	57	60	25	34	57	62
26	49	58	42	26	7	58	63
27	38	59	21	27	14	59	61
28	28	60	20	28	28	60	57
29	41	61	59	29	56	61	49
30	19	62	57	30	51	62	33
31	56			31	37		

TABLE 3-2: TBRPC AND TBPRC TABLES

σ	$\sigma_1\alpha$ TBAL1	$\sigma_2\alpha^2$ TBAL2	$\sigma_3\alpha^3$ TBAL3	σ	$\sigma_1\alpha$ TBAL1	$\sigma_2\alpha^2$ TBAL2	$\sigma_3\alpha^3$ TBAL3
0	0	0	0	32	3	6	12
1	2	4	8	33	1	2	4
2	4	8	16	34	7	14	28
3	6	12	24	35	5	10	20
4	8	16	32	36	11	22	44
5	10	20	40	37	9	18	36
6	12	24	48	38	15	30	60
7	14	28	56	39	13	26	52
8	16	32	3	40	19	38	15
9	18	36	11	41	17	34	7
10	20	40	19	42	23	46	31
11	22	44	27	43	21	42	23
12	24	48	35	44	27	54	47
13	26	52	43	45	25	50	39
14	28	56	51	46	31	62	63
15	30	60	59	47	39	58	55
16	32	3	6	48	35	5	10
17	34	7	14	49	33	1	2
18	36	11	22	50	39	13	26
19	38	15	30	51	37	9	18
20	40	19	38	52	43	21	42
21	42	23	46	53	41	17	34
22	44	27	54	54	47	29	58
23	46	31	62	55	45	25	50
24	48	35	5	56	51	37	9
25	50	39	13	57	49	33	1
26	52	43	21	58	55	45	25
27	54	47	29	59	53	41	17
28	56	51	37	60	59	53	41
29	58	55	45	61	57	49	33
30	60	59	53	62	63	61	57
31	62	63	61	63	61	57	49

TABLE 3-3: CHIEN SEARCH TABLES



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FIGURE 3-7: PARAMETER FRAME MAP

previous frame of DCT coefficients. Thus, the Synthesize process determines the DCT code word lengths for a given frame based on sideband information received in the previous frame. It passes this information to the Receive background program in buffer RIBITS and sets RG02 (integer scalar 115) to 1 to indicate that new data is present. The Receive background program waits for RG02 to be set before it proceeds with the DCT deserialization and clears RG02. The deserialized DCT coefficients are stored in buffer RQTDCT. The Receive background program then terminates. Figure 3-8 shows the organization of the complete Receive background program.

3.2.4 Digital Out Process

The Digital Out process serializes and protects the data to be transmitted and controls the digital output to the I/O scroll. The Digital Out process consists of three components: the I/O scroll program, the Transmit interrupt routine, and the Transmit background program. Figure 3-9 shows the components of the Digital Out process.

3.2.4.1 I/O Scroll Program

The transmitter function of the I/O scroll program is described in subsection 3.2.3.1. This program puts out the low order bit of each half-word in the double buffer pair SEN1 and SEN2 and generates an interrupt when either buffer is emptied.

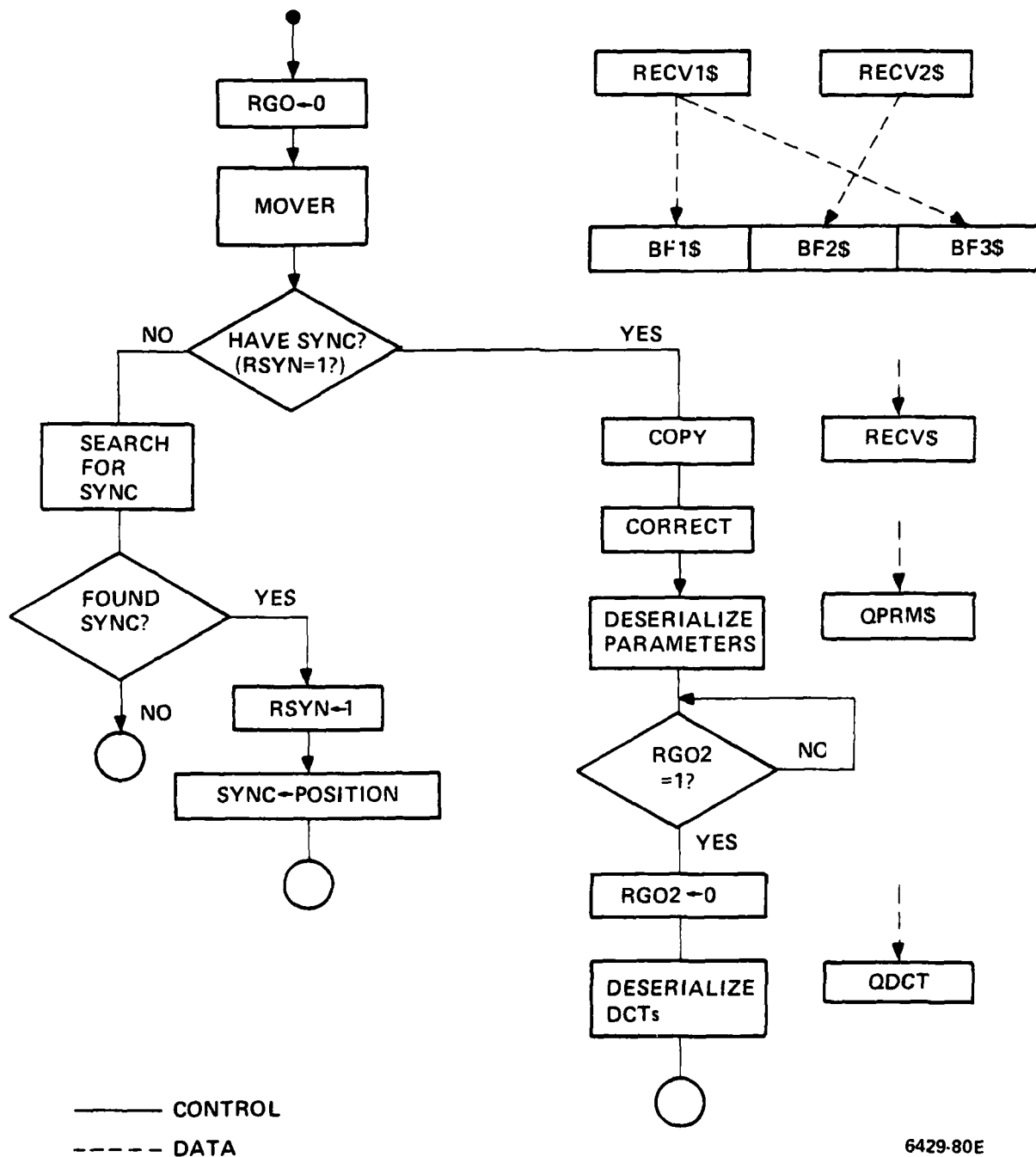
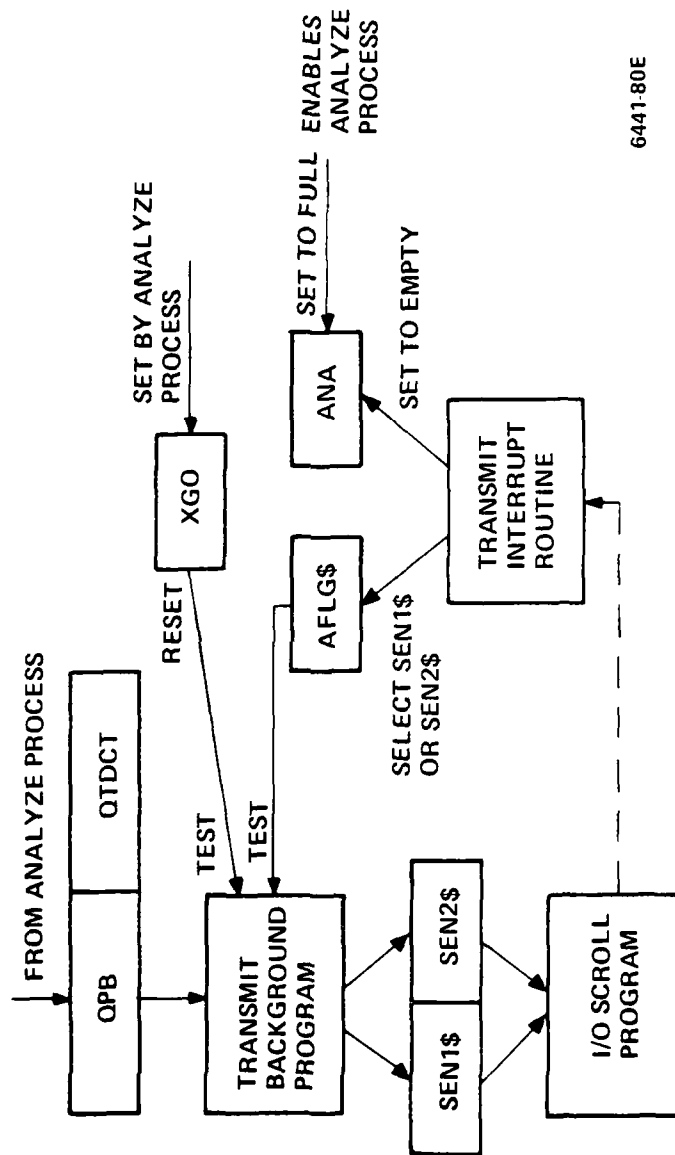


FIGURE 3-8: RECEIVE BACKGROUND PROCESS



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FIGURE 3-9: DIGITAL OUT PROCESS

3.2.4.2 Transmit Interrupt Routine

The Transmit interrupt routine runs in the CSPU at level 7. Its function is to keep track of which transmit buffer, SEN1 or SEN2, has been most recently emptied, and to transmit this information to the Transmit background program.

When an interrupt from the transmit portion of the I/O scroll program occurs, the Transmit interrupt routine clears ANAS (integer scalar 100) to indicate that a transmit buffer has become empty and clears or sets AFLG (integer scalar 101) to indicate that buffer SEN1 or SEN2, respectively, has been most recently emptied.

3.2.4.3 Transmit Background Program

The Transmit background program runs in the CSPU and is enabled by flag XG0 (integer scalar 115) being set. This flag is set by the Analysis process, which is in turn enabled by ANAS being clear. The Transmit background process sets ANAS to one on entry.

The Transmit background program first transforms the quantized sideband parameters from buffer QPBS and the quantized DCT coefficients from buffer QTDCTS into a bitstream which is built up in buffer SENS. The SENS buffer contains 369 half-words, the low order bits of which form the bitstream. The function of the higher order bits is described in Section 3.4.

The serialization procedure is performed in two parts. The first part serializes the sideband information according to the frame map in Figure 3-7. This part also skips 18 words in SENS to allow for later

insertion of protection bits. The second part of the serialization procedure serializes the DCT coefficients. These coefficients are those obtained from the previous frame. That is, the DCT coefficients are delayed in the Analysis process by one frame before being serialized, while the sideband parameters are those from the current frame and experience no additional delay. The quantized DCT coefficients are to be transmitted as code words of variable lengths. The background program obtains the length for each coefficient from buffer IBITS. The DCT coefficients in buffer QTDCTS are ordered by word length. These word lengths are constrained to be monotonically decreasing, so that the word length of each DCT coefficient must be equal to or less than that of the previous coefficient.

When all parameters and coefficients have been serialized, the background program generates BCH error protection bits for the resulting bit stream. The protection bits are found using a table look-up procedure. Table 3-4 shows the protection table, TBENC. For each of the 45 bits that is set, the bit position is used as an index into this table. The 18-bit values found from the table are exclusive or'ed together to form the protection bits. Each of the 18-bit values occupies two half words, the 15 least significant bits in one half word and the remaining 3 bits in an adjacent half word.

When all error correction bits have been inserted into buffer SENS, the background program copies this buffer into either SEN1S or SEN2S, depending on AFLG being clear or set. The Transmit background program then terminates.

Bit Position	Value		Bit Position	Value	
	Low Order	High Order		Low Order	High Order
0	30764	7	23	21534	7
1	17466	4	24	21027	4
2	8733	2	25	10513	6
3	4336	5	26	5256	7
4	28843	5	27	29288	4
5	16505	1	28	14644	2
6	22544	3	29	7322	1
7	21540	6	30	30305	7
8	10770	3	31	17180	0
9	27941	6	32	8590	0
10	13970	7	33	4295	0
11	25445	4	34	2147	4
12	12722	6	35	1073	6
13	6361	3	36	536	7
14	29760	2	37	31008	4
15	14880	1	38	15504	2
16	25916	7	39	7752	1
17	19122	4	40	30472	7
18	9561	2	41	17320	4
19	4780	5	42	8660	2
20	29050	5	43	4330	1
21	16529	5	44	28761	7
22	22628	1			

TABLE 3-4: ERROR PROTECTION ENCODING TABLE TBENC

3.2.5 Analyze Process

The Analyze process performs the Adaptive Transform Coding algorithm on the digital speech waveforms supplied to it. The result of each instance of this process is a frame of quantized and coded sideband parameters and Discrete Cosine Transform coefficients.

The Analyze process is composed of six functional procedures, each of which consists of one or more array functions. The entire Analyze process runs in the Arithmetic Processor portion of the MAP. The six functional procedures are: Cycle Delay Lines, Compute DCT Coefficients, Compute and Quantize Sideband Parameters, Determine Basis Spectrum, Bit Allocation, and DCT Coefficient Quantization. The relationships of these procedures are shown in Figure 3-10.

3.2.5.1 Cycle Delay Lines Procedure (FCB 237, PBFCB 172)

The Cycle Delay Lines procedure partially implements the buffering between processes required by the ATC system. It consists of the single array function VMOV1, as shown in Figure 3-11. The buffer delays implemented in VMOV1 are shown in Figure 3-12.

VMOV1 first moves the quantized parameters (QPRM) computed in the previous instance of the Analyze process to the buffer (AQPB) used as input by the Transmit background program. Then, it cycles two buffer delay lines, one for the DCT coefficients (QTDCT1) and one for the corresponding bit allocations (MIBIT4). The outputs of these delay lines are moved to input buffers (AQTDCT and AIBIT4) used by the Transmit background program. The delay of one frame in the DCT coefficients and bit assignments is required for the proper operation of the Synthesize process. VMOV1 also sets flag XG0 (integer scalar 115) which enables the Transmit background program to run.

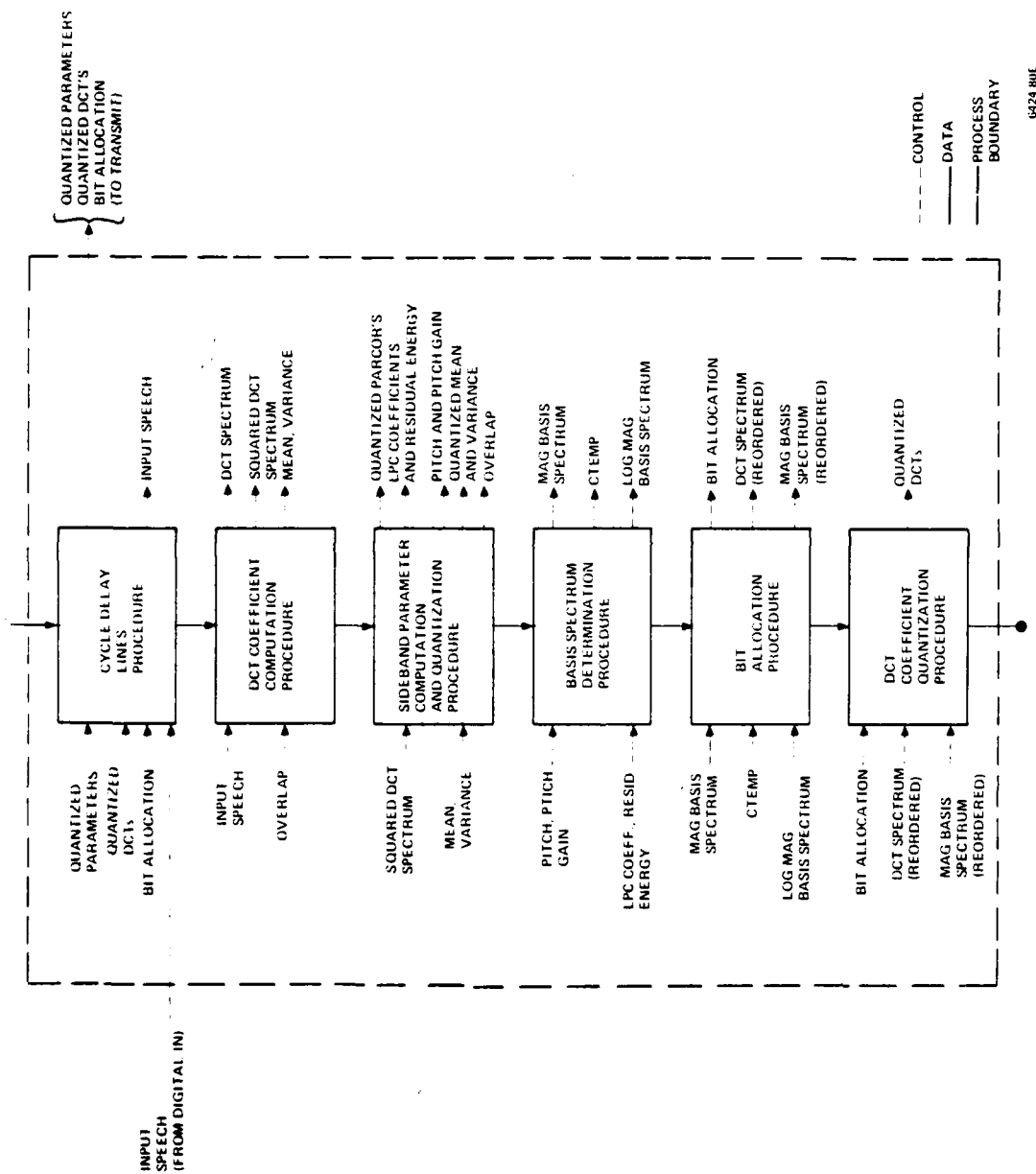


FIGURE 3-10: ANALYZE PROCESS

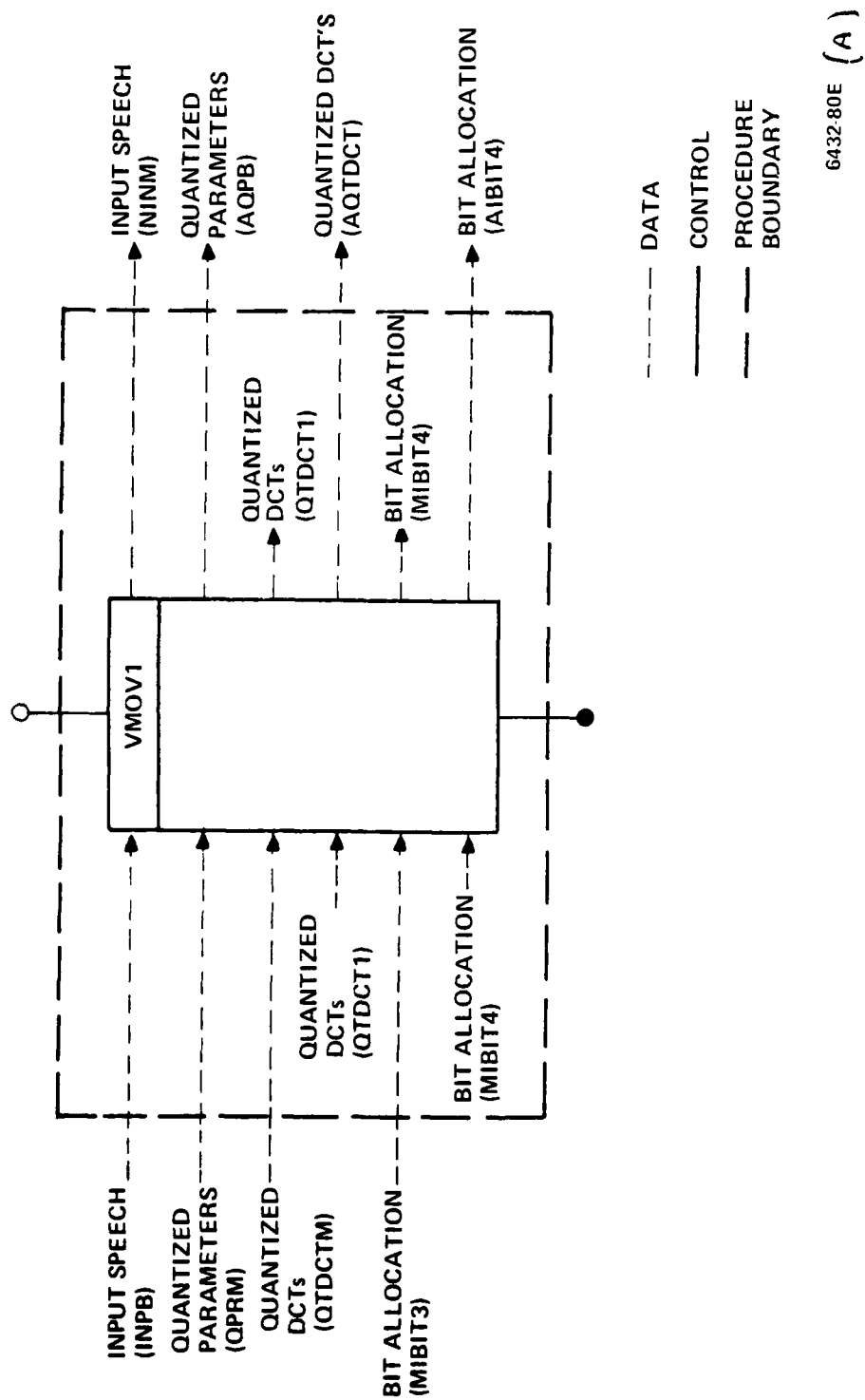
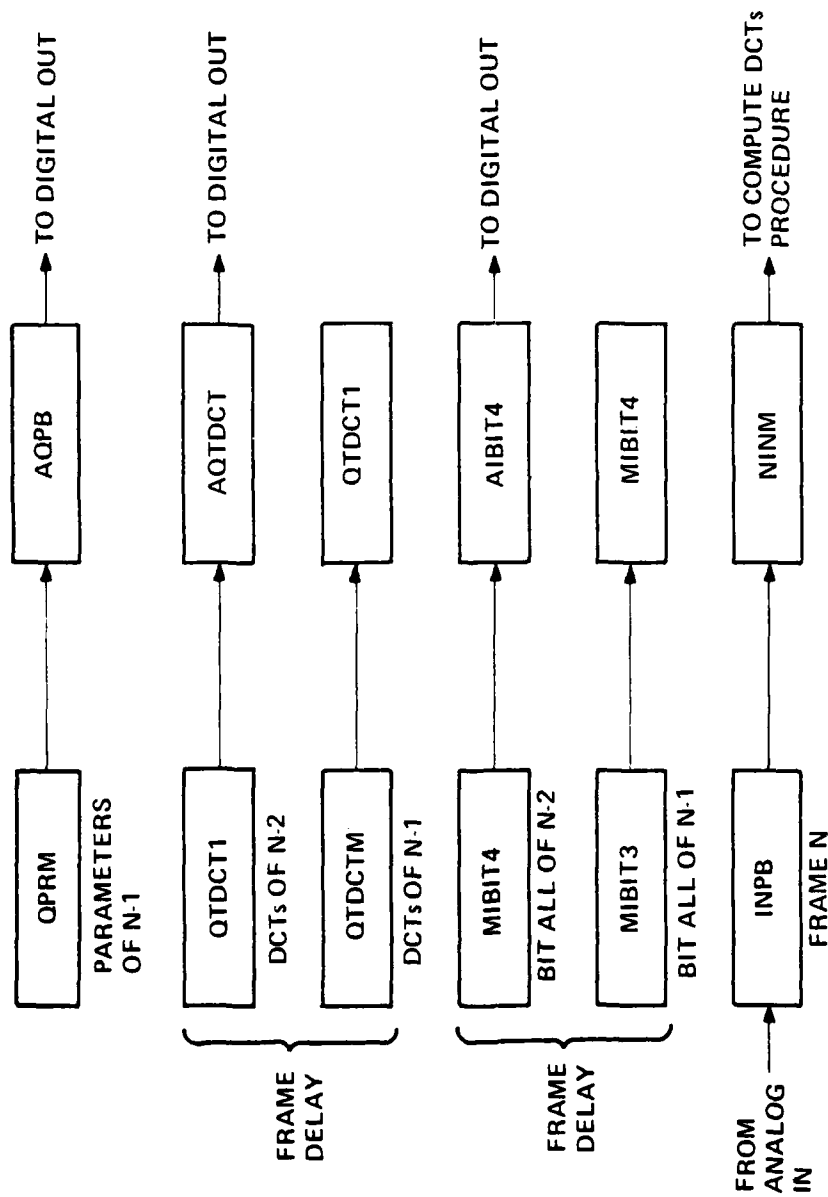


FIGURE 3-11: CYCLE DELAY LINES (ANALYZE)



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FIGURE 3-12: VMOV1 OPERATION FOR FRAME N

3.2.5.2 Compute DCT Coefficients Procedure

The Compute DCT Coefficients procedure is used to find the Discrete Cosine Transform representation of the current input frame. This procedure performs this transformation by using the appropriate pre-and post-processing around a Fast Fourier Transform. In addition, it performs the reformatting of the input data and the input frame overlapping.

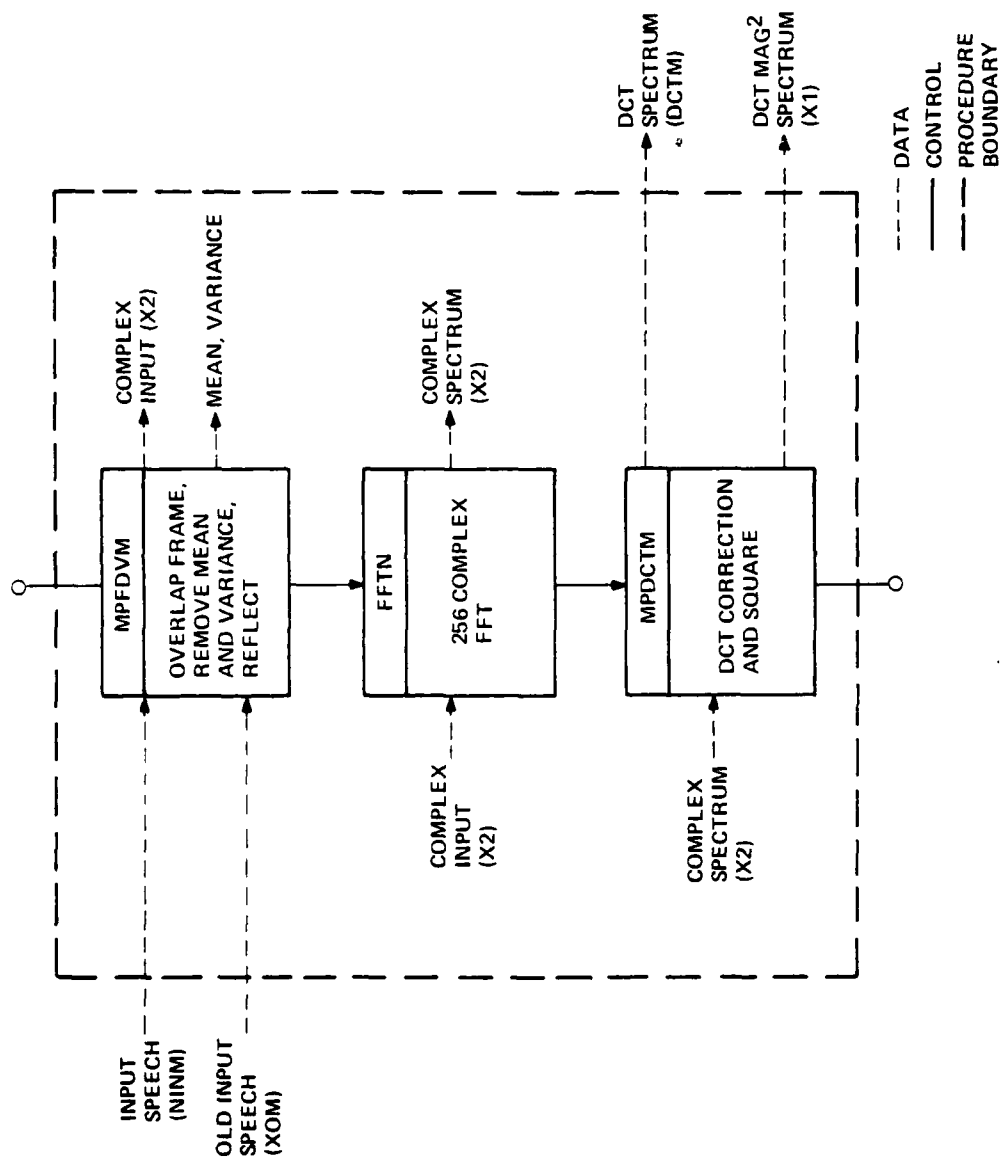
This procedure consists of three array functions: MPFDVM, FFTN (supplied by CSPI), and MPDCTM. The relationships between these array functions are shown in Figure 3-13.

3.2.5.2.1 MPFDVM (FCB240, PBFCB 153)

MPFDVM converts the data in NINM from fixed point to floating point, concatenates these 246 samples with the last 10 input samples of the previous frame, computes and removes the mean and variance of the frame of 256 samples, and reorders the frame in preparation for the following FFT. The reordering algorithm, given by Makhoul¹, consists of generating a 256 point complex buffer, of which the imaginary points are all zero, the first half of the real points are the even numbered points of the original, and the second half are the reversed odd-numbered points of the original buffer. This array function stores the variance (in dB) in scalar 55 and stores the mean divided by the variance in scalar 52. The re-ordered input is stored in buffer X2.

3.2.5.2.2 FFTN (FCB 204, PBFCB 168)

The FFTN array function is supplied by CSPI. As used in the Compute DCT Coefficients procedure, it performs a 256 complex-to-complex FFT not in place. To do so, it makes use of a cosine table (VSHRT) generated during initialization. The output is stored in buffer X2.



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FIGURE 3-13: COMPUTE DCT COEFFICIENTS PROCEDURE

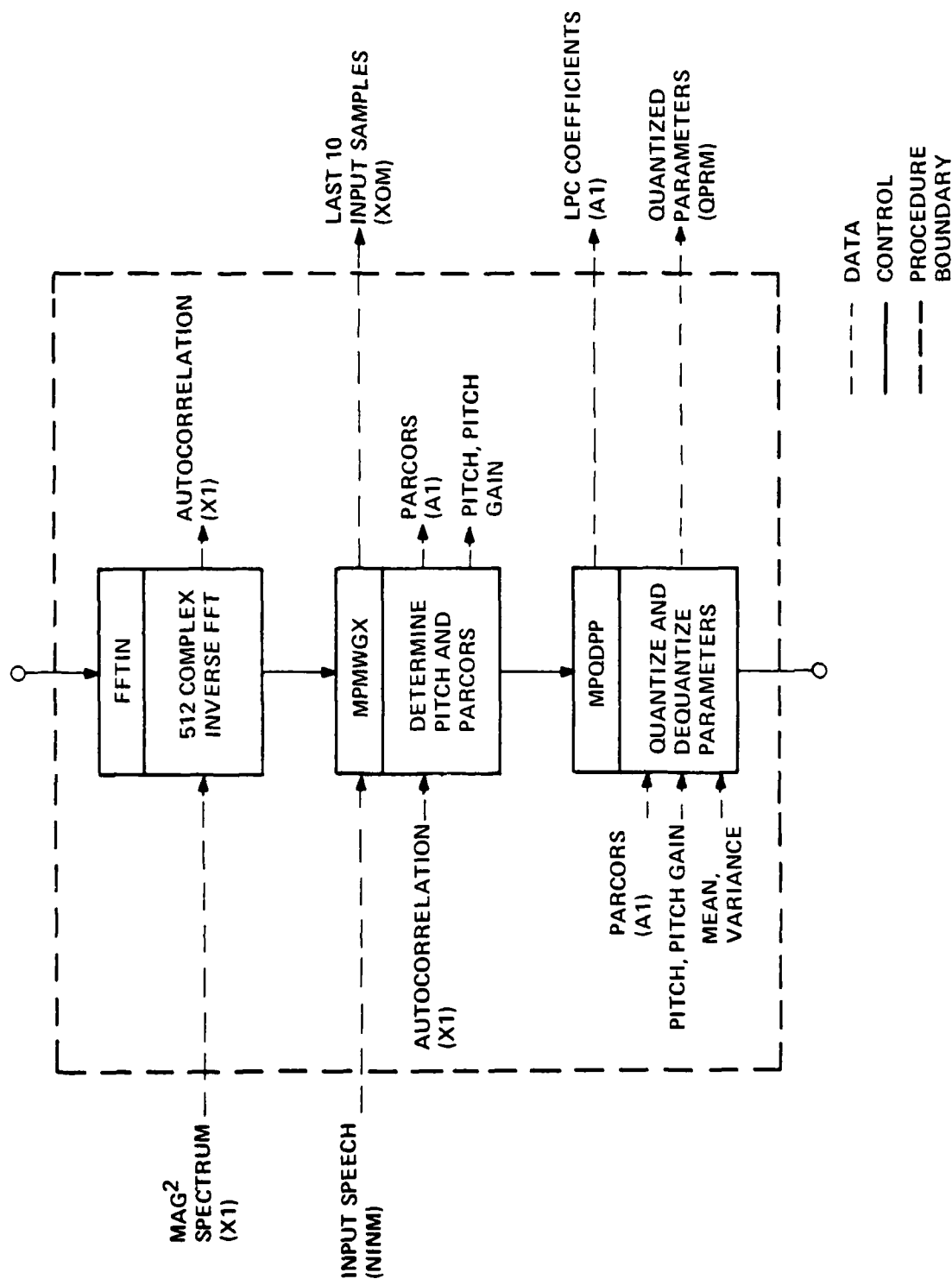
3.2.5.2.3 MPDCTM (FCB 241, PBFCB 154)

MPDCTM forms the 256 unique, real Discrete Cosine Transform coefficients from the 256 complex frequency samples generated by FFTN. It also forms the squared magnitudes of the DCT coefficients. These will be used later by the Compute and Quantize Sideband Parameters procedure for the calculation of an autocorrelation function.

The transformation from Complex Fourier coefficients is given by Makhoul⁹. Since the Fourier coefficients result from the FFT of a real sequence, they are conjugate symmetric and only the first half (128) of them need be used. These 128 Fourier coefficients are multiplied by one eighth of a cycle of a complex exponential. The real parts of the result form the first 128 DCT coefficients and the imaginary parts reversed form the negative of the remaining 128 DCT's. These 256 DCT coefficients are stored in buffer DCTM. The squares of the DCT's are also formed and are used to generate the real parts of 256 complex samples, which are used later. The imaginary parts of these 256 samples are zeroed. These 256 complex samples are then reflected to form a 512 point conjugate symmetric spectrum. The magnitude-squared spectrum is stored in buffer X2.

3.2.5.3 Compute and Quantize Sideband Parameters Procedure

This procedure consists of three array functions, as shown in Figure 3-14: FFTIN, MPMWGX, and MPQDPP. Its function is to compute the LPC spectrum and the pitch which are later used to form the bit allocation basis spectrum.



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FIGURE 3-14: COMPUTE AND QUANTIZE SID BAND PARAMETERS PROCEDURE

3.2.5.3.1 FFTIN (FCB 206, PBFCB 169)

FFTIN is a CSPI supplied array function that performs an inverse Fast Fourier Transform, not in place. As used in Sideband Parameter procedure, it performs a 512 point complex transformation. Since the input to this function is a symmetric magnitude spectrum, the result is an autocorrelation function consisting of 256 unique real points.

3.2.5.3.2 MPMWGX (FCB 244, PBFCB 155)

The MPMWGX function determines 8 parcor coefficients and an estimate of the pitch and pitch gain from the autocorrelation input. In addition, it also copies the last 10 samples from the current input buffer to an area in the front of the input buffer. These samples will form the first 10 samples of the next frame.

The parcor coefficients are found using the Weiner-Levinson-Durbin algorithm as implemented by CSPI in the MWLD array function.

The pitch is estimated by finding the maximum autocorrelation value that lies between sample indices 15 and 94. The position of the maximum is used as the pitch period M, and the ratio of the maximum value to the zeroth autocorrelation is used as the pitch gain. The pitch is stored as a floating point number in scalar 61, and the pitch gain is stored in scalar 62.

3.2.5.3.3 MPQDPP (FCB 243, PBFCB 156)

This array function quantizes the 8 parcors determined earlier, as well as the mean, variance, pitch, and pitch gain. It also computes the associated dequantized values, in order that the basis spectrum generated from them later will be the same as that found by the receiver, assuming

no uncorrectable channel errors. The quantization/dequantization tables used are shown in Table 3-5.

Once the parcors have been quantized and dequantized, they are used to find the corresponding Linear Prediction (LPC) coefficients. The following recursion is used to find the first through eighth LPC coefficients:

$$A_m(m) = -P(m)$$

$$A_m(L) = A_{m-1}(L) - P_m * A_{m-1}(M-L) \quad \begin{matrix} 1 \leq m \leq 8 \\ 1 \leq L < m \end{matrix}$$

where $A_i(j)$ is the j th LPC coefficient during the i th iteration and $P(j)$ is the j th parcor coefficient. These LPC coefficients are used later in the Compute Basis Spectrum procedure. The residual energy of the LPC coefficients is also computed and stored as ENG in scalar 60.

The quantized, coded parameters are collected in buffer QPRM with the parcor coefficients first, followed by the mean, variance, pitch, and pitch gain.

3.2.5.4 Determine Basis Spectrum Procedure

This procedure, shown in Figure 3-15 computes the basis spectrum from the quantized pitch value and the LPC coefficients. It consists of three array functions: MPFSTV, FF2R, and MPBASP.

3.2.5.4.1 MPFSTV (FCB 245, PBFCB 157)

This array function forms two time-domain functions, one from the quantized pitch and one from the LPC coefficients, and stores them as real and imaginary parts of a complex buffer. The time domain function

Parameter	Threshold	Code	Dequantized Value	Parameter	Threshold	Code	Dequantized Value
PARCOR 1		31	1.997	PARCOR 2		31	0.087
	1.933	30	1.869		1.962	30	0.263
	1.789	29	1.703		1.926	29	0.369
	1.644	28	1.531		1.890	28	0.462
	1.541	27	1.502		1.867	27	0.556
	1.444	26	1.385		1.836	26	0.624
	1.343	25	1.300		10804	25	0.681
	1.240	24	1.181		1.768	24	0.736
	1.122	23	1.063		1.732	23	1.714
	1.019	22	0.975		1.695	22	1.676
	0.930	21	0.885		1.653	21	1.630
	0.843	20	0.802		1.602	20	1.574
	0.771	19	0.740		1.547	19	1.520
	0.696	18	0.652		1.492	18	1.464
	0.615	17	0.577		1.436	17	1.407
	0.543	16	0.509		1.373	16	1.338
	0.477	15	0.445		1.306	15	1.274
	0.418	14	0.392		1.238	14	1.203
	0.368	13	0.344		1.162	13	1.121
	0.324	12	0.304		1.085	12	1.048
	0.286	11	0.268		1.012	11	0.977
	0.251	10	0.235		0.942	10	0.907
	0.219	9	0.204		0.876	9	0.846
	0.190	8	0.176		0.819	8	0.792
	0.162	7	0.150		0.764	7	0.736
	0.137	6	0.124		0.709	6	0.681
	0.112	5	0.100		0.653	5	0.624
	0.090	4	0.080		0.590	4	0.556
	0.070	3	0.060		0.509	3	0.462
	0.051	2	0.042		0.415	2	0.369
	0.033	1	0.025		0.316	1	0.263
	0.014	0	0.008		0.175	0	0.087

TABLE 3-5: PARAMETER QUANTIZATION AND DEQUANTIZATION TABLE

Parameter	Threshold	Code	Dequantized Value	Parameter	Threshold	Code	Dequantized Value
PARCOR 3	1.750	15	1.844	PARCOR 4	1.882	15	1.950
	1.585	14	1.655		1.754	14	1.183
	1.462	13	1.516		1.642	13	1.695
	1.358	12	1.408		1.539	12	1.589
	1.261	11	1.309		1.441	11	1.489
	1.168	10	1.213		1.346	10	1.393
	1.080	9	1.123		1.255	9	1.300
	0.995	8	1.037		1.165	8	1.209
	0.911	7	0.953		1.076	7	1.121
	0.829	6	0.870		0.988	6	1.032
	0.745	5	0.787		0.897	5	0.943
	0.658	4	0.703		0.801	4	0.851
	0.562	3	0.613		0.696	3	0.752
	0.452	2	0.512		0.572	2	0.640
	0.315	1	0.392		0.409	1	0.504
		0	0.238			0	0.314

TABLE 3-5: PARAMETER QUANTIZATION AND DEQUANTIZATION TABLE (CONT'D)

Parameter	Threshold	Code	Dequantized Value
PARCOR 5		7	1.591
	1.482	6	1.374
	1.297	5	1.219
	1.153	4	1.086
	1.022	3	0.958
	0.891	2	0.825
	0.747	1	0.669
	0.557	0	0.445

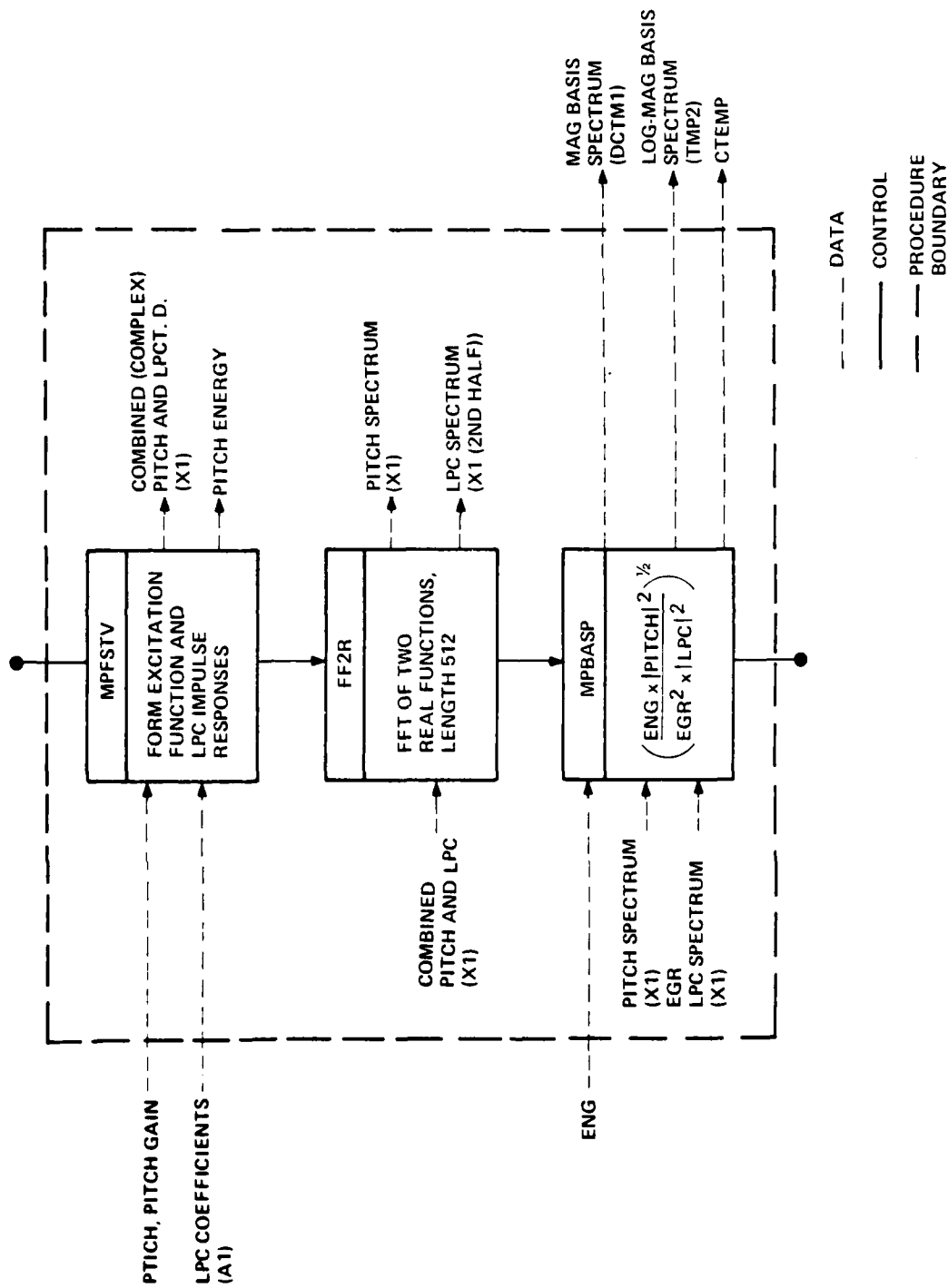
Parameter	Threshold	Code	Dequantized Value
PARCOR 6		7	1.621
	1.509	6	1.398
	1.319	5	1.240
	1.173	4	1.105
	1.041	3	0.976
	0.909.	2	0.842
	0.763	1	0.685
	0.573	0	0.462

PARCOR 7		3	1.320
	1.167	2	1.013
	0.875	1	0.736
	0.568	0	0.397

PARCOR 8		3	1.382
	1.243	2	1.105
	0.985	1	0.865
	0.722	0	0.879

Mean		15	3.132	Variance	31	60.709
	2.854	14	2.576		30	58.810
	2.394	13	2.212		29	57.223
	2.072	12	1.932		28	55.758
	1.816	11	1.699		27	54.385
	1.598	10	1.496		26	53.034
	1.403	9	1.314		25	51.633
	1.231	8	1.148		24	50.167
	1.070	7	0.992		23	48.695
		6	0.846		22	47.224
		5	0.706		21	45.741
		4	0.572		20	44.222
		3	0.441		19	42.664
		2	0.313		18	41.065
		1	0.187		17	39.343
		0	0.062		16	37.430
					15	35.525
Pitcngain	0.824	3	0.898		14	33.741
	0.667	2	0.750		13	32.035
	0.488	1	0.584		12	30.322
		0	0.392		11	28.495
					10	26.458
					9	24.372
					8	22.439
					7	20.412
					6	18.140
					5	15.849
					4	13.242
					3	10.868
					2	8.794
					1	6.802
					0	4.460

Pitch (M)	Code	Decoded Pitch	Pitch	Code	Decoded Pitch	Pitch	Code	Decoded Pitch
0	0	15	32	17	32	64	48	63
1	0	15	33	18	33	65	49	65
2	0	15	34	19	34	66	49	65
3	0	15	35	20	35	67	50	67
4	0	15	36	21	36	68	50	67
5	0	15	37	22	37	69	51	69
6	0	15	38	23	38	70	51	69
7	0	15	39	24	39	71	52	71
8	0	15	40	25	40	72	52	71
9	0	15	41	26	41	73	53	73
10	0	15	42	27	42	74	53	73
11	0	15	43	28	43	75	54	75
12	0	15	44	29	44	76	54	75
13	0	15	45	30	45	77	55	77
14	0	15	46	31	46	78	55	77
15	0	15	47	32	47	79	56	79
16	1	16	48	33	48	80	56	79
17	2	17	49	34	49	81	57	81
18	3	18	50	35	50	82	57	81
19	4	19	51	36	51	38	58	83
20	5	20	52	37	52	48	58	83
21	6	21	53	38	53	85	59	85
22	7	22	54	39	54	86	59	85
23	8	23	55	40	55	87	60	87
24	9	24	56	41	56	88	60	87
25	10	25	57	42	57	89	61	89
26	11	26	58	43	58	90	16	89
27	12	27	59	44	59	91	62	91
28	13	28	60	45	60	92	62	91
29	14	29	61	46	61	93	63	93
30	15	30	62	47	62	94	63	93
31	16	31	63	48	63			



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FIGURE 3-15: BASIS SPECTRUM DETERMINATION PROCEDURE

formed from the pitch represents the excitation function. It is an exponentially decaying pulse train with a period equal to the pitch and contains zeroes except at multiples of the pitch period. The values at the pulse points are the positive powers of the pitch gain. That is:

$$XR(K*M) = PG^{K+1} \quad \text{for } 0 \leq K*M < 256$$

where M is the pitch period, PG the pitch gain, and XR is the output. The excitation is stored in the real part of the 512 point complex output buffer. The sum of the pitch pulses is accumulated and stored in scalar 73.

The other time domain function is the vocal tract filter function. It is formed from the impulse response of the LPC filter, which is simply 1, -A(1), -A(2), ... -A(8). The remaining 503 imaginary values are zeroed.

The two time domain functions, though both real, are stored in a complex buffer to facilitate the use of an efficient FFT algorithm, FF2R.

3.2.5.4.2 FF2R (FCB 214, PBFCB 170)

FF2R is a CSPI supplied array function used to transform two real signals simultaneously. One input function is stored in the real part of a complex buffer, and its transform appears in the first half of the complex buffer output. Similarly, the second function is stored in the imaginary parts of the input buffer, and its transform appears in the second half of the output buffer. As used in the ATC system, FF2R uses 512 point complex buffers as input and output. The output buffer is divided in half into two 256 point complex buffers. The first contains

the frequency spectrum of the pitch excitation function. The second contains the frequency response of the vocal tract inverse filter.

3.2.5.4.3 MPBASP (FCB 253, PBFCB 158)

MPBASP forms the magnitude basis spectrum from the frequency spectrum and the inverse filter response. It also forms the base 2 logarithm of this basis spectrum to be used later in bit allocation.

This routine first calculates the magnitude squared of the basis spectrum by the following formula:

$$|BASIS|^2 = \frac{ENG * |Pitch|^2}{EGR^2 * |Inverse Filter|^2}$$

where ENG is the residual energy of the LPC spectrum as calculated in MPQDPP and stored in scalar 60, and EGR is the sum of the excitation pulses as determined by MPFSTV and stored in scalar 73. Then one half the base 2 logarithm of the result is calculated. This is equivalent to the base 2 log-magnitude basis spectrum. For reasons of efficiency, the single MAP instructions LGS and RCP, which approximate the base 16 log and the reciprocal, are used. The DC component of the log magnitude basis spectrum is forced to be very small.

The magnitude basis spectrum is found as follows:

$$|BASIS| = 2^{\left(\frac{\log |BASIS|^2}{2} \right)}$$

This exponentiation is performed using the same technique as the CSPI supplied array function VEXP. This approach was found to be faster than finding the square root directly.

The log base 2 magnitude basis spectrum output is stored in buffer TMP2 (256 real points). The magnitude basis spectrum output is stored in buffer DCTM1 (also 256 real points). Also, scalar 73, EGR, is modified to become $1.0/EGR$. Finally, the sum of the log magnitudes is subtracted from the number of bits to be allocated, the difference is divided by 256, and the resultant average bit allocation is stored in CTEMP, scalar 80.

3.2.5.5 Bit Allocation Procedure

The bit allocation procedure determines how many bits are to be used to code each DCT coefficient for transmission under four constraints. The four constraints are: 1) each DCT must be coded with an integral number of bits; 2) that the sum of the bits allocated to the DCT coefficients must equal 267; 3) that the maximum number of bits allocated to any DCT coefficient is three; and 4) that the numbers of bits allocated to the DCT coefficients approximate the log-magnitude basis spectrum.

Satisfying these constraints exactly is a difficult, time consuming task. An approximate solution is found by reordering the log magnitude basis spectrum coefficients (and the corresponding DCT coefficients and magnitude spectrum coefficients) by size. The bit allocation for the reordered DCT coefficients will then be monotonically decreasing. Monotonically decreasing bit allocations are useful for horizontal encoding and protection.

The bit allocation procedure consists of three array functions, as shown in Figure 3-16. They are MPASRT, which performs the reordering, MPSCAN, which determines the preliminary bit allocation, and MPCDBA, which performs a correction on the preliminary bit allocation to produce a final bit allocation.

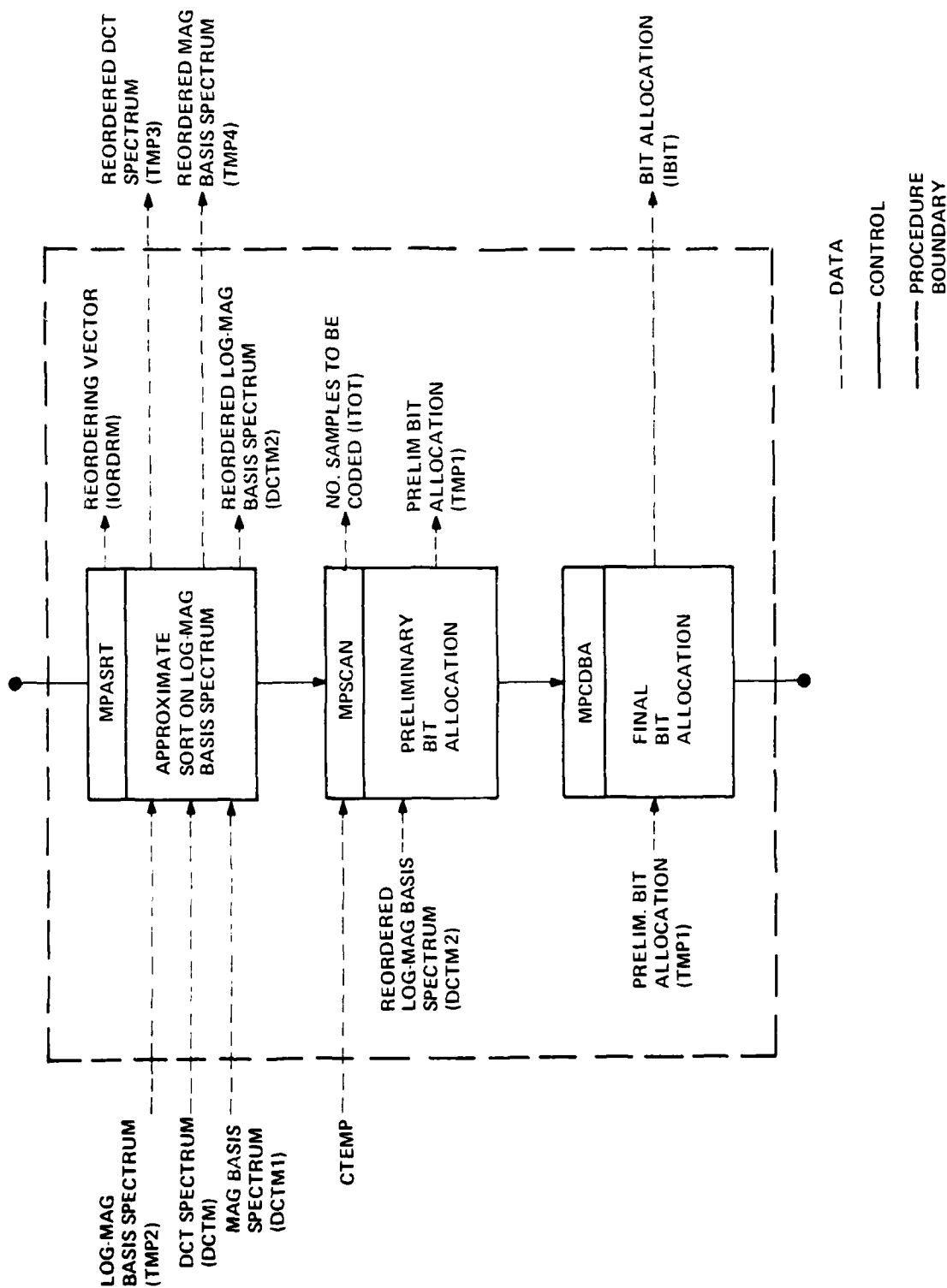


FIGURE 3-16: BIT ALLOCATION PROCEDURE

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3.2.5.5.1 MPASRT (FCB 249, PBFCB 159)

This array function performs an approximate sort on the log-magnitude basis spectrum coefficients in buffer TMP2. It determines a vector of indices expressing the sorted order, and reorders the DCT coefficients (in buffer DCTM) and the magnitude basis spectrum coefficients (in DCTM1) accordingly.

The sort is performed by assigning each log-magnitude basis spectrum coefficient to one of four bins, depending on size. The index, the position in the buffer of each coefficient, is added to an ordering list corresponding to the bin. The thresholds for the four bins are shown in Table 3-6. When all indices have been added to one of the four lists, the lists are concatenated to form a complete ordering vector in buffer IORDR.

Once IORDR has been obtained, the three input buffers DCTM, (DCT coefficients), DCTM1 (magnitude basis spectrum), and TMP2 (log-magnitude basis spectrum) are reordered by using IORDR as indirect addresses into each of the input buffers and forming sequential outputs. That is,

$$\text{OUTPUT}(I) = \text{INPUT}(\text{IORDRM}(I))$$

The three output buffers are TMP3, TMP4, and DCTM2, respectively.

The required indirect addressing is achieved by performing a "scatter write" into APS program memory to modify the addresses used in the input instructions.

3.2.5.5.2 MPSCAN (FCB 254, PBFCB 160)

This array function proceeds in two passes to form a preliminary bit allocation. The first calculates a new log magnitude spectrum whose

Bin #	Threshold
3	2.907
2	1.322
1	-0.488
0	

TABLE 3-6: BIT ALLOCATION THRESHOLDS

mean is equal to the average number of bits per sample to be allocated. It also determines the last sample of this new spectrum to be coded. That is, the samples which follow will be allocated zero bits. It also keeps a running sum of the samples of the new spectrum to be coded.

The second pass generates another log magnitude spectrum whose mean is the average number of bits available per sample to be coded and whose trailing samples are zero. The coefficients of this spectrum are then rounded and fixed to form the preliminary bit allocation.

Inputs to MPSCAN are buffer DCTM2, the log-magnitude basis spectrum, and CTEMP, the average bit offset to be applied in pass 1, computed in MPBASP and stored in scalar 80. The output is stored as floating point numbers in buffer TMP1. In addition, ITOT, the number of samples to be coded, is stored in scalar 83.

3.2.5.5.3 MPCDBA (FCB 255, PBFCB 161)

This array function consists of at least four passes. The first pass forces the preliminary bit allocations to be monotonically decreasing and sums them. If this sum ever exceeds the available number of bits, BTLTH = 267, the remaining bit allocations in the input buffer are zeroed. The second pass clips the bit allocations at 3, and updates the total bits allocated sum accordingly. Pass 3 allocates the leftover bits, beginning at the front of the buffer, but never raising the bit allocation of any coefficient over 3.0. If some bits remain to be allocated at the end of this pass, the pass is repeated. Finally, the bit allocations are changed from floating point integers to fixed point numbers and stored.

The input to MPCDBA is buffer TMP1, the preliminary bit assignment. The output is stored in IBIT.

3.2.5.6 DCT Coefficient Quantization Procedure (FCB 250, RBFCB 162)

This procedure, shown in Figure 3-17, consists of one array function, MPFSTQ. From the bit allocations in IBIT, this array function determines the number of quantization levels and the associated thresholds for each (reordered) DCT coefficient. The thresholds are multiplied by the corresponding magnitude basis spectrum value and then used to quantize and code the DCT coefficients into code words of various lengths. The thresholds, quantization levels, and resultant code words are shown in Table 3-7.

The inputs to MPFSTQ are IBIT; TMP3, the reordered DCT coefficients; and TMP4, the reordered magnitude basis spectrum. The output of MPFSTQ is stored as fixed point numbers in buffer QTDCTM.

3.2.6 Synthesize Process

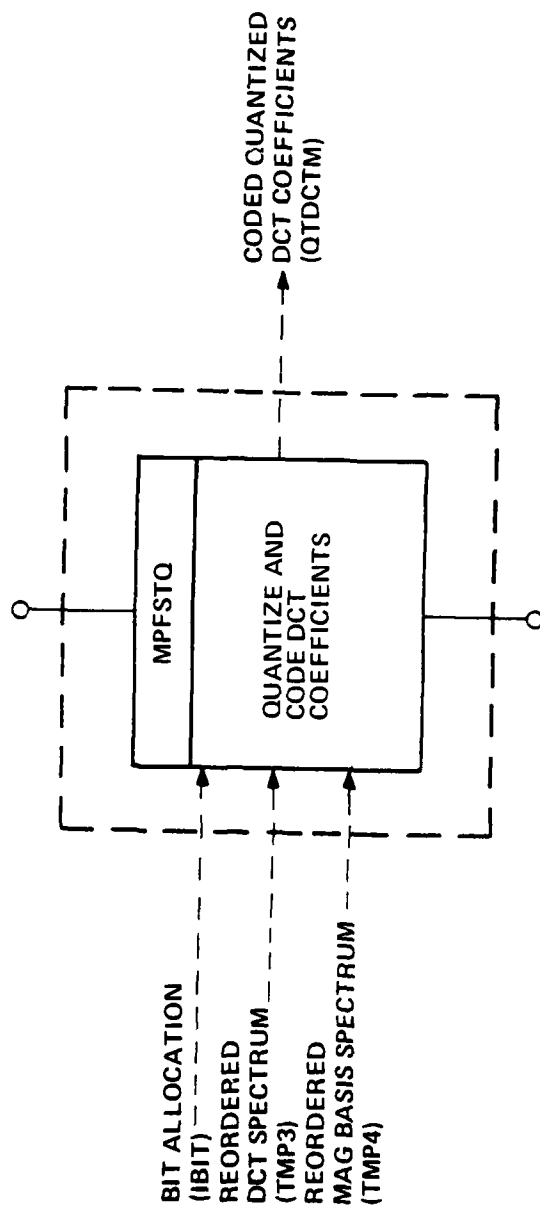
The Synthesize process performs the Adaptive Transform Decoding algorithm to obtain digital speech waveforms. The input to this process is coded, quantized sideband parameters and coded, quantized DCT coefficients.

The Synthesize process is composed of seven functional procedures: Cycle Delay Lines, Dequantize Sideband Parameters, Basis Spectrum Determination, Bit Allocation, Copy Temporary Buffers, DCT Coefficients Dequantization, and Inverse DCT Computation. The relationships between these procedures are shown in Figure 3-18. Each procedure consists of one or more array functions. The entire Synthesize process runs in the Arithmetic Processor portion of the MAP.

Code	3 Bit Threshold	2 Bit Threshold	1 Bit Threshold
3	2.3796	-	-
2	1.2327	-	-
1	0.5332	1.1269	-
0	0	0	0

Note: Negative values are quantized similarly,
 sign bit of code is set

TABLE 3-7: DCT COEFFICIENT QUANTIZATION



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FIGURE 3-17: DCT QUANTIZATION PROCEDURE

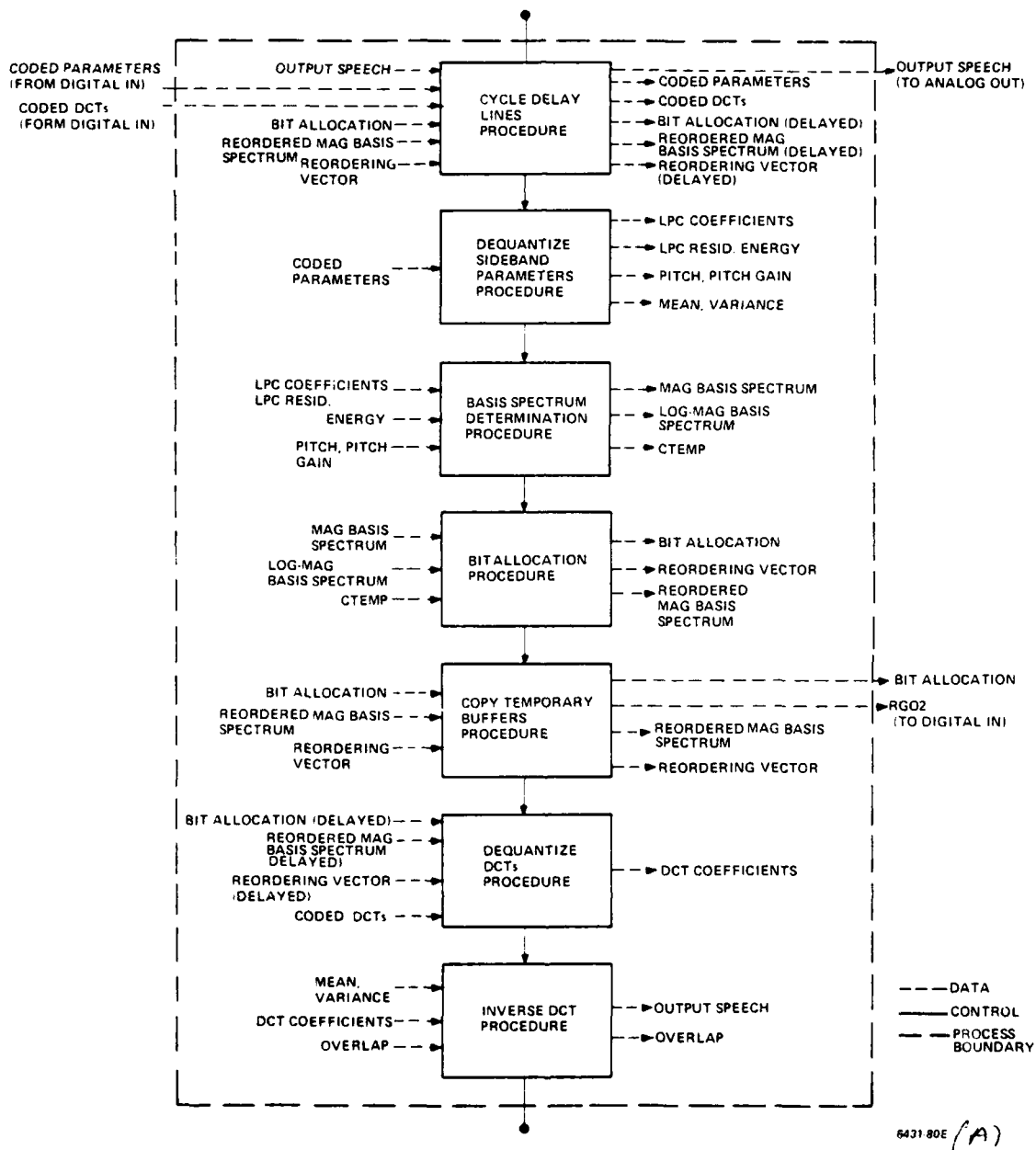


FIGURE 3-18: SYNTHESIZE PROCESS

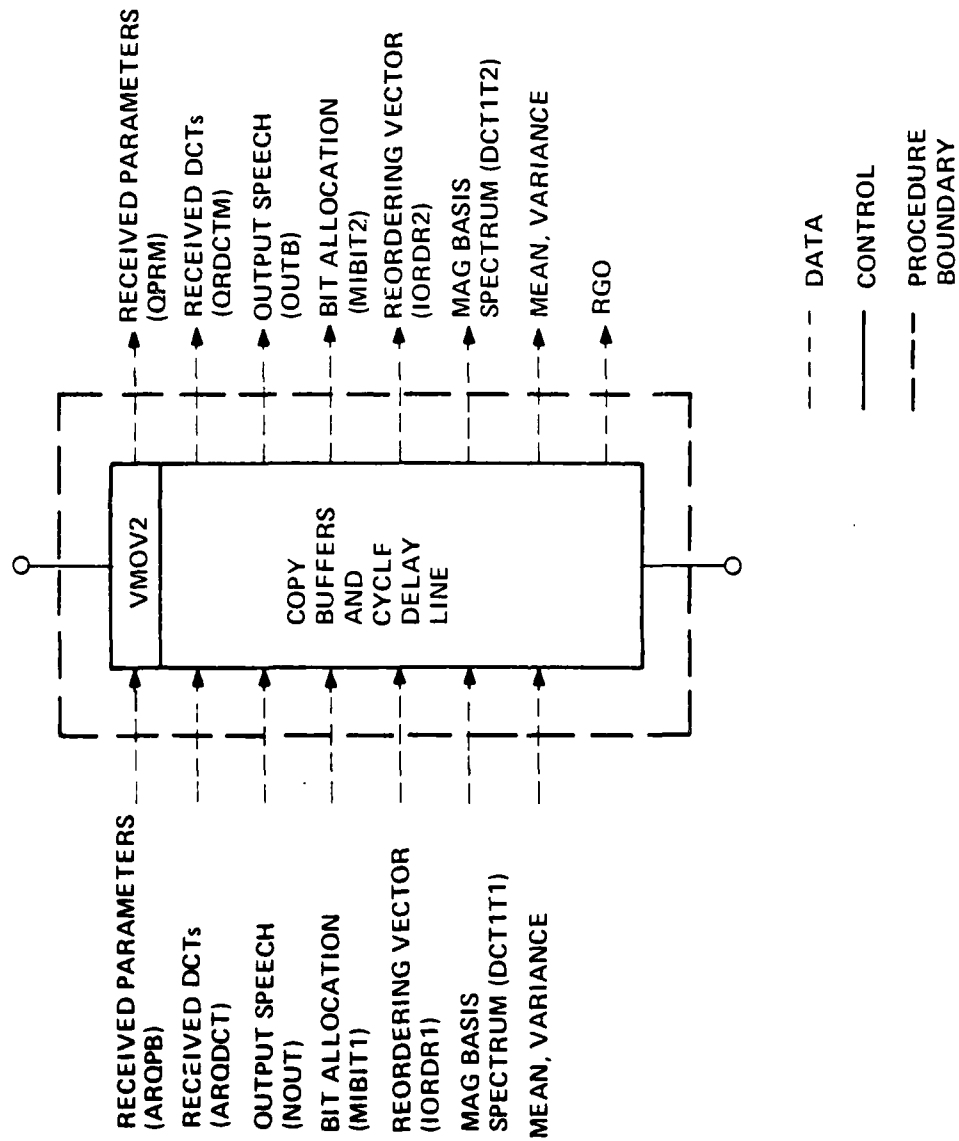
3.2.6.1 Cycle Delay Lines Procedure (FCB 238, PBFCB 173)

The Cycle Delay Lines procedure consists of a single array function, VMOV2. It provides data to the Analog Out process and accepts data from the Digital In process, enables the Receive background program, and provides a single frame delay. This procedure is shown in Figure 3-19.

Output speech data from the previous instance of the Synthesize process is stored in buffer NOUT. VMOV2 copies this data to buffer OUTB, which will be used by the Analog Out process. VMOV2 sets the flag OUTFUL (integer scalar 111) to indicate that new data is present, thus enabling the A/D background program.

Similarly, VMOV2 copies data from two buffers, ARQPB and ARQDCT. These buffers have been filled by the Receive background program and contain the de-serialized sideband parameters and DCT coefficients. VMOV2 copies these buffers into QPRM and QRDCM and sets flag RG0 to indicate that the input buffers are empty, enabling the Receive background program to execute and fill them again.

The input sideband parameters in QPRM do not represent the same frame of speech as the DCT coefficients in QRDCM. Because the bit allocation, which is derived from the sideband parameters, must be obtained before the DCT coefficients can be deserialized, the transmitted DCT coefficients lag the transmitted sideband parameters by one frame. This means that the bit allocation, basis spectrum, and reordering vector determined in one instance of the Synthesize process must be saved to be used in the next instance of the process. VMOV2 implements this delay, as shown in Figure 3-20. The bit allocation, basis spectrum, and reordering vector have been stored in buffers MIBIT1, DCT1T1, and IORDR1, by the



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FIGURE 3-19: CYCLE DELAY LINES PROCEDURE (SYNTHESIS/E)

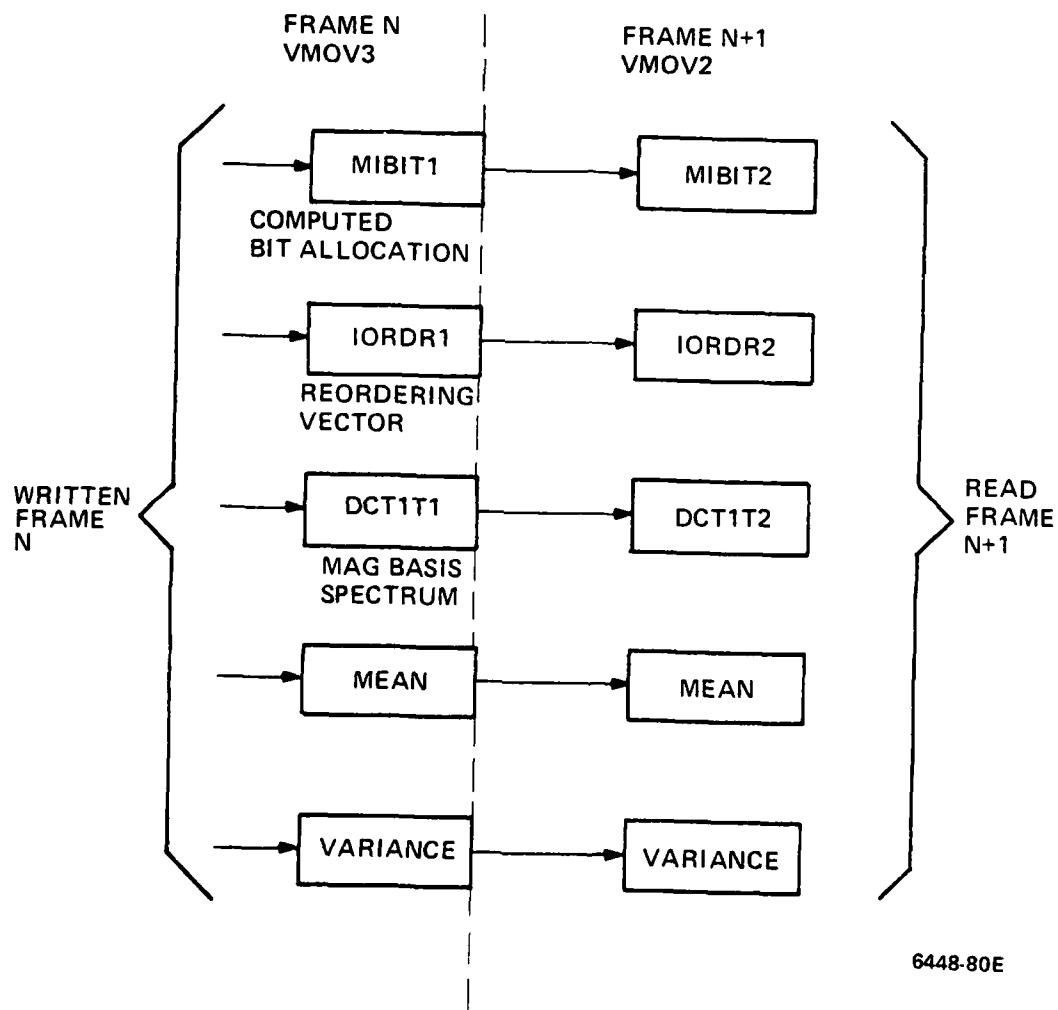


FIGURE 3-20: SYNTHESIZE DELAY LINES

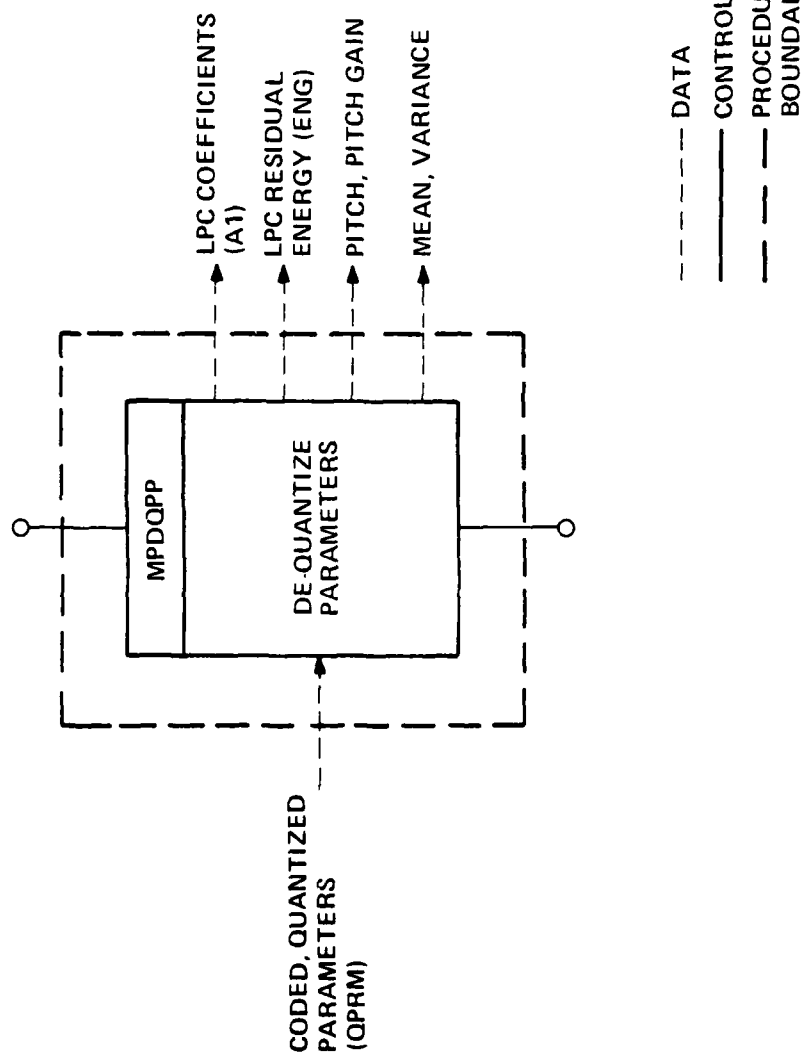
previous instance of the process, and are moved by VMOV2 to MIBIT2, DCT1T2, and IORDR2, from which they will be used in the DCT dequantization procedure. The mean and variance have been stored in scalars 102 and 103 and are moved to scalars 100 and 101 to implement a similar frame delay on these parameters.

3.2.6.2 Sideband Parameter Dequantization Procedure (FCB 244, PBFCB 163)

This procedure, shown in Figure 3-21, consists of a single array function, MPDQPP. MPDQPP decodes 12 sideband parameters: 8 parcor coefficients, pitch, pitch gain, mean, and variance using the same tables used by MPQDPP (subsection 3.2.5.3.3). The input parameters are fixed point integers and are contained in buffer QPRM. These integers are used as indices into the appropriate decoding tables. The dequantized parcor coefficients are transformed to LPC coefficients as in MPQDPP, which are then output to buffer A1. The residual energy of the LPC coefficients (ENG) is stored in scalar 60. The dequantized pitch is output to scalar 91, the pitch gain to scalar 90, the variance to scalar 89, and the mean to scalar 88.

3.2.6.3 Basis Spectrum Determination Procedure

This procedure is identical to the Basis Spectrum Determination performed in the Analyze process (subsection 3.2.5.4, Figure 3-15). This procedure uses MPFSTV, FF2R, and MPBASP to determine the log-magnitude basis spectrum, stored in TMP2, and the magnitude basis spectrum, stored in DCTM1. If there are no uncorrectable transmission errors, these spectra will be identical to those determined in the Analyze process.



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FIGURE 3-21: PARAMETER DEQUANTIZATION PROCEDURE

3.2.6.4 Bit Allocation Procedure

This procedure determines the number of bits used to code each of the DCT coefficients. It is very similar to the bit allocation procedure used in the Analyze process (subsection 3.2.5.5) except that the sorting function is slightly changed.

The bit allocation procedure in the Synthesize process also consists of three array functions: MPSSRT, MPSCAN, and MPCDBA. This procedure is shown in Figure 3-22.

3.2.6.4.1 MPSSRT (FCB 248, PBFCB 167)

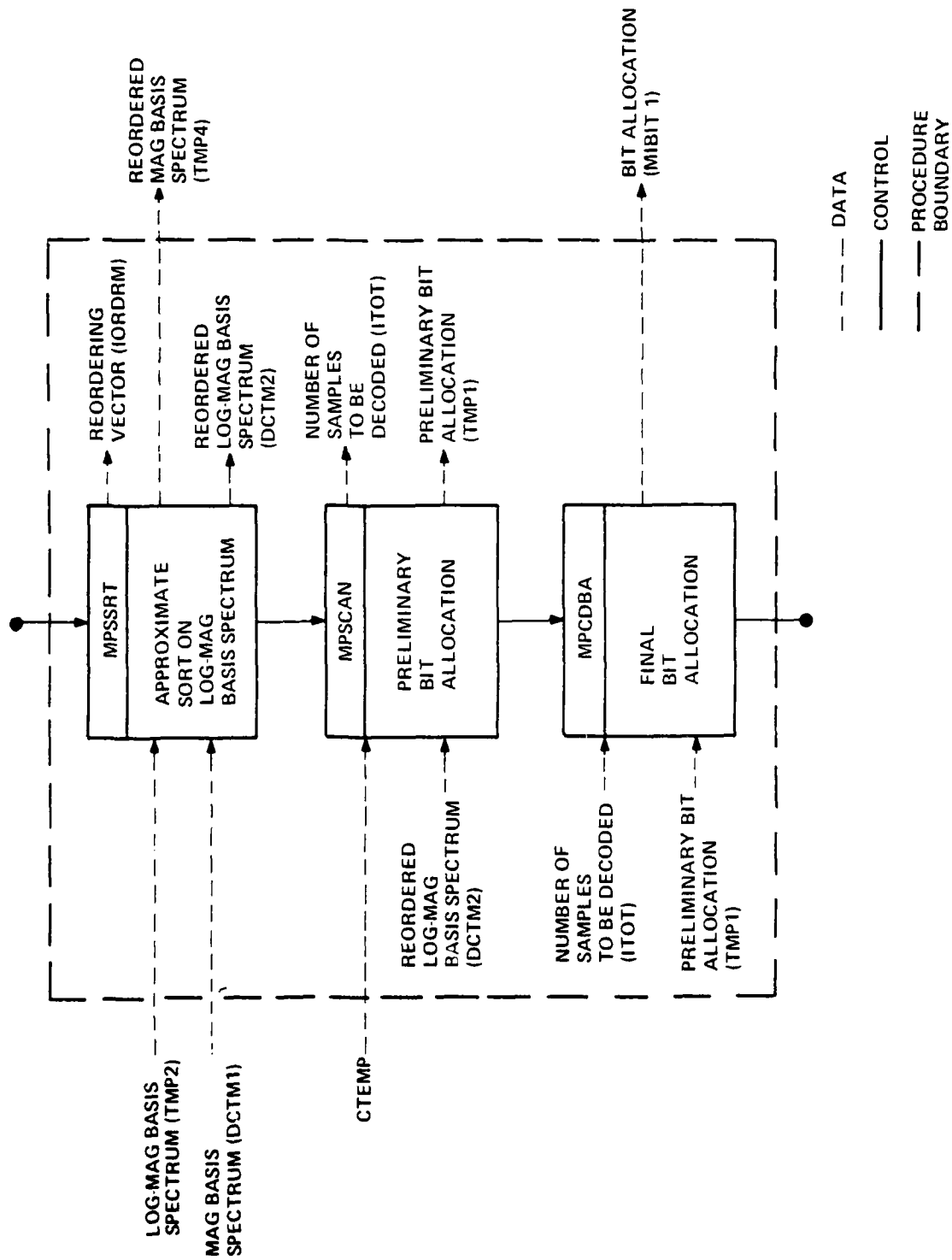
This array function is similar to MPASRT (subsection 3.2.5.5.1) in that it reorders the magnitude and log-magnitude basis spectra contained in buffers DCT1 and TMP2, respectively, and stores the reordering information in a vector of indices, IORDRM. However, MPSSRT performs no reordering on the (coded) DCT coefficients. The coded DCT coefficients are transmitted in sorted order. They will be reordered to normal order in the dequantization procedure (subsection 3.2.6.6) later.

3.2.6.4.2 MPSCAN (FCB 254, PBFCB 160)

This is the same array function used in the Analyze process and described in subsection 3.2.5.5.2. It uses the same input and output buffers and is invoked using the identical prebound FCB.

3.2.6.4.3 MPCDBA (FCB 255, PBFCB 175)

This is the same array function used in the Analyze process and described in subsection 3.2.5.5.3. It is the identical FCB, but pre-bound differently. As used in the Synthesize process, it is prebound FCB



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FIGURE 3-22: BIT ALLOCATION PROCEDURE (SYNTHESIS)

175. The only difference made by this alternate prebinding is that the output buffer, which stores the bit allocations, is MIBIT1 instead of MIBIT2, as is used in the Analyze process.

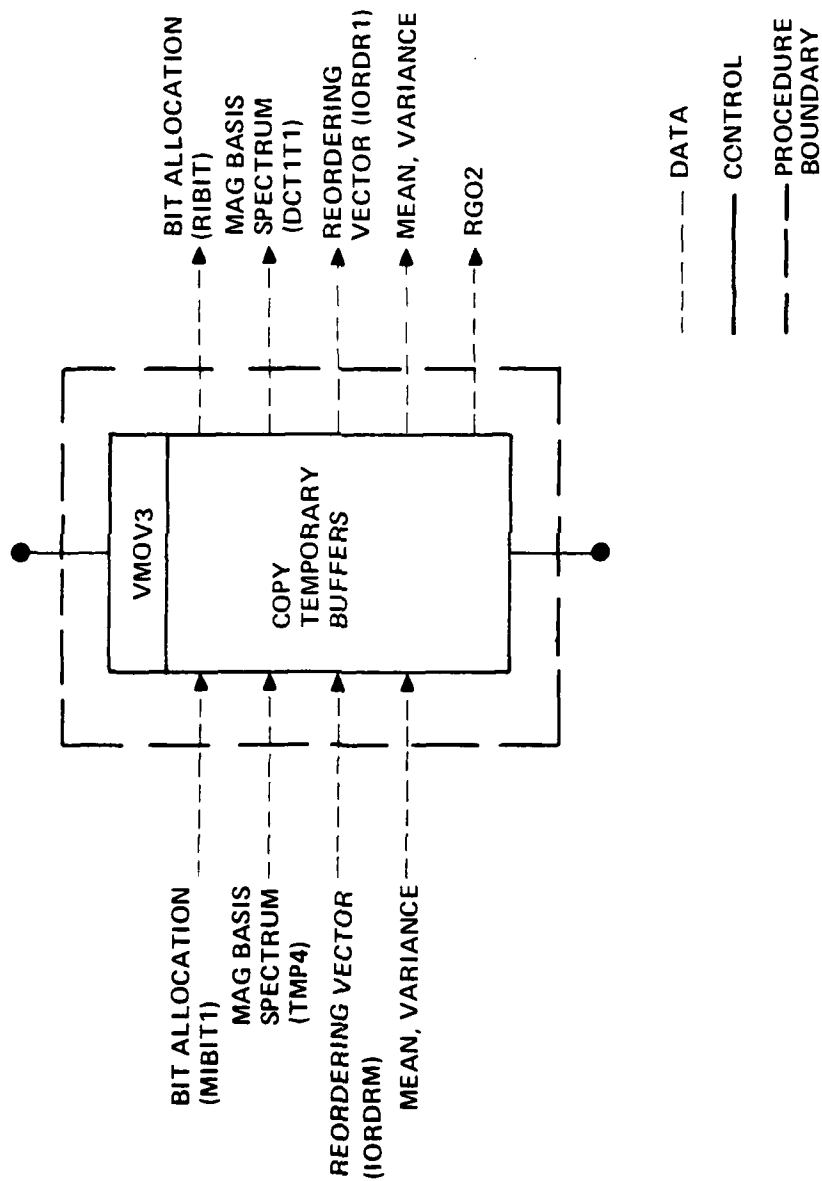
3.2.6.5 Copy Temporary Buffers Procedure (FCB 240, PBFCB 174)

This procedure, shown in Figure 3-23, consists of a single array function, VMOV3. Its purpose is to copy data from temporary buffers, used to speed up computation, to permanent buffers, which will remain unmodified until the next instance of the Synthesize process. It also makes the DCT bit allocation information available to the Receive process and enables the Receive process to proceed by setting the flag RG02 (integer scalar 116).

VMOV3 copies buffer IORDRM, the reordering indices, into buffer IORDR1, which is the first stage of the Synthesize ordering delay line described in subsection 3.2.6.1. It also copies TMP4, the reordered magnitude basis spectrum, into DCT1T1, the first stage of the Synthesize basis spectrum delay line. Buffer MIBIT1, the first stage of the Synthesize bit allocation delay line, already contains the bit allocation information, as a result of MPCDBA. VMOV3 copies this information into buffer RIBIT, which will be used by the Receive process to deserialize the received DCT coefficients, and sets flag RG02 to enable the Receive process to proceed with this deserialization. VMOV3 also copies the dequantized mean and variance from scalars 88 and 89 to scalars 102 and 103.

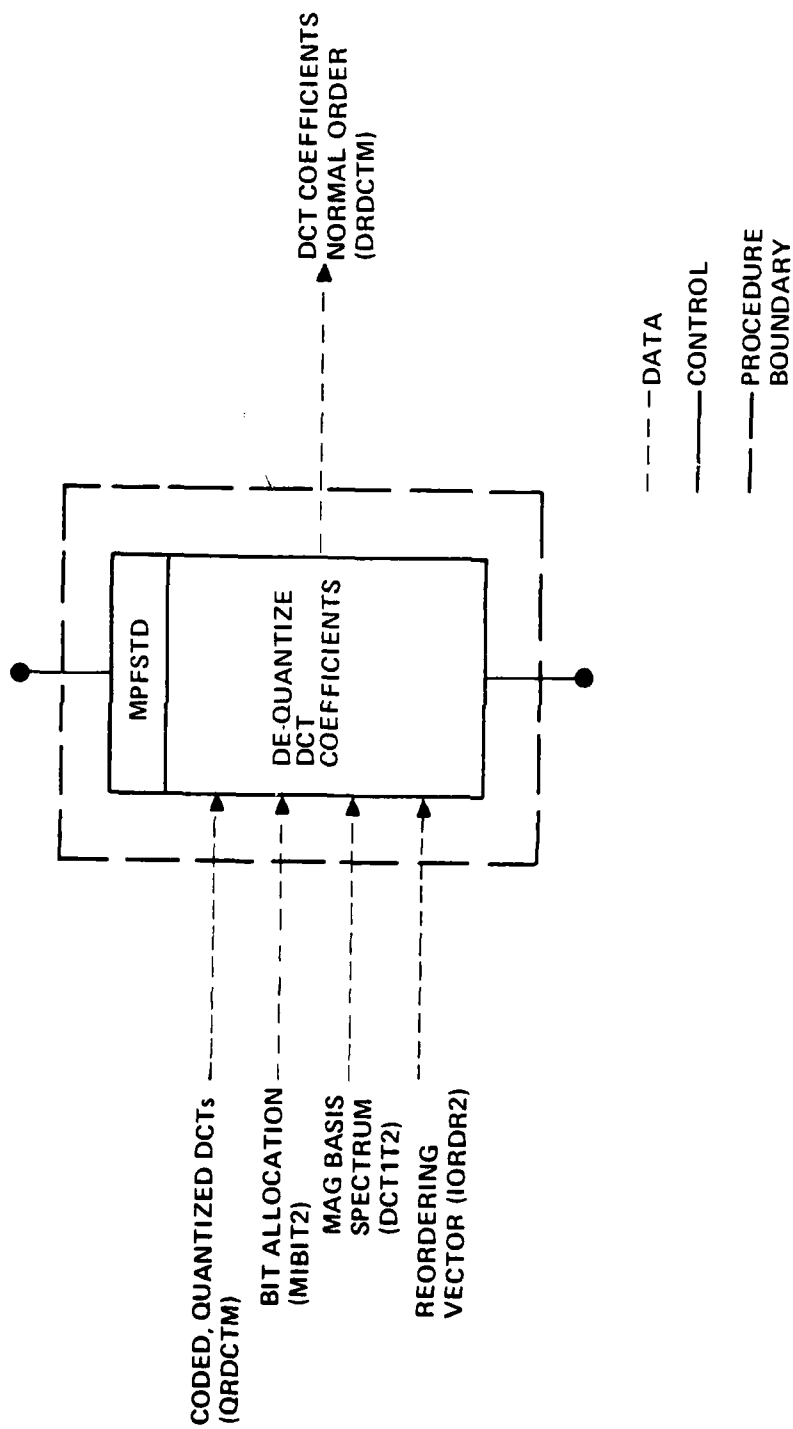
3.2.6.6 DCT Coefficient Dequantization Procedure (FCB 252, PBFCB 164)

This procedure, shown in Figure 3-24, consists of a single array function, MPFSTD. MPFSTD uses each coded DCT coefficient (sorted order)



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FIGURE 3-23: COPY TEMPORARY BUFFERS PROCEDURE



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FIGURE 3-24: DCT DEQUANTIZATION PROCEDURE

from buffer QRDCTM as an index into a decoding table. The particular table used is determined from the bit allocation vector, MIBIT2. The value found from the decoding table is multiplied by the corresponding magnitude basis spectrum coefficient (DCT1T2). The resulting product is the dequantized DCT coefficient and is stored in buffer DRDCTM, using the reordering vector (ICRDR2) as the index into this output buffer so that the results will be in normal order. That is,

$$\text{DRDCTM}(\text{ICRDR2}(I)) = \text{Decode}(\text{QRDCTM}(I)) \times \text{DCT1T2}(I)$$

The decoding tables are given in Table 3-8.

3.2.6.7 Inverse DCT Procedure

This procedure, shown in Figure 3-25, consists of three array functions, MPIDCM, FFTIN, and MPVDNM. It transforms the dequantized DCT coefficients back to a time domain waveform, restores the variance and mean to this waveform, and overlaps it with the waveform obtained in the previous frame to generate the output speech waveform.

3.2.6.7.1 MPIDCM (FCB 249, PBFCB 165)

This array function performs the inverse of the correction applied by the Analyze process array function MPDCTM (subsection 3.2.5.2.3). It forms a conjugate symmetric spectrum to which an inverse FFT will later be applied from the dequantized DCT coefficients in buffer DRDCTM. The real and complex parts of the output buffer $X2$ ($XR(K)$ and $XI(K)$), respectively, are formed as follows:

Code	3 Bit Value	2 Bit Value	1 Bit Value
7	-3.0867	-	-
6	-1.6725	-	-
5	-0.8330	-	-
4	-0.2334	-	-
3	3.0867	-1.8340	-
2	1.6725	-0.4196	-
1	0.8330	1.8340	-0.7071
0	0.2334	0.4196	0.7071

TABLE 3-8: DCT COEFFICIENT DEQUANTIZATION

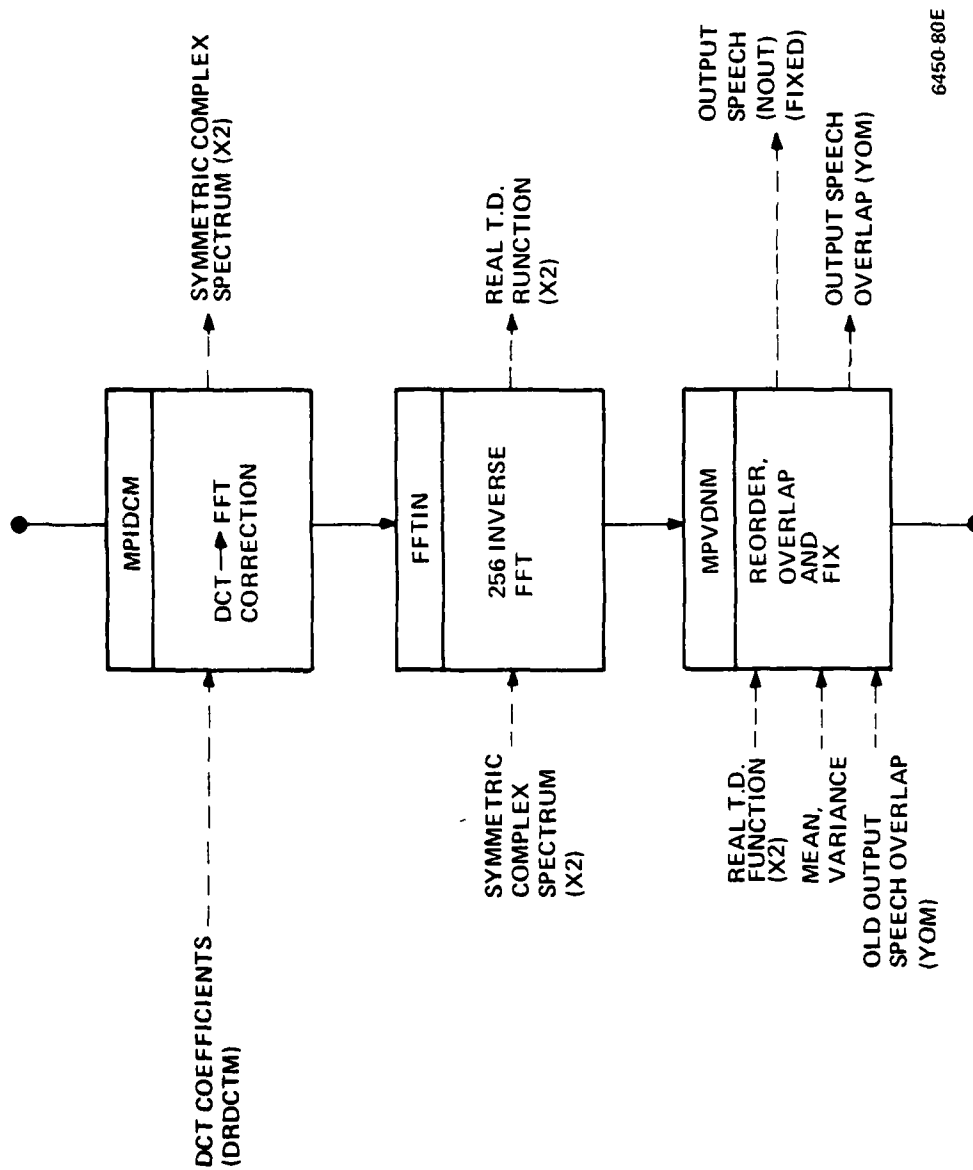


FIGURE 3-25: INVERSE DCT PROCEDURE

$$XR(K) = XR(N-K) = \cos(\pi K/2N) * DCT(K) + \sin(\pi K/2N) * DCT(N-K)$$

$$XI(K) = -XI(N-K) = \sin(\pi K/2N) * DCT(K) - \cos(\pi K/2N) * DCT(N-K)$$

$$XR(0) = DCT(0), XI(0) = 0$$

where N is 256 and K varies between 1 and 255.

This array function also computes a corrected mean and variance from the (delayed) dequantized mean and variance. The dequantized variance (scalar 101) is in dB and is changed to be linear. The dequantized mean (scalar 100) is multiplied by this linear variance to form an adjusted mean. Finally, the linear variance is divided by the frame length to form the per sample variance. The resulting mean and variance are stored in scalars 100 and 101, respectively.

3.2.6.7.2 FFTIN (FCB 206, PBFCB 171)

This array function, supplied by CSPI, performs a 256 complex inverse FFT (not in place) on the corrected DCT coefficients in buffer X2. The resulting time domain function is also stored in X2.

3.2.6.7.3 MPVDNM (FCB 253, PBFCB 166)

This array function restores natural order to the 256 point time domain function in X2, multiplies it by the variance (scalar 101), adds the mean (scalar 100), and overlaps the result with the last 10 samples of the output from the previous frame to form 246 output speech samples which are stored in buffer NOVt.

The overlapping consists of multiplying the first 10 samples by an increasing ramp and the last 10 samples of the previous frame by a descending ramp and adding the results as follows:

$$\text{OUTPUT}(I) = \text{NEW}(I) * .1 * I + \text{OLD}(236) * (1 - .1 * I)$$

where I ranges from 0 through 9.

The reordering of the time domain function forms the even numbered DCT coefficients from the first half of the real parts of buffer X2. The odd numbered coefficients are obtained by accessing the second half of the buffer in reverse order.

3.2.7 Executive Process

The Executive process is used to schedule the tasks performed by the Arithmetic Processor and the CSPU. The Executive, as supplied by CSPI, runs in the CSPU and assigns the AP sequential tasks from function lists, loading the AP with and starting it on a new array function as soon as it has finished with the previous one. Responding to interrupts and this AP scheduling are the only functions required of the CSPU in the standard SNAP software system. The ATC system, however, also uses the CSPU to perform computations required by the algorithm and unsuited to the AP, such as serialization and error correction. The standard SNAP executive has been modified to include this additional use of the CSPU.

3.2.7.1 Executive Modifications

The new CSPU program, which will be referred to as the Background program, actually includes the four background processes and a dispatch routine to schedule them. It is interfaced to the existing executive as a co-routine. However, in order to minimize modifications to the executive, it is implemented as a single interrupt routine containing multiple exits.

The standard Executive loads and starts the AP, prepares the next function to be loaded into the AP, and then waits for the AP to finish. The ATC modification changes this wait to a programmed interrupt (Device 24, level 8) which causes the Background program to continue from its last exit. The Background program periodically checks flag APDNFL (integer scalar 126) to determine whether the AP has finished. If it has, the Background program saves its state and exits, returning to the point in the Executive program following the original wait. The Executive then continues normally, restarting the AP and preparing the next array function, until it again reaches the state where it would wait for the AP.

The APDONE interrupt service routine, also part of the original Executive, has been modified for this new program structure. It now sets APDNFL before returning.

3.2.7.2 Process Scheduling

The process scheduling required in the ATC system includes the scheduling of the Analyze and Synthesize processes as well as the scheduling of the four I/O process background programs. The internal scheduling of array functions in the Analyze and Synthesize processes is accomplished by function lists, as described in subsection 3.3. The scheduling of the processes themselves is determined by flags ANA (integer scalar 100) and SYN (integer scalar 102) as shown in Figure 3-26. The Analyze process is enabled by flag ANA being clear. ANA is cleared by the Transmit interrupt routine to indicate that the last output has been used and the output buffer is available. ANA is set by the Transmit background program, which will fill the output buffer. The Synthesize process is enabled by flag SYN being clear. SYN is cleared by the Re-

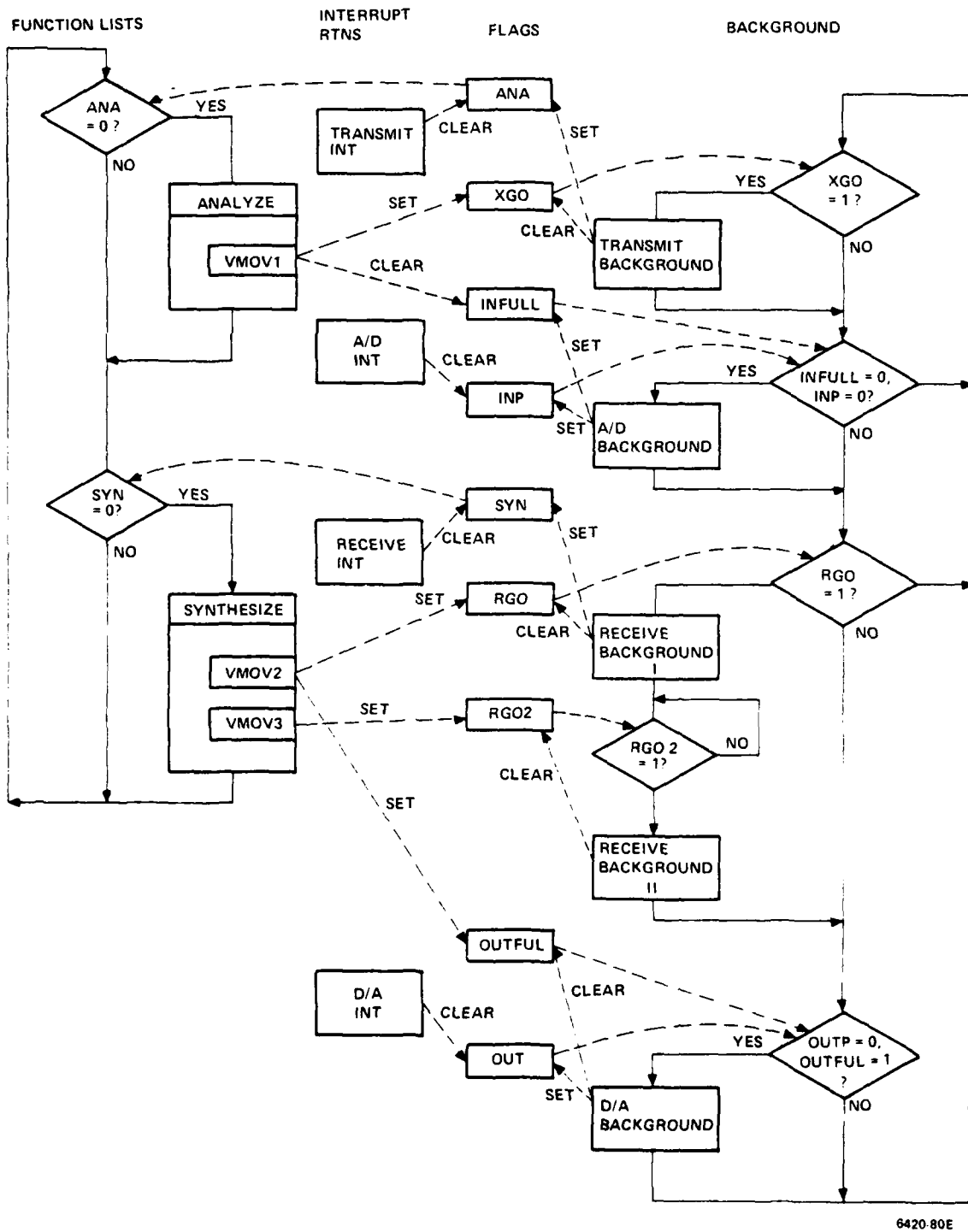


FIGURE 3-26: PROCESS SYNCHRONIZATION

ceive interrupt routine to indicate that the input buffer has been filled and is, therefore, available. SYN is set by the Receive background program which will read this buffer.

The Transmit background program is enabled by flag XG0 being set. It is set by VMOV1 in the Analyze process when the output data has been copied into buffers AQPB and ACTDCT and is thus available to the Transmit background process. The background process clears the flag.

The Receive background program is enabled by flag RG0 being set. It is set by VMOV2 in the Synthesize process when the data in ARQPB has been copied into a processing buffer, thus making ARQPB available for writing by the Receive background program, which clears the flag. When the Receive background program has deserialized the sideband parameters, it waits until the bit allocation determined by the Synthesize process is made available to it. VMOV3 in the Synthesize process sets flag RG02 when it has written new data into buffer RIBIT. RG02 being set enables the Receive background program to proceed to deserialize the DCT coefficients and to clear RG02.

The A/D background program is enabled by a combination of two flags. It runs when new input is available to it and its previous output has been used. Flag INP is cleared by the A/D interrupt routine to indicate that new input data is available. Flag INFULL is cleared by VMOV1 in the Analyze process to indicate that it has read the previous output of the background program (INPB) and that buffer is available for refilling. The A/D background program sets both of these flags.

The D/A background program is also enabled by a combination of two flags. It runs when its output buffer is empty and its input buffer is

full. Flag OUTP is cleared by the D/A interrupt routine to indicate that a buffer has been emptied and is available for more output. Flag OUTFUL is set by VMOV2 in the Synthesize process when new output speech has been copied to buffer OUTB and is available to the background program. The background program clears OUTFUL and sets OUTP.

3.3 System Software

The ATC system software consists of MAP-300 software and host PDP-11 software.

3.3.1 MAP-300 Software

The MAP-300 software includes the array functions described individually in section 3.2 and the Executive program. It also includes the IOS-2SM program, LINE. Areas of MAP memory used by the MAP-300 software are shown in Table 3-9.

3.3.1.1 Array Functions

The array functions contained in the ATC system are shown in Table 3-10. The starred functions have been written by GTE. All other array functions normally included in the CSPI SNAP II system have been deleted. This means that before a program using any of these deleted functions can be run after running the ATC system, the standard SNAP II system must be reloaded into the MAP.

3.3.1.2 Executive Program

The Executive program that is included in the ATC system has been modified from the standard SNAP II release. The modifications consist of a revised APDONE routine which has had buffer checking eliminated for reasons of efficiency and which has added flag control and additional software to provide computational support for the AP. The additions to the Executive are described in subsection 3.2.7.

Bus 1

0-21FE	EXEC
2260-38A4	Prebinding Buffers
4000-4678	SNAP Array Functions
4690-49C4	FF2R
49D0-515A	IOS
5714-5BC4	Scroll Program Buffers
639C-6EC4	GTE Array Functions
7150-89F8	GTE Array Functions
8A00-8AA5	Data Buffers
8AA6-AC60	CSPUIS (background programs)
AC62-ACA8	Scroll Interrupt Additions
BFFF	Top of Memory

Bus 2

400-17C0	Data Buffers
1D30-2300	Table Storage
231E-2F43	Data Buffers
3FFF	Top of Memory

Bus 3

0-1FFF	Data Buffers
1FFF	Top of Memory

TABLE 3-9: MEMORY MAP

Function	FCB Number	GTE-Supplied
VFLT (Y, A, U, B)	136	
VMOV (Y, U)	143	
VSMA1 (Y, A, U, B)	144	
VCOS (Y, A, U, B, V, C)	186	
FFTN (Y, U, V, W)	204	
FFTNB	205	
FFTIN (Y, U, V, W)	206	
FFTINB	207	
FF2R (Y, U, V, W)	214	
FF2RB	215	
VMOV1 (Y)	237	*
VMOV2 (Y)	238	*
VMOV3 (Y)	239	*
MPFDVM (Y, U, V)	240	*
MPDCTM (Y, U, V, W)	241	*
MPQDPP (Y, U, V)	242	*
MPDQPP (Y, U, V)	243	*
MPMWGX (Y, U, V, W)	244	*
MPFSTV (U)	245	*
MPSSRT (Y)	247	*
MPIDCM (Y, U, V)	248	*
MPASRT (Y)	249	*
MPFSTQ (Y, U, V, W)	250	*
MPFSTD (Y, U, V, W)	251	*
MPVDNM (Y, U, V)	252	*
MPBASP (Y, U, V, W)	253	*
MPSCAN (Y)	254	*
MPCDBA (Y)	255	*

TABLE 3-10: ARRAY FUNCTIONS

3.3.1.3 LINE Program

This program runs in the IOS-2SM scroll. It is loaded, initialized, and started by the host Fortran program. The function and operation of this program is described in subsection 3.2.3.1.

3.3.2 PDP-11 Software

The PDP-11 software consists of a Fortran control program, used to configure and initialize MAP buffers and to define function lists; software which provides the interface between this control program and the MAP driver; and utility software which provides an improved user interface to the MAP assembler.

3.3.2.1 Fortran Control Program

The Fortran control program consists of five major segments. These are: Configure and Initialize Buffers and Scalars (MAPON), Prebind Functions (CREATE), Define Function Lists (DEFINE), Configure Scroll Programs (SETUP), and Execute Function List (System).

These five segments are each subroutines, contained in separate overlays. The main program, MASTER, calls these subroutines in sequence. Data is passed between the main program and the subroutines through named common blocks. Other subroutines, mainly debugging aids, are included in the Fortran program which was designed to operate in any of three modes - Real-Time, Timing, and File-to-File. Only the Real-Time mode is operative in the delivered system.

3.3.2.1.1 Configure and Initialize Buffers

The buffer locations, sizes, types, and identifiers are shown in Table 3-11. The buffer initialization defines three cosine vectors.

M A P - 3 0 0
B U F F E R A R E A S

BID	BID #	BUS	BA	BA+AS-1	AS	ST	SI	WS
AOPR	43	1	35328.	35341.	14	I	E	L
AQDCT	44	1	35342.	35597.	256	I	E	L
AIBIT	45	1	35598.	35853.	256	I	E	L
SEN	49	1	35854.	36223.	370	I	E	L
INPB	50	1	36224.	36479.	256	I	E	L
BF1	63	1	36480.	36848.	369	I	E	L
BF2		1	36849.	37217.	369	I	E	L
BF3		1	37218.	37586.	369	I	E	L
ZRO	1	1	37587.	37955.	369	I	E	L
OUTR	59	1	37956.	38211.	256	I	E	L
RECV	56	1	38212.	38581.	370	I	E	L
ARQPR	57	1	38582.	38595.	14	I	E	L
ARQDCT	58	1	38596.	38851.	256	I	E	L
SYNR1	62	1	38852.	39220.	369	I	E	L
SYNR2		1	39221.	39589.	369	I	E	L
RIBIT	2	1	39590.	36245.	256	I	E	L
BIDBF1	63	1	10000.	14499.	4500	I	E	L
BIDBF2		1	8800.	9999.	1200	I	E	L
COSZ	24	2	1024.0	1535.0	256	R	E	L
YOM	26	2	2048.0	2067.0	10	R	E	L
XOM	27	2	2068.0	2087.0	10	R	E	L
MAPX#	28	2	2068.0	2579.0	256	R	E	L
MIBIT	29	2	2088.0	2343.0	256	I	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION

DCTM	30	2	2344.0	2855.0	256	R	E	L
		2	2856.0	2867.0	12	I	E	L
QTDCTM	32	2	2864.0	3123.0	256	I	E	L
NINM	33	2	3124.0	3369.0	246	I	E	L
VIN1	34	2	3370.0	3615.0	246	I	E	L
VIN2	35	2	3616.0	3861.0	246	I	E	L
VOU1M	36	2	3862.0	4107.0	246	I	E	L
VOU1	37	2	4108.0	4353.0	246	I	E	L
VOU2	38	2	4354.0	4599.0	246	I	E	L
RECV1	39	2	4600.0	4964.0	369	I	E	L
RECV2	40	2	4969.0	5337.0	369	I	E	L
SEN1	41	2	5340.0	5709.0	370	I	E	L
SEN2	42	2	5710.0	6079.0	370	I	E	L
DCT1T1	51	2	9000.0	9511.0	256	R	E	L
DCT1T2	52	2	9512.0	10023.0	256	R	E	L
IORDR1	53	2	10024.0	10279.0	256	I	E	L
IORDR2	54	2	10280.0	10535.0	256	I	E	L
MIBIT1	55	2	10536.0	10791.0	256	I	E	L
MIBIT2	60	2	10792.0	11047.0	256	I	E	L
QTDCT1	7	2	11048.0	11303.0	256	I	E	L
MIBIT3	5	2	11304.0	11559.0	256	I	E	L
MIBIT4	6	2	11560.0	11815.0	256	I	E	L
QPRM	31	2	11816.0	11829.0	14	I	E	L
QRPRM	8	2	11830.0	11843.0	14	I	E	L
QRDCIM	9	2	11844.0	12099.0	256	I	E	L
NDPA	10	3	0.0	2047.0	512	C	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION (CONT'D)

X1	11	3	2048.0	4095.0	512	C	E	L
X1R*		3			512	R	E	L
X11*		3			512	R	E	L
X2	48	3	2048.0	3071.0	256	C	E	L
IORDRM*	12	3	0.0	255.0	256	I	E	L
PWEITM*	13	3	0.0	255.0	256	I	E	L
DCTM1*	14	3	512.0	1023.0	256	R	E	L
DCTM2*	15	3	1024.0	1535.0	256	R	E	L
R1*	16	3	0.0	17.0	9	R	E	L
A1*	17	3	18.0	33.0	8	R	E	L
KF*	18	3	35.0	66.0	16	R	E	L
PARC*	3	3			8	R	E	L
IMP1*	19	3	2048.0	2559.0	256	R	E	L
IMP2*	20	3	2560.0	3071.0	256	R	E	L
IMP3*	46	3	3584.0	4095.0	256	R	E	L
IMP4*	47	3	3072.0	3583.0	256	R	E	L
DRDCTM*	21	3	1024.0	1535.0	256	R	E	L
YNM*		3	512.0	1023.0	256	R	E	L
VLONG	23	2	4096.0	5119.0	512	R	E	L
VSHRT	25	2	5120.0	5631.0	256	R	E	L

TABLE 3-11: ATC SYSTEM BUFFER CONFIGURATION (CONT'D)

Two of these, VSHRT and VLONG, are used for FFT's and contain a full cycle of the cosine each, consisting of 256 and 512 points, respectively. The third, COSZ, is used in the DCT correction and contains one quarter cycle of the cosine in 256 points. The initialization also clears the overlap buffers XOM and YOM, the I/O buffers NIN1, NIN2, NOUT1, NOUT2, RECV1, RECV2, SEN1, and SEN2. It also clears the data delay line buffers AQPB, AQTDCT, DCT1T1, DCT1T2, QTDCT1, QPRM, and QRPRM, and the sync buffers BF1, BF2, SYNBI, and SYNBI. It stores a buffer of zeros, ZRO, for subsequent use by the ATC system. It initializes the reordering vectors IORDR1 and IORDR2 to be the integers from 0 to 255, and sets the bit allocation buffers, MIBIT1, MIBIT2, MIBIT3, and MIBIT3 to contain 134 values of two and the remainder of the values in these buffers to be zero.

The scalars used and their initial values are shown in Table 3-12.

3.3.2.1.2 Prebound FCB's

To reduce AP loading overhead, all array functions used in the real-time loop of the ATC system are prebound. That is, a separate copy of the APS portion of each array function is maintained for each set of calling parameters given the function. The values of these parameters are stored in the copy at pre-binding and the time copy is block-loaded into the APS at execution time. Table 3-13 lists the prebound functions and the associated calling parameters.

3.3.2.1.3 Function Lists

The prebound array functions are collected into function lists. The use of function lists allows the MAP to run independently of the host which, by eliminating host overhead, increases the speed of the system.

SID	NAME	DESCRIPTION	INITIAL VALUE	CONSTANT
50		constant	1/512.	*
51		constant	1/256.	*
52	unused			
53	unused			
54	LTHINV	LTH Inverse	1/256.	*
55	unused			
56	DARG	constant	1/1024.	*
57	MDARG	constant	1/1024.	*
58	unused			
.	.			
.	.			
.	.			
73	unused			
74	LPCNP1	LPC order +1	9	*
75	LPCN	LPC order	8	*
76		constant	4.0	*
77		constant	10E-10	*
78	unused			
79	BTLTH	Bits avail. for DCTs	268	*
80	CTEMP	Bit all offset	-	
81	unused			
82	BITSMN	Temp for CTEMP calc.	-	
83	ITOT	# of DCTs to be coded	-	
84	BITMAX	Max bits/DCT	3.0	*
85		CTEMP +.5	-	
86		LPC Threshold	.995	*
87	LTH	Frame Length	256	*
88	DQDC	Dequantized Mean	-	
89	DQVAR	Dequantized Variance	-	
90	DQPG	Dequantized Pitch Gain	-	
91	DQM	Dequantized Pitch	-	
92	LTH2	2*LTH	512	*
93		Inverse Variance	-	
94		constant	20.0	*
95		Log 16 (10)		*
96	LTH4	4*LTH	1024.	*
97	LTHM1	LTH-1	255	*
98		constant	.05	*
99	unused			
100	DC ⁻²	Mean from 2nd prev. frame	-	
101	VAR ⁻²	Variance from 2nd prev. frame	-	
102	DC ⁻¹	Mean from prev. frame	-	
103	VAR ⁻¹	Variance from prev. frame	-	

TABLE 3-12: SCALARS

```

STATUS=MPCHF(RIDBF1,153)
INSTR=153
STATUS=MPEDVF(MAPX,NIMM,X2)
INSTR=154
STATUS=MPDCFM(DCTM,X1,X2,COSZ)
INSTR=155
STATUS=MPWGX(P1,NIMM,KF,A1)
INSTR=156
STATUS=MPQDPP(OPRM,PARC,A1)
INSTR=157
STATUS=MPFSTV(A1)
INSTR=158
STATUS=MPBASP(DCTM1,PWEITM,TMP2,X1)
INSTR=159
STATUS=MPASKT(IORDRM)
INSTR=160
STATUS=MPSCAN(DCTM2)
INSTR=161
STATUS=MPCDHA(MIBIT3)
INSTR=162
STATUS=MPFSTQ(QTOCTM,MIBIT3,TMP1,TMP2)
INSTR=163
STATUS=MPHOPP(PARC,ORPRM,A1)
INSTR=164
STATUS=MPFSTD(DRDCTM,QRDCTM,TMP1,IORDR2)
INSTR=165
STATUS=MPIOCM(X2,DRDCTM,COSZ)
INSTR=166
STATUS=MPVDM(NQIM,YOM,X2)
INSTR=167
STATUS=MPSSRF(IORDRM)
STATUS=MPTRF(0)
INSTR=3
STATUS=MPCRF(RIDBF2,168)
INSTR=168
STATUS=FFTIN(X2,1,X2,VSHPI,WORK)
INSTR=169
STATUS=FFTIN(X1,1,X1,VLONG,WORK)
INSTR=170
STATUS=FF2R(X1,4,X1,VLONG,WORK)
INSTR=171
STATUS=FFTIN(X2,1,X2,VSHRT,WORK)
INSTR=4
STATUS=MPTRF(0)
STATUS=MPCHF(RIDBF3,172)
INSTR=172
STATUS=VMOV1(ANPB)
INSTR=173
STATUS=VMOV2(ORPRM)
INSTR=174
STATUS=VMOV3(RIBIT)
INSTR=175
STATUS=MPCDHA(MIBIT1)
STATUS=MPTRF(0)

```

TABLE 3-13: PREBOUND FCB'S

The top level function list (WAIT) is very simple. It consists of three subordinate function lists, STRTUP, ATCSYN, and ATCANA, which are conditionally executed or not based on the state of three associated scalars, INIT, SYN, and ANA. STRTUP is executed only once. Its execution causes INIT to be set to unity, which denies the execution condition for all succeeding passes through the main function list. STRTUP initializes the process synchronization flags and double buffer pointers as shown in Table 3-14. It also loads and starts the three I/O scrolls.

Both ATCSYN and ATCANA are unconditional, sequential lists of pre-bound array functions. They are shown in Table 3-15. ATCANA is the control structure of the Analyze process, as discussed in section 3.2.5, and ATCSYN is the control structure of the Synthesize process, as discussed in section 3.2.6.

3.3.2.2 Host Support Software

The host support software consists of a function definition module for each array function, an additional function, FCBGN, called by each of these definitions, error reporting routines, and the MAP driver interface routine. This software is contained in the SNAPHS library. This library has been modified for the ATC system, as detailed in section 3.5. The changes are to delete unused array function definitions, to make separate libraries for each of the three MAP drivers in the host operating system, and to change FCBGN to support newly released array functions. This last modification increases the size of table ISARG from 18 to 22. ISARG contains parameter usage configurations. The ATC system requires these additional configurations.

Flag	Integer Scalar	Value	Meaning	Description
OUT	104	1	false	A/D buffer full
INP	106	1	false	D/A buffer empty
AFLG	101	0	buffer 1	current send buffer
SFLG	103	0	buffer 1	current receive buffer
IFLG	107	0	buffer 1	current A/D buffer
OFLG	105	0	buffer 1	current D/A buffer
APDNFL	126	0	false	APDONE has occurred
RG0	114	0	false	enable Receive background
RG02	116	0	false	enable Receive background part 2
XGO	115	0	false	enable Transmit background
INFULL	110	0	false	Analyze input full
OUTFUL	111	0	false	Synthesize output full
SYNC	117	0	-	sync position
RSYN	118	0	sync lost	sync state
NEWSYN	119	0	-	frames since sync acquired
OLSYN	120	0	-	previous frame sync bit
LSTER	121	0	false	error previous frame
ERSYN	122	0	-	number of sync bit errors

TABLE 3-14: SYSTEM FLAG INITIALIZATION

C LIST "WAIT": THE WAIT LOOP
 STATUS=MPEFL(WAIT)
 STATUS=MPIIF(INIT,EQ,0,STRTUP,FLO)
 STATUS=MPIIF(SYN,EQ,0,ATCSYN,FLO)
 STATUS=MPIIF(ANA,EQ,0,ATCANA,FLO)
 STATUS=MPEFL(FL3)

C LIST "STRTUP": INITIALIZATION LOOP
 STATUS=MPEFL(STRTUP)
 STATUS=MPIST(INIT,1)
 STATUS=MPIST(OUT,1)
 STATUS=MPIST(INP,1)
 STATUS=MPIST(AFLG,0)
 STATUS=MPIST(SFLG,0)
 STATUS=MPIST(IFLG,0)
 STATUS=MPIST(UELG,0)
 STATUS=MPIST(INFULL,-1)
 STATUS=MPIST(OUTFUL,-1)
 STATUS=MPIST(APDNFL,0)
 STATUS=MPIST(RG0,0)
 STATUS=MPIST(XG0,0)
 STATUS=MPIST(RG02,0)
 STATUS=MPIST(SYNC,0)
 STATUS=MPIST(RSYN,0)
 STATUS=MPIST(MFWSYN,0)
 STATUS=MPIST(OLSYN,0)
 STATUS=MPIST(LSTER,0)
 STATUS=MPIST(FPSYN,0)
 STATUS=MPLDS(AOM,IOS,AOMPM)
 STATUS=MPRNS(AOM,IOS,AOMSA)
 STATUS=MPLDS(ADAM,IOS,ADAMPM)
 STATUS=MPRNS(ADAM,IOS,ADAVSA)
 STATUS=MPLDS(IOSID,IOS,IOSPM)
 STATUS=MPRNS(IOSID,IOS,IOSSA)
 STATUS=MPEFL(FL4)

TABLE 3-15: FUNCTION LISTS

```

C      LIST "ATCANA": ATC ANALYZER *ERROR CODING AND SERIALIZATION
        INSTR=1043
        STATUS=MPREF(ATCANA)
C      RF(172)=VMOV1(AJPH)
        STATUS=MPXRF(172)
C      RF(153)=MPFDVM(MAPX,HINM,X2)
        STATUS=MPXRF(153)
C      RF(168)=FFTQ(X2,1,X2,VSHFT,WORK)
        STATUS=MPXRF(168)
C      RF(154)=MPDCTM(DCTA,X1,X2,COSZ)
        STATUS=MPXRF(154)
C      RF(169)=FFTIN(X1,1,X1,VLONG,WORK)
        STATUS=MPXRF(169)
C      RF(155)=MPMWGX(R1,HINM,KE,A1)
        STATUS=MPXRF(155)
C      RF(156)=MPQDPP(QPRA,PARC,A1)
        STATUS=MPXRF(156)
C      RF(157)=MPFSIV(A1)
        STATUS=MPXRF(157)
C      RF(170)=FF2P(X1,4,X1,VLONG,WORK)
        STATUS=MPXRF(170)
C      RF(158)=MPRASP(DCTM1,PWEITM,IMP2,X1)
        STATUS=MPXRF(158)
C      RF(159)=MPASRT(IORDM)
        STATUS=MPXRF(159)
C      RF(160)=MPSCAN(DCTM2)
        STATUS=MPXRF(160)
C      RF(161)=MPCDPA(MINIT3)
        STATUS=MPXRF(161)
C      RF(162)=MPFSTQ(QDCTA,MINIT,IMP1,IMP2)
        STATUS=MPXRF(162)
C      IF(TIMING.EQ.YES) STATUS=MPXFL(ATCSYN)
        INSTR=1050
        STATUS=MPXFL(FL1)
C

```

TABLE 3-15: FUNCTION LISTS (CONT'D)

```

C      LIST "ATCSYN": ATC SYNTHESIZER W/ERROR DECODER & DESERIALIZATION
      INSTR=1055
      STATUS=MPXBF(ATCSYN)
C      BF(173)=V*DV2(ORPRK)
      STATUS=MPXBF(173)
C      BF(163)=MPQRP(PAPC,ORPRK,A1)
      STATUS=MPXBF(163)
C      BF(157)=MPSTV(A1)
      STATUS=MPXBF(157)
C      BF(170)=PF2R(X1,4,X1,VLONG,WDRF)
      STATUS=MPXBF(170)
C      BF(158)=MPBASP(DCT#1,P*EITM,TMP2,X1)
      STATUS=MPXBF(158)
C      BF(167)=MPSSRI(10PDR*)
      STATUS=MPXBF(167)
C      BF(160)=MPSCAN(DCT#2)
      STATUS=MPXBF(160)
C      BF(175)=MPCDBA(M*BIT1)
      STATUS=MPXBF(175)
C      BF(174)=V*DV3(R*BIT)
      STATUS=MPXBF(174)
C      BF(164)=MPSTB(ORDCM,ORDCTM,PAF1,10RDR2)
      STATUS=MPXBF(164)
C      BF(165)=MPIDCM(X2,ORDCTM,COSZ)
      STATUS=MPXBF(165)
C      BF(171)=PF1N(X2,1,X2,VSHRT,WDRK)
      STATUS=MPXBF(171)
C      BF(166)=MPVDRM(VOUT#4,YOUT,X2)
      STATUS=MPXBF(166)
      STATUS=MPXBF(FL2)

```

TABLE 3-15: FUNCTION LISTS (CONT'D)

AD-A091 663

GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME 50--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

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3.3.2.3 Utility Software

A utility program, PREMAP, is included in the delivered ATC system. This program reformats text file to be acceptable to the MAP assembler, MAPASM. In particular, it changes tabs in the input text to sequences of spaces in the output text and supplies continuation lines for comments.

3.4 System Hardware

The ATC system includes equipment manufactured by CSP Inc., Billerica, Mass., and equipment designed and built by GTE Sylvania.

3.4.1 CSPI-Supplied Hardware

The CSPI-supplied hardware consists of a MAP-300 Array Processor, Model 1030, with attached options. These options are:

- 8K x 32 MOS Master Memory, 500 nsec, Bus 1, model 2030
- 16K x 32 MOS Slave Memory, 500 nsec, Bus 1, model 2050
- 8K x 32 MOS Master Memory, 300 nsec, Bus 2, model 2203
- 4K x 32 MOS Master Memory, 170 nsec, Bus 3, model 2410
- PDP-11 Interface, model 3110
- I/O Scroll type 2SM, model 4020
- Bus Switch (2), model 4040
- Analog Data Acquisition Module (ADAM), model 5120
- Analog Output Module (AOM), model 5130
- Expansion Chassis, model 6100
- Auxiliary Power Supply, model 6200

The MAP includes preprogrammed micro-code in read-only memory. The ATC system requires Revision 18 of this micro-code.

3.4.2 GTE-Supplied Hardware

The GTE-supplied hardware consists of the Speech Processing Interface (SPI), which provides an analog interface to the MAP, and the Full Duplex Interface (FDI), which provides a digital interface to the MAP. These interfaces were supplied to two other contractors as well as being used in the GTE ATC system.

As the ATC project progressed, it became apparent that modifications to the GTE-supplied hardware were required. Subsection 3.4.2.3 describes these modifications.

3.4.2.1 GTE Speech Processing Interface (SPI)

The SPI assembly provides the amplification, equalization, and filtering necessary to interface an audio handset to the MAP-300 A/D and D/A converters. A block diagram of the SPI is shown in Figure 3-27. The SPI also serves as the common junction point for all digital signals between the MAP-300 and an external modem. The interconnections between the MAP, the SPI, and the external devices are shown in Figure 3-28.

A switch on the SPI front panel, which is shown in Figure 3-29, permits the selection of either of two sets of filters, thereby permitting a choice of cutoff frequency. Filters having cutoff frequencies of 3200 Hz and 3800 Hz are provided. The filters are of the plug-in type, thereby enabling the user to install other filters with different cutoff frequencies of his choice if so desired.

The handset provided with the equipment uses a dynamic microphone which has been designed to GTE Sylvania specifications and has been optimized for use in speech processing applications. The handset connects directly to the front panel of the audio interface assembly and may be stored on the hookswitch, which is also located on the front panel. When "on hook," the audio circuits (both receiving and transmitting) for the handset are disabled. A 25-foot extension cable for use with the handset is also provided.

A pair of telephone jacks located on the rear panel of the audio interface unit (shown in Figure 3-30) may be used to connect a tape recorder or test equipment for test and measurement purposes. The audio circuits for the tape recorder are always active and are unaffected by the operation of the hookswitch. A single connector, also located on the rear panel, is used to connect the assembly to the A/D-D/A converters of the MAP-300. Table 3-16 gives the specifications of the SPI.

Handset Interface

```

Input impedance.....150 ohms, balanced
Output impedance.....150 ohms, balanced
Gain, input to A/D converter.....42 dB
Gain, D/A converter to output.....-24 dB

```

Tape Recorder Interface

```

Input impedance.....600 ohms, balanced
Output impedance.....600 ohms, balanced
Gain, input to A/D converter.....0 dB
Gain, D/A converter to output.....0 dB

```

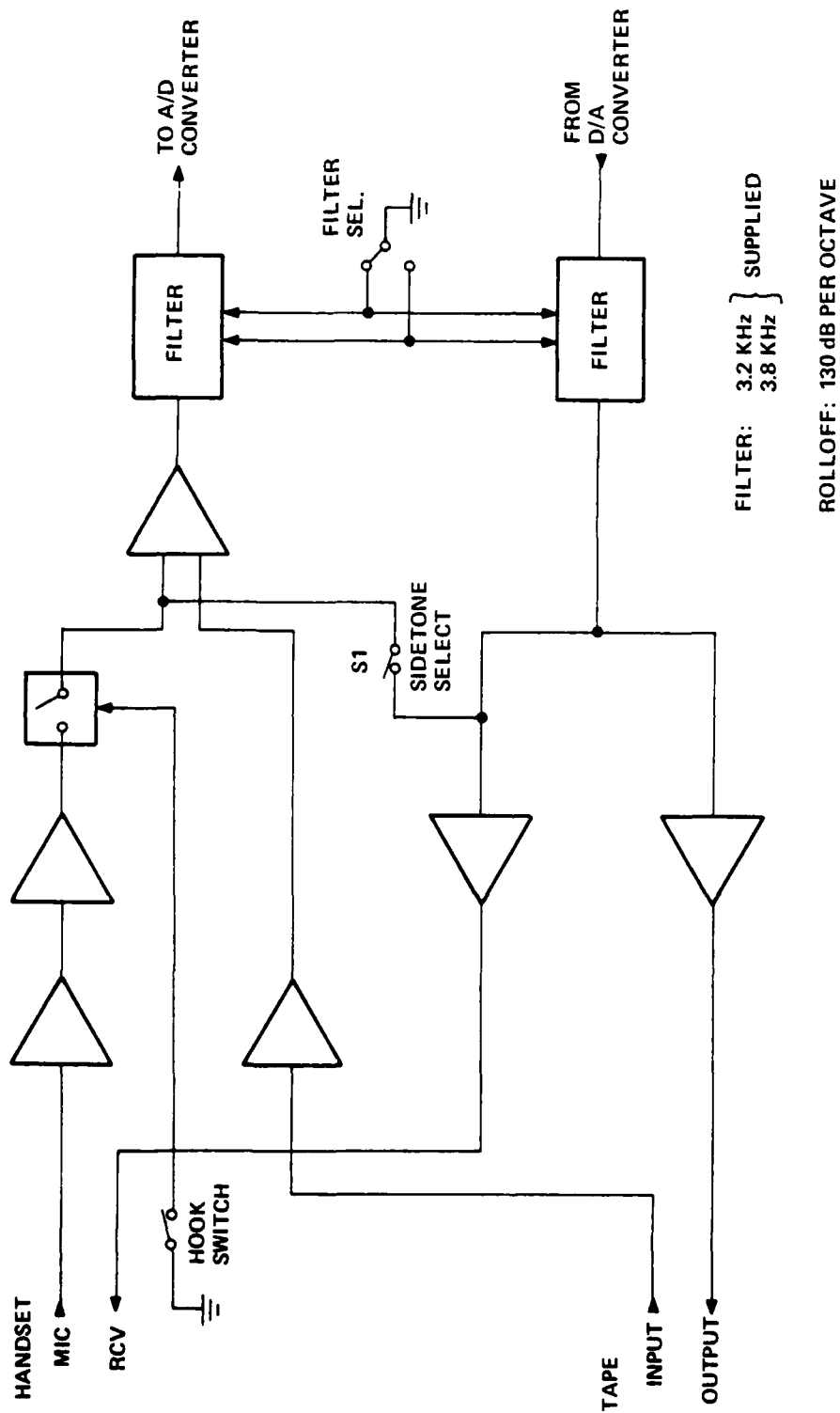
MAP-300 Interface

```
Output impedance - A/D converter.....3000 ohms
Signal level - A/D converter .....+5 V pk-pk
Input impedance - D/A converter.....3000 ohms
Signal level - D/A converter.....+5 V pk-pk
```

Filter Characteristics

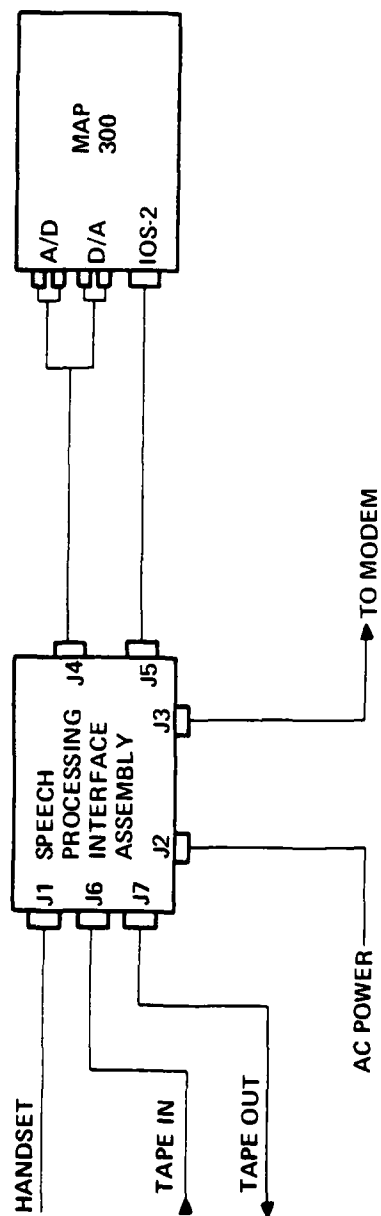
```
Cutoff frequency.....3.2 KHz } switch
                               3.8 KHz } selectable
Rolloff.....130 dB per octave
```

TABLE 3-16: SPI SPECIFICATIONS



6421-80E

FIGURE 3-27: SPI BLOCK DIAGRAM



6435-80E

FIGURE 3-28: INTERCONNECTION DIAGRAM

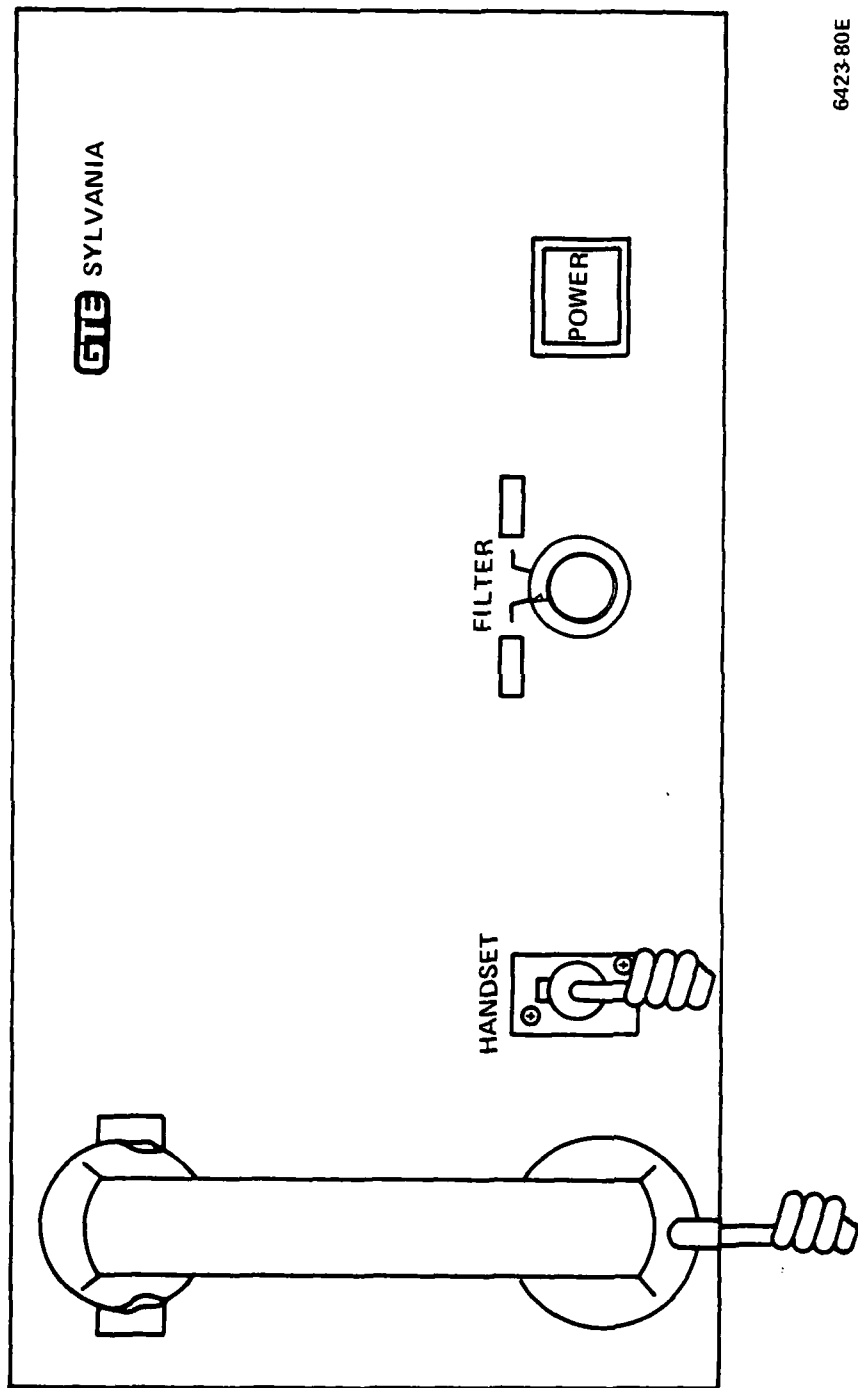
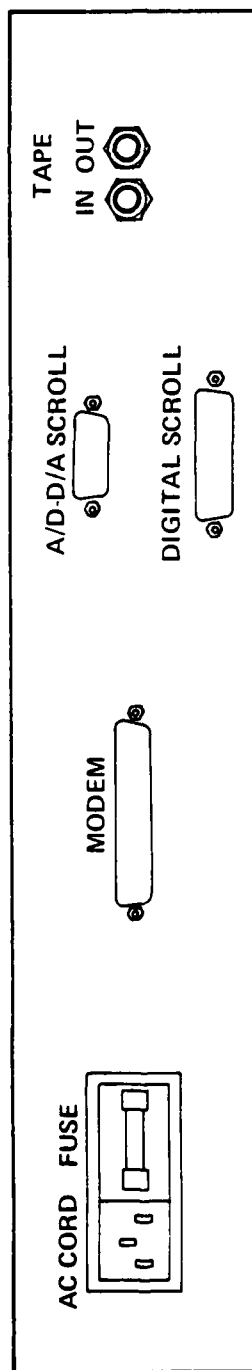


FIGURE 3-29: FRONT PANEL



6422-80E

FIGURE 3-30: REAR PANEL

3.4.2.2 Full Duplex Interface (FDI)

The CSPI IOS-2SM scroll has been modified by GTE Sylvania to provide a means for interfacing to any modem employing an EIA RS-449/RS-432 interface. The interface connections are given in Table 3-17. The interface design is such that an EIA RS-232-C interface may also be used, and the line drivers and receivers have been selected such that the protective networks for RS-449/RS-232 interoperation (refer to EIA Industrial Bulletin No. 12) are not required. Table 3-18 is a comparison between RS-449 and RS-232-C conventions. In addition to the modem interface, the modified I/O scroll includes a programmable real-time clock which generates the timing signals for speech sampling and the modem data. The I/O scroll is connected to the audio interface assembly by means of a single cable.

The data and speech sampling rates are set by issuing a single 16-bit control word from the IOS-2SM. The format of this control word and the FDI data formats are shown in Figure 3-31. The most significant byte of the control word determines the data rate, whereas the least significant byte determines the speech sampling rate. These control bytes and the associated data and sampling rates are given in Table 3-19. Each control byte controls a separate frequency divider. The basic clock frequency, 1.536 MHz, is first divided by four and then further divided by the selected divide ratio. The modem clock is then divided by an additional factor of two to produce a square wave with a 50% duty cycle. Once the real-time clock has been so programmed, it is not necessary to issue any other control words unless it is desired to change either of the clock rates, or unless power is removed from the MAP-300. It should be noted that the entire control word must be issued whenever changing rates, even if only one rate is to be changed.

<u>PIN</u>	<u>SIGNAL</u>
2	SI - Signal Rate Indicator
4	SD - Send Data
6	RD - Receive Data
7	RS - Request to Send
8	RT - Receive Timing
9	CS - Clear to Send
11	DM - Data Mode
12	TR - Terminal Ready
13	RR - Receiver Ready
15	IC - Incoming Call
16	SR - Signal Rate Selector
17	TT - Terminal Timing
19	SG - Signal Ground
20	RC - Receive Common
33	SQ - Signal Quality
37	SC - Send Common

TABLE 3-17: MODEM INTERFACE CONNECTIONS (RS-449)

RS-449		RS-232C	
SG SC RC	SIGNAL GROUND SEND COMMON RECEIVE COMMON	AG 	SIGNAL GROUND
IS IC TR DM	TERMINAL IN SERVICE INCOMING CALL TERMINAL READY DATA MODE	CE CD CC	RINGS INDICATOR DATA TERMINAL READY DATA SET READY
SD RD	SEND DATA RECEIVE DATA	DA DB	TRANSMITTED DATA RECEIVED DATA
TT ST RT	TERMINAL TIMING SEND TIMING RECEIVE TIMING	DA DB DD	TRANSMITTER SIGNAL ELEMENT TIMING (DTE SOURCE) TRANSMITTER SIGNAL ELEMENT TIMING (DCE SOURCE) RECEIVER SIGNAL ELEMENT TIMING
RS CS RR SQ NS SF SR SI	REQUEST TO SEND CLEAR TO SEND RECEIVER READY SIGNAL QUALITY NEW SIGNAL SELECT FREQUENCY SIGNALING RATE SELECTOR SIGNALING RATE INDICATOR	CA CB CF CG CH CI	REQUEST TO SEND CLEAR TO SEND RECEIVED LINE SIGNAL DETECTOR SIGNAL QUALITY DETECTOR DATA SIGNAL RATE SELECTOR (DTE SOURCE) DATA SIGNAL RATE SELECTOR (DCE SOURCE)
SSD SRD	SECONDARY SEND DATA SECONDARY RECEIVE DATA	SRA SRB	SECONDARY TRANSMITTED DATA SECONDARY RECEIVED DATA
SRS SCS SRR	SECONDARY REQUEST TO SEND SECONDARY CLEAR TO SEND SECONDARY RECEIVER READY	SCA SCB SCF	SECONDARY REQUEST TO SEND SECONDARY CLEAR TO SEND SECONDARY RECEIVED LINE SIGNAL DETECTOR
LL RL TM	LOCAL LOOPBACK REMOTE LOOPBACK TEST MODE		
SS SB	SELECT STANDBY STANDBY INDICATOR		

TABLE 3-18: RS-449/RS-232C COMPARISON

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 1

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
0	00	00000000	256	0.750	1.500
1	01	00000001	255	0.753	1.506
2	02	00000010	254	0.756	1.512
3	03	00000011	253	0.759	1.518
4	04	00000100	252	0.762	1.524
5	05	00000101	251	0.765	1.530
6	06	00000110	250	0.768	1.536
7	07	00000111	249	0.771	1.542
8	08	00001000	248	0.774	1.548
9	09	00001001	247	0.777	1.555
10	0A	00001010	246	0.780	1.561
11	0B	00001011	245	0.783	1.567
12	0C	00001100	244	0.787	1.574
13	0D	00001101	243	0.790	1.580
14	0E	00001110	242	0.793	1.587
15	0F	00001111	241	0.797	1.593
16	10	00010000	240	0.800	1.600
17	11	00010001	239	0.803	1.607
18	12	00010010	238	0.807	1.613
19	13	00010011	237	0.810	1.620
20	14	00010100	236	0.814	1.627
21	15	00010101	235	0.817	1.634
22	16	00010110	234	0.821	1.641
23	17	00010111	233	0.824	1.648
24	18	00011000	232	0.828	1.655
25	19	00011001	231	0.831	1.662
26	1A	00011010	230	0.835	1.670
27	1B	00011011	229	0.838	1.677
28	1C	00011100	228	0.842	1.684
29	1D	00011101	227	0.846	1.692
30	1E	00011110	226	0.850	1.699
31	1F	00011111	225	0.853	1.707
32	20	00100000	224	0.857	1.714
33	21	00100001	223	0.861	1.722
34	22	00100010	222	0.865	1.730
35	23	00100011	221	0.869	1.738
36	24	00100100	220	0.873	1.745
37	25	00100101	219	0.877	1.753
38	26	00100110	218	0.881	1.761
39	27	00100111	217	0.885	1.770
40	28	00101000	216	0.889	1.778
41	29	00101001	215	0.893	1.786

TABLE 3-19: REAL TIME CLOCK CONTROL
3-99

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHz

PAGE 2

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
42	2A	00101010	214	0.997	1.794
43	2B	00101011	213	0.998	1.803
44	2C	00101100	212	0.999	1.811
45	2D	00101101	211	0.999	1.820
46	2E	00101110	210	0.999	1.829
47	2F	00101111	209	0.999	1.837
48	30	00110000	208	0.998	1.846
49	31	00110001	207	0.998	1.855
50	32	00110010	206	0.998	1.864
51	33	00110011	205	0.998	1.873
52	34	00110100	204	0.998	1.882
53	35	00110101	203	0.998	1.892
54	36	00110110	202	0.998	1.901
55	37	00110111	201	0.998	1.910
56	38	00111000	200	0.998	1.920
57	39	00111001	199	0.998	1.930
58	3A	00111010	198	0.998	1.939
59	3B	00111011	197	0.998	1.949
60	3C	00111100	196	0.998	1.959
61	3D	00111101	195	0.998	1.969
62	3E	00111110	194	0.998	1.979
63	3F	00111111	193	0.998	1.990
64	40	01000000	192	1.000	2.000
65	41	01000001	191	1.000	2.010
66	42	01000010	190	1.011	2.021
67	43	01000011	189	1.016	2.032
68	44	01000100	188	1.021	2.043
69	45	01000101	187	1.027	2.053
70	46	01000110	186	1.032	2.065
71	47	01000111	185	1.038	2.076
72	48	01001000	184	1.043	2.087
73	49	01001001	183	1.049	2.098
74	4A	01001010	182	1.055	2.110
75	4B	01001011	181	1.061	2.122
76	4C	01001100	180	1.067	2.133
77	4D	01001101	179	1.073	2.145
78	4E	01001110	178	1.079	2.157
79	4F	01001111	177	1.085	2.169
80	50	01010000	176	1.091	2.182
81	51	01010001	175	1.097	2.194
82	52	01010010	174	1.103	2.207
83	53	01010011	173	1.110	2.220

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)
3-100

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.535 MHZ

PAGE 3

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
84	54	01010100	172	1.116	2.233
85	55	01010101	171	1.123	2.246
86	56	01010110	170	1.129	2.259
87	57	01010111	169	1.136	2.272
88	58	01011000	168	1.143	2.286
89	59	01011001	167	1.150	2.299
90	5A	01011010	166	1.157	2.313
91	5B	01011011	165	1.164	2.327
92	5C	01011100	164	1.171	2.341
93	5D	01011101	163	1.178	2.356
94	5E	01011110	162	1.185	2.370
95	5F	01011111	161	1.193	2.385
96	60	01100000	160	1.200	2.400
97	61	01100001	159	1.208	2.415
98	62	01100010	158	1.215	2.430
99	63	01100011	157	1.223	2.446
100	64	01100100	156	1.231	2.462
101	65	01100101	155	1.239	2.477
102	66	01100110	154	1.247	2.494
103	67	01100111	153	1.255	2.510
104	68	01101000	152	1.263	2.526
105	69	01101001	151	1.272	2.543
106	6A	01101010	150	1.280	2.560
107	6B	01101011	149	1.289	2.577
108	6C	01101100	148	1.297	2.595
109	6D	01101101	147	1.306	2.612
110	6E	01101110	146	1.315	2.630
111	6F	01101111	145	1.324	2.648
112	70	01110000	144	1.333	2.667
113	71	01110001	143	1.343	2.685
114	72	01110010	142	1.352	2.704
115	73	01110011	141	1.362	2.723
116	74	01110100	140	1.371	2.743
117	75	01110101	139	1.381	2.763
118	76	01110110	138	1.391	2.783
119	77	01110111	137	1.401	2.803
120	78	01111000	136	1.412	2.824
121	79	01111001	135	1.422	2.844
122	7A	01111010	134	1.433	2.866
123	7B	01111011	133	1.444	2.887
124	7C	01111100	132	1.455	2.909
125	7D	01111101	131	1.466	2.931

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)
3-101

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 4

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
126	7E	01111110	130	1.477	2.954
127	7F	01111111	129	1.483	2.977
128	80	10000000	128	1.500	3.000
129	81	10000001	127	1.512	3.024
130	82	10000010	126	1.524	3.048
131	83	10000011	125	1.536	3.072
132	84	10000100	124	1.543	3.087
133	85	10000101	123	1.561	3.122
134	86	10000110	122	1.574	3.148
135	87	10000111	121	1.587	3.174
136	88	10001000	120	1.600	3.200
137	89	10001001	119	1.613	3.227
138	8A	10001010	118	1.627	3.254
139	8B	10001011	117	1.641	3.282
140	8C	10001100	116	1.655	3.310
141	8D	10001101	115	1.670	3.339
142	8E	10001110	114	1.684	3.368
143	8F	10001111	113	1.699	3.397
144	90	10010000	112	1.714	3.429
145	91	10010001	111	1.730	3.459
146	92	10010010	110	1.745	3.491
147	93	10010011	109	1.761	3.523
148	94	10010100	108	1.773	3.556
149	95	10010101	107	1.794	3.589
150	96	10010110	106	1.811	3.623
151	97	10010111	105	1.829	3.657
152	98	10011000	104	1.846	3.692
153	99	10011001	103	1.864	3.729
154	9A	10011010	102	1.882	3.765
155	9B	10011011	101	1.901	3.802
156	9C	10011100	100	1.920	3.840
157	9D	10011101	99	1.939	3.879
158	9E	10011110	98	1.959	3.918
159	9F	10011111	97	1.977	3.959
160	A0	10100000	96	2.000	4.000
161	A1	10100001	95	2.021	4.042
162	A2	10100010	94	2.043	4.085
163	A3	10100011	93	2.065	4.129
164	A4	10100100	92	2.087	4.174
165	A5	10100101	91	2.110	4.220
166	A6	10100110	90	2.133	4.267
167	A7	10100111	89	2.157	4.315

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)
3-102

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 5

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
168	A8	10101000	88	2.182	4.364
169	A9	10101001	87	2.207	4.414
170	AA	10101010	86	2.233	4.465
171	AB	10101011	85	2.259	4.518
172	AC	10101100	84	2.286	4.571
173	AD	10101101	83	2.313	4.627
174	AE	10101110	82	2.341	4.683
175	AF	10101111	81	2.370	4.741
176	B0	10110000	80	2.400	4.800
177	B1	10110001	79	2.430	4.861
178	B2	10110010	78	2.462	4.923
179	B3	10110011	77	2.494	4.987
180	B4	10110100	76	2.526	5.053
181	B5	10110101	75	2.560	5.120
182	B6	10110110	74	2.595	5.189
183	B7	10110111	73	2.630	5.260
184	B8	10111000	72	2.667	5.333
185	B9	10111001	71	2.704	5.408
186	BA	10111010	70	2.743	5.486
187	BB	10111011	69	2.783	5.565
188	BC	10111100	68	2.824	5.647
189	BD	10111101	67	2.866	5.731
190	BE	10111110	66	2.909	5.818
191	BF	10111111	65	2.954	5.909
192	C0	11000000	64	3.000	6.000
193	C1	11000001	63	3.043	6.085
194	C2	11000010	62	3.087	6.174
195	C3	11000011	61	3.148	6.295
196	C4	11000100	60	3.200	6.400
197	C5	11000101	59	3.254	6.508
198	C6	11000110	58	3.310	6.621
199	C7	11000111	57	3.368	6.737
200	C8	11001000	56	3.429	6.857
201	C9	11001001	55	3.491	6.982
202	CA	11001010	54	3.556	7.111
203	CB	11001011	53	3.623	7.245
204	CC	11001100	52	3.692	7.385
205	CD	11001101	51	3.765	7.529
206	CE	11001110	50	3.840	7.680
207	CF	11001111	49	3.918	7.837
208	D0	11010000	48	4.000	8.000
209	D1	11010001	47	4.085	8.170

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHz

PAGE 6

DECIMAL	COUNTER SETTING HEXADECIMAL	BINARY	DIVIDE RATIO	OUTPUT RATE - KHZ LINE	SPEECH
210	02	11010010	45	4.174	8.348
211	03	11010011	45	4.267	8.533
212	04	11010100	44	4.364	8.727
213	05	11010101	43	4.465	8.930
214	06	11010110	42	4.571	9.143
215	07	11010111	41	4.683	9.366
216	08	11011000	40	4.800	9.600
217	09	11011001	39	4.923	9.846
218	0A	11011010	38	5.053	10.105
219	0B	11011011	37	5.189	10.377
220	0C	11011100	36	5.333	10.667
221	0D	11011101	35	5.483	10.971
222	0E	11011110	34	5.647	11.294
223	0F	11011111	33	5.818	11.636
224	10	11100000	32	6.000	12.000
225	11	11100001	31	6.194	12.387
226	12	11100010	30	6.400	12.800
227	13	11100011	29	6.621	13.241
228	14	11100100	28	6.857	13.714
229	15	11100101	27	7.111	14.222
230	16	11100110	26	7.385	14.769
231	17	11100111	25	7.680	15.360
232	18	11101000	24	8.000	16.000
233	19	11101001	23	8.348	16.696
234	1A	11101010	22	8.727	17.455
235	1B	11101011	21	9.143	18.286
236	1C	11101100	20	9.600	19.200
237	1D	11101101	19	10.105	20.211
238	1E	11101110	18	10.667	21.333
239	1F	11101111	17	11.294	22.588
240	20	11110000	16	12.000	24.000
241	21	11110001	15	12.800	25.600
242	22	11110010	14	13.714	27.429
243	23	11110011	13	14.769	29.538
244	24	11110100	12	16.000	32.000
245	25	11110101	11	17.455	34.909
246	26	11110110	10	19.200	38.400
247	27	11110111	9	21.333	42.667
248	28	11111000	8	24.000	48.000
249	29	11111001	7	27.429	54.857
250	2A	11111010	6	32.000	64.000
251	2B	11111011	5	38.400	76.800

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)

PROGRAMMABLE OSCILLATOR OUTPUT RATES

OSCILLATOR FREQUENCY = 1.536 MHZ

PAGE 7

DECIMAL	COUNTER SETTING		DIVIDE RATIO	OUTPUT RATE - KHZ	
	HEXADECIMAL	BINARY		LINE	SPEECH
252	FC	11111100	4	48.000	96.000
253	FD	11111101	3	64.000	128.000
254	FE	11111110	2	96.000	192.000
255	FF	11111111	1	192.000	384.000

TABLE 3-19: REAL TIME CLOCK CONTROL (CONT'D)

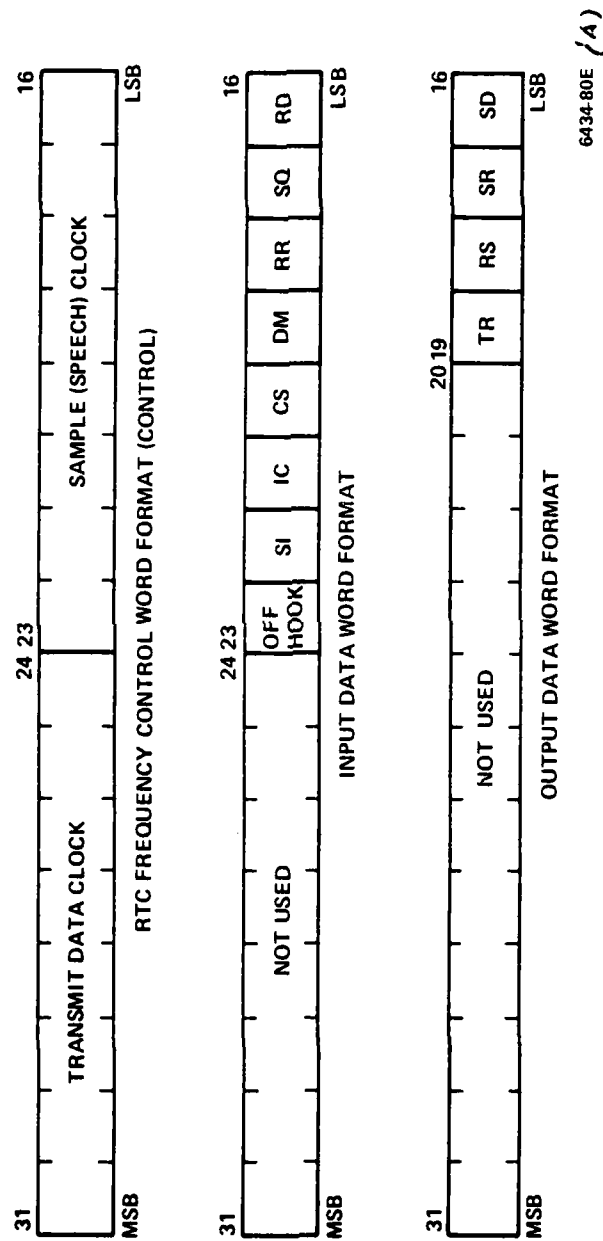


FIGURE 3-31: FDI FORMATS

Data transfers between the MAP-300 and the modem take place via the IOS-2SM data bus. The data transfer process is interrupt driven, with timing based on the transmit data clock, for output transfers, and the modem receiver clock, for input transfers. The interrupts will, therefore, occur at the data rate, thereby allowing sufficient time for the IOS-2SM to acknowledge each interrupt and perform the appropriate data transfer. The interrupts are controlled by a two-phase clock such that simultaneous interrupts can never occur. Furthermore, the interrupts are mutually exclusive so that the IOS-2SM can receive only one interrupt at a time. Interrupt number 1 is used for receive data, and interrupt number 2 is used for transmit data.

Upon receiving interrupt number 1, the processor should perform an input data transfer, acknowledging the interrupt after the transfer is complete. The input data word will contain the current modem data sample (least significant bit), RS-449 interface status data (bits 17-22), and a hookswitch status bit (bit 23). The receive interrupts are timed by the modem receive data clock; hence, the maximum response time to input the data and acknowledge the interrupt is approximately one-half of a period at the modem data rate. (The one-half period results from the fact that the processor must also supply transmit data.)

Upon receiving interrupt number 2, the processor should perform an output data transfer, acknowledging the interrupt after the transfer is complete. The output data word must contain the next data bit in the least significant bit position, and the appropriate RS-449 interface control bits in bit positions 17-19. The transmit interrupts are timed by the transmit data clock; hence, the maximum response time to output the data and acknowledge the interrupt is approximately one-half of a

period at the transmit data rate. In order to insure the correct timing of the transmit data at the modem interface, the output data is double buffered. Hence, while the MAP-300 is transferring transmit data bit n, data bit n-1 appears at the modem interface.

The audio interface has been tested according to the attached table. These tests insure that all amplifiers and filters are performing to the designed specifications. The loop tests performed for both the handset and tape audio paths insure that the entire audio interface functions properly with the MAP-300, and that the overall loop gains and frequency response requirements are met.

The real time clock portion of the modified IOS-2SM scroll has been tested by programming several different line and speech sampling rates. A sufficient number of possible rates have been programmed to insure proper functioning of all RTC control bits. The modem interface has been tested by first outputting various control and data bit patterns from the MPA-300, and insuring that the correct bit patterns appear at the modem interface connector. Once the output had been determined to function properly, the modem input was tested by inputting various control and data bit patterns, looping through the MAP-300, and observing the bit patterns at the interface connector. A digital loop test was performed in which the MAP-300 output a digital data stream which was looped back at the modem interface connector and fed back into the processor where the resulting input was compared to the original output.

3.4.2.3 Modifications on Map Analog I/O Hardware

Due to the fact that the Map Analog circuitry is located in a very noisy environment (with the switching power supplies and the digital

circuitry nearby), noise reduction techniques are very important in order to reduce the background noise in the presence of a signal. The modifications listed below were found necessary to reduce the background noise to ± 1 LSB of a 12 bit A/D converter operating at 5V full scale. The noise reduction techniques consisted of the reducing ground loops, separating ground and signal ground, and the reconfiguring the sample and hold amplifier with a differential input to reject common mode noise. It was observed that a good air circulation was required to reduce the terminal drift of the components in the analog circuitry.

List of Modifications on Map Analog I/O Hardware

1. ADAM Board

- a. Added decoupling capacitors between $\pm 15V$ and ground at the A/D, S/H, and MUX
- b. Cut ground etch to eliminate ground loops on analog circuit (around S/A and Hold capacitor)
- c. Added ground from A/D to S/H directly.
- d. Converted S/H to differential input. As a result, signal inputs are on Pin 28 (signal +) and Pin 29 (signal -) instead of Pin 34 and Pin 36. (Note: Only S/H designated as #4 on schematic has been modified.)

2. AOM Board

- a. Cut ground etch from I/O pin 15 and 16 and added ground wire directly from D/A (pin 21) to I/O pin 15 and 16.
- b. D/A cable modified. Wires to connector P4 pin 7 (purple) and pin 8 (black) have been removed to separate I/O drawer ground from AOM board ground.

3.5 System Usage

This section describes the operating instructions for executing the GTE system and for generating it. The examples in this section will use MAP A. Changes required for MAPs B and C are noted when not apparent.

Three steps are necessary for running the ATC system once it has been generated. First, the MAP must be allocated and loaded with the required programs as follows:

```
>ALL MA:
>
>RUN MAID
  OBJECT INPUT?
  BINARY INPUT?GTE.BD
    -- STOP
>
```

Second, the task must be installed as follows:

```
>INS ATCA
>
```

Third, the ATC task must be run, as follows:

```
>RUN ATCA
>
```

```

>>>> GTE 9600 RPS ATC SYSTEM <<<<

START-UP(Y/N)? Y
  (1) MAP OPENED AND BUFFERS DECLARED!
  (2) PRE-BOUND FUNCTIONS CREATED!
  (3) FUNCTION LISTS DEFINED!
  (4) IOS-2 SETUP COMPLETE!
  (5) ADM SETUP COMPLETE!
  (6) ADAM SETUP COMPLETE!

PDP <==> MAP LINKAGE HAS BEEN SUSPENDED
  (DATE AND TIME)
  TYPE "RPS ATCA" TO TERMINATE
```

The file LINE.PBJ must be available for proper execution. The task starts MAP A and then suspends itself. To halt the ATC system, the task must be resumed as follows:

```
RES ATCA
>
ATCA -- STOP
>
```

When resumed, the task closes the MAP and exits.

If a complete, two MAP system is desired, the process above must be repeated for the second MAP.

If the ATC system is to be started again, without additional loading of GTE.B0, the start-up procedure can be avoided by answering "N" to the "Start-up" question, as follows:

```
>RUN ATCA
>
```

>>>>> GTE 9600 RPS ATC SYSTEM <<<<<

START-UP(Y/N)? N

- (1) MAP OPENED AND BUFFERS DECLARED!
- (2) PRE-ROUND FUNCTIONS CREATED!
- (3) FUNCTION LISTS DEFINED!
- (4) IOS-2 SETUP COMPLETE!
- (5) AOM SETUP COMPLETE!
- (6) ADAM SETUP COMPLETE!

PDP <==> MAP LINKAGE HAS BEEN SUSPENDED
(DATE AND TIME)
TYPE "RES ATCA" TO TERMINATE

3.5.1 System Generation

The ATC system generation consists of generating the MAP load module and of generating the ATCA, ATCB, and ATCC tasks.

3.5.1.1 MAP Load Module Generation

The process of generating a MAP load module consists of four steps. For purposes of illustration, the array function VMOV1 will be used in the example to follow.

First, the source file, VMOV1.TXT must be reformatted using PREMAP, as follows:

```
>RUN PREMAP
INPUT FILENAME= VMOV1.TXT
OUTPUT FILENAME= VMOV1.MAP
NORMAL EXIT AFTER MANY LINES!

-- STOP
>
```

VMOV1.MAP is the reformatted source file, acceptable to the MAP assembler. Instructions for building the PREMAP tasks are included in Section 3.5.2.2.

Second, the reformatted source file must be assembled as follows:

```
>RUN MAPASM
SOURCE FILENAME?
VMOV1.MAP
OBJECT FILENAME?
VMOV1.PRG
LISTING OUTPUT?
VMOV1.LST
0:
$1,9999

LINES WITH ERRORS:      0  (MAP VERSION 800101.10)  E=      0
>
```

Third, all of the PBJ modules must be combined into a single PBJ file. This can be accomplished by using the command file GTESYS.CMD as follows:

```
*GTESYS
PIP *AF.PBJ:*/*DE
PIP GTE.PBJ:*/*DE
PIP GTE.PBJ:*/*DE
PIP MAP.PBJ=S300EX.PBJ,APDONE.PBJ,GTE300.PBJ,FF2R.PBJ,IUS.PBJ
PIP GTE.PBJ=MAP.PBJ,MPFDM.PBJ,MPDCTM.PBJ,MPGDPF.PBJ,MPBOPF.PBJ
PIP GTE.PBJ=GTE.PBJ,*PMWGX.PBJ,MPFSTV.PBJ,MPASKI.PBJ,MPSSRT.PBJ
PIP GTE.PBJ=GTE.PBJ,MPFSTQ.PBJ,MPFSTD.PBJ,MPHASP.PBJ,MPSCAN.PBJ
PIP GTE.PBJ=GTE.PBJ,*PCDRA.PBJ,MPIDCR.PBJ,MPVDM.PBJ,GPATCH.PBJ
PIP GTE.PBJ=GTE.PBJ,VMOV1.PBJ,VMOV2.PBJ,VMOV3.PBJ,CSPUIS.PBJ
PIP GTE.PBJ/PI
;
;      USE MACRO DUCK!
;      1<N(CK)FFFF(CK)S=1LSKSTS>SS
;
;      USE MACRO 23 TIMES!
;      23<N(FFFF)S=1LSKSTS>SS
;
;FC GTE.PBJ
*1<N
FFFF
S=1LSKSTS>SS
10*      APDONE      AP PROCESS INTERRUPT HANDLER      MAY 28, 1980
23<N(FFFF)S=1LSKSTS>SS
10*      SNAP-II     MAP-300 ARITH. MODULES - PROG #830101.03 MAY 7, 1980
10:FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS      18 AUG
10*      SNAP-II     IUS PACKAGE ---- JUNE 25, 1979 ---- PROGRAM # 840001.00
10'?FCH 241...      "MPDCTM(Y,U,V,W)"?FORM TRUE DCT AND ITS REFLECTED MAG SQ
10'?FCH 242...      "MPGDPF(Y,U,V)"?QUANTIZE/DEQUANTIZE SIDEBAND
10'?FCH 243...      "MPBOPF(Y,U,V)"?DEQUANTIZE SIDEBAND
10'?FCH 244...      "MPWGX(Y,U,V,W)"?EXTRACT M,PG,MOVE X
10'?FCH 245...      "MPFSTV(U)"?CREATE PITCH AND VOCAL TRACT RESPONSE
10'?FCH 249...      "MPASKI(U)"?SORT DCT1(.) COEFFICIENTS
10'?FCH 247...      "MPSSRT(Y)"?SORT DCT1(.) COEFFICIENTS
10'?FCH 250...      "MPFSTQ(Y,U,V,W)"?QUANTIZE DCT PARAMETERS
10'?FCH 251...      "MPFSTD(Y,U,V,W)"?DEQUANTIZE DCT PARAMETERS
10'?FCH 253...      "MPHASP(Y,U,V,W)"?BASIS SPECTRUM CALCULATION
10'?FCH 254...      "MPSCAN(Y,U,V)"?SCAN DCT BASIS SPECTRUM
10'?FCH 255...      "MPCDRA(Y,U,V)"?COMPLETE DCT BIT ASSIGNMENT
10'?FCH 248...      "MPIDCR(Y,U,V)"?MAKHOUZ IDCT CORRECTION
10'?FCH 252...      "MPVDM(Y,U,V)"?VAR.,DC,INTERPOLATE,FIX
10      *L=S7AID
10'?FCH 237...      "VMOV1(Y)"?MOVE BUNCH OF BUFFERS TO OTHER BUFFERS
10'?FCH 238...      "VMOV2(Y)"?MOVE BUNCH OF BUFFERS TO OTHER BUFFERS
10'?FCH 239...      "VMOV3(Y)"?MOVE BUNCH OF BUFFERS TO OTHER
10'?PROG. CSPUIS.TXT
*
*EXSS
PIP GTE.PBJ/PI
*PI
*CK -- TASK NOT IN SYSTEM
>
```


Note that once the command file has invoked TECO, the user must give the specified TECO commands. The PREMAP and assembly procedures must be repeated for each of the files used in GTESYS.CMD. If the file 5300EX.PBJ is unavailable, it must be obtained from the delivered tape, as assembly is not possible. In addition, the procedure must be performed on LINE.TXT if LINE.PBJ does not exist.

Fourth, the file GTE.PBJ must be transformed by the MAP Loader into a binary object file, as follows:

```
>RUN MAPL
OBJECT INPUT?GTE.PBJ
BINARY OUTPUT?GTE.BO
LOAD MAP? (Y OR N) N
-- STOP 2
>
```

The file GTE.BO is then ready for use.

3.5.1.2 Task Generation

The command files ATCA.CMD and TBATCA.CMD are used to build the ATC task. When taskbuilding the ATC tasks, it is necessary, for reasons of space, to use the GTE-supplied RSX system library, F4PLIB.OLB. The existing system library, SYSLIB.OLB, should be renamed to something else, e.g., OSYSLIB.OLB, and F4PLIB.OLB should be renamed to SYSLIB.OLB. The file ATCA.ODL is used to specify the overlay structure. The contents of ATCA.CMD are:

```
FILE
FILE TBATCA
FILE
```

The contents of TBATCA are:

```
ATCA=ATCA/*P
TVLIS=10
ASG=TI:7
ASG=NI:5
ASG=NY:4
//
```

The contents of ATCA.ODL are:

```
A:      .ROOT A=*(B,C,D,K,H,I,V,F,F,G,J,L,M,O,P,R,S,T,FO,U,VO,Y,Z,ZO,Z2,Z3)
B:      .FCTR MASTER=MSMPA
C:      .FCTR INIT
D:      .FCTR USER
E:      .FCTR MAPON
F:      .FCTR CREATE
G:      .FCTR DEFIDE=10/LB
H:      .FCTR SETUP=(LOAD,IUS4=(O/LB,ADA=10/LB,ADAM=10/LB)
I:      .FCTR SYSTEMA=10/LB
J:      .FCTR INPUT-TAPE2=(OPT1,OPT2,OPT3,OPT4,OPT5,OPT6,OPT7,OPT8A)
K:      .FCTR DCVAR
L:      .FCTR DCTF
M:      .FCTR QOPARM
N:      .FCTR PIFPC
O:      .FCTR VPS
P:      .FCTR BASIS
Q:      .FCTR ASRT
R:      .FCTR SPC1
S:      .FCTR B11A
T:      .FCTR QDCT
U:      .FCTR CHANL
V:      .FCTR LPARM
VO:     .FCTR SSET
Y:      .FCTR DDCT
Z:      .FCTR DCTR
ZO:     .FCTR VARDC
Z2:     .FCTR OUTPUT1-TAPE2=(OPT1,OPT2,OPT3,OPT4,OPT5,OPT6,OPT7,OPT8A)
Z3:     .FCTR SNR
        .END
```

ATCA.ODL requires a particular host support library, NSNPA, which has the unused array functions deleted. The command file to create this library is AGTESN.CMD. Its contents are

```
TIM
;
;      CREATE SEPARATE VERSION OF "SNPLIB.OLB"
;
PIP NSNPA.OLB=SNALB.OLB
;
;      ADD ATC HSP ROUTINES
;
LBR NSNPA/IN=FF2RHSP
LBR NSNPA/IN=MPFDVM,MPDCTM,MPMWGX,MPQDPP,MPFSTV,MPRASP,MPASRT
LBR NSNPA/IN=PPSCAB,PPCDRA,PPFSTG,PPLOPP,PPSSRT,PPFSTG,PPFDCR,MPVDVM
;
;      DELETE UNUSED "COMPLEX FCR" HSP ROUTINES
;
LBR SY:NSNPA/DE:CCVMB:CMINV:CPAL:CPCT:CSOCT:CSMA1:CSMA2:CVABD
LBR SY:NSNPA/DE:CVCAJ:CVMBL:CVRCF:CVSRH:CMMLR:CMMLL
;
;      DELETE UNUSED "ARITHMETIC FCR" HSP ROUTINES
;
LBR SY:NSNPA/DE:SPOT:SMAX:SMIN:SMVAR:SMXAB:SMYAB:SMYSJ:SAAD:SSUB
LBR NSNPA/IN=VMOV1,VMOV2,VMOV3
LBR SY:NSNPA/DE:DCVY:DDIFF:DFL22:DINTG:DPHF
LBR NSNPA/DE:MYLD:SDIV:SMUL:SSUB:VMA1:VTHF
LBR SY:NSNPA/DE:FFLR3:FFLR:FFTL:FFTL:FFTL3:FFLR:FFLR3:FFLR3:FFLR:FFLR
LBR SY:NSNPA/DE:VALGH:VALN:VHIST:VIN:VMAAP:VMAA1:VMAA2:VMAA3
LBR SY:NSNPA/DE:VMAX:VMIN:VMINS:VA:VAH:VAHSQ:VAD
LBR SY:NSNPA/DE:VALOG:VAL2:VCLIP:VCORF:VDIV:VDV:VFIX8:VFETH
LBR SY:NSNPA/DE:VERCT:VETI:VINPT:VITE:VLG16:VLIM:VLOG:VLOGH
LBR SY:NSNPA/DE:VL2:VMAG:VMG:VMGSQ:VMI:VMMAH:VMSQ:VMTHR:VMUL
LBR SY:NSNPA/DE:VMXA:VNEG:VPOLY:VPOW:VRC:VRCP:VS:VSAD:VSB
LBR SY:NSNPA/DE:VSIN1:VSMUL:VSO:VSORT:VSSQ:VLAG:VXP
;
;      DELETE UNUSED "MANAGEMENT FCR" HSP ROUTINES
;
LBR SY:NSNPA/DE:MPCLM:MPCOL:MPNXC:MPNKR:MPROW:MPRSA:MPRSI:MPART
LBR NSNPA/DE:MPCLA:MPCRH:MPFTM:MPFOR:MPTLB
LBR NSNPA/DE:MPCSO:MPAPA:MPCEH:MPFSS:MPWIC:MPRBC:MPILM:MPMUL
LBR NSNPA/DE:MPIAD:MPICS:MPIDV:APIF:MPICL:MPIER:MPIFX
LBR NSNPA/DE:MPIAD:MPIML:MPIRS:MPISS:MPITM:MPITE:MPITS:MPIWL:MPIWS
LBR NSNPA/DE:MPHF:MPHF:MPDHR:MPFRA:MPRBS:MPSCR:MPSRB
LBR NSNPA/DE:MPISH:MPIS:MPABA:MPFVN:MPDD:MPWT
;
;      EXTRACT AS AN OBJECT FOR ROOT SEGMENT
;
LBR SY:NSNPA=NSNPA/FX
PIP NSNPA.OBJ/LI
PIP NSNPA.*/*
TIM
```

The library NSNPA is based on the library SNALB, which contains the GTE-delivered version of FCBGN, as well as SNAPHX, EAFHSP, MRTPCCK, and MAADVR.

If it is necessary to compile the Fortran files, three command files are provided for this purpose. GTEFTN.CMD compiles the host support modules for the GTE-written array functions. Its contents are:

```
F4P VMOV1,LP/LI:1=VMOV1/DE/NOTR
F4P VMOV2,LP/LI:1=VMOV2/DE/NOTR
F4P VMOV3,LP/LI:1=VMOV3/DE/NOTR
F4P MPEDVM,LP/LI:1=MPEDVM/DE/NOTR
F4P MPEDTM,LP/LI:1=MPEDTM/DE/NOTR
F4P MPEDPP,LP/LI:1=MPEDPP/DE/NOTR
F4P MPEDPP,LP/LI:1=MPEDPP/DE/NOTR
F4P MPV*GX,LP/LI:1=MPV*GX/DE/NOTR
F4P MPFSTV,LP/LI:1=MPFSTV/DE/NOTR
F4P MPSSRT,LP/LI:1=MPSSRT/DE/NOTR
F4P MPIDCM,LP/LI:1=MPIDCM/DE/NOTR
F4P MPASK1,LP/LI:1=MPASK1/DE/NOTR
F4P MPFSTU,LP/LI:1=MPFSTU/DE/NOTR
F4P MPFSTD,LP/LI:1=MPFSTD/DE/NOTR
F4P MPVDN*,LP/LI:1=MPVDN*/DE/NOTR
F4P MPBASP,LP/LI:1=MPBASP/DE/NOTR
F4P MPSCAN,LP/LI:1=MPSCAN/DE/NOTR
F4P MPCDBA,LP/LI:1=MPCDBA/DE/NOTR
```

CFTN.CMD compiles most of the modules in the Fortran control program.

The delivered version is incorrect and must be modified to correspond to the following:

```
F4P MASTER=MASTER.AFC/NOTR
F4P INIT=INIT/DE/NOTR
F4P USER=USER/DE/NOTR
F4P MAPON=MAPON/DE/NOTR
F4P CREATE=CREATE/DE/NOTR
F4P DEFINE=DEFINE/DE/NOTR
F4P SETUP=SETUP/DE/NOTR
F4P LOAD=LOAD/NOTR
F4P IUS*=IUS*/NOTR
F4P AQM=AQM/NOTR
F4P ADAM=ADAM/NOTR
F4P SYSTEM=SYSTEM/DE/NOTR
F4P SYSTM=SYSTM/DE/NOTR
F4P SYSTM=SYSTM/DE/NOTR
F4P INPUT=INPUT/DE/NOTR
F4P DCVAR=DCVAR/DE/NOTR
F4P DCTF=DCTF/DE/NOTR
F4P PTLPC=PTLPC/DE/NOTR
F4P QDPARM=QDPARM/DE/NOTR
F4P VPBS=VPBS/DE/NOTR
F4P BASIS=BASIS/DE/NOTR
F4P ASPT=ASPT/DE/NOTR
F4P SDCI=SDCI/DE/NOTR
F4P BITA=BITA/DE/NOTR
F4P QDCT=QDCT/DE/NOTR
F4P CHANI=CHANI/DE/NOTR
F4P DPARM=DPARM/DE/NOTR
F4P SSRT=SSRT/DE/NOTR
F4P DDCT=DDCT/DE/NOTR
F4P DCTR=DCTR/DE/NOTR
F4P VANDC=VANDC/DE/NOTR
F4P OUTPUT=OUTPUT/DE/NOTR
F4P SNR=SNR/DE/NOTR
```

The command file TAPFTN.CMD compiles the remaining files needed by the Fortran control program. Its contents must be modified to conform to the following:

```
F4P TAPE2=TAPE2/NOTR
F4P OPT1=OPT1/NOTR
F4P OPT2=OPT2/NOTR
F4P OPT3=OPT3/NOTR
F4P OPT4=OPT4/NOTR
F4P OPT5=OPT5/NOTR
F4P OPT6=OPT6/NOTR
F4P OPT7=OPT7/NOTR
F4P OPT8=OPT8/NOTR
F4P OPT8A=OPT8A/NOTR
F4P OPT8B=OPT8B/NOTR
F4P OPT8C=OPT8C/NOTR
```

A command file for generating PREMAP is also provided. The contents of PREMAP.CMD are:

```
FOR SY:PREMAP,LP/LI:1=SY:PREMAP
TKR SY:PREMAP,LP=SY:PREMAP,FIDEN
PIP PREMAP.*//PI
```

3.6 System Listings

The ATC system listings which follow are divided into three sections: MAP Functions, Executive Programs, and Fortran Host Support, Control, and Support Programs.

3.6.1 MAP Functions

The modules contained in this section are:

VMOV1
VMOV2
VMOV3
MPFDVM
MPDCTM
MPQDPP
MPDQPP
MPMWGX
MPFSTV
MPSSRT
MPIDCM
MPASRT
MPFSTQ
MPFSTD
MPVDNM
GPATCH
MPBASP
MPSCAN
MPCDBA
LINE


```
A1F 063FA 3C500000 (00104) LOAD(RW1,101)
A1F 063F0 3F500000 (00105) LOAD(RW1,MSS)
A20 063F2 40500000 (00106) LOAD(RW1,MSS)
A21 063F4 42C2A000 (00107) LOAD(RW0,AUPHS(1),TF)
A22 063F6 44500005 (00108) LOAD(RW1,5)
A23 063F8 46A00002 (00109) 01 ADD(RW0,2,TF)
A24 063FA 48112301 (00110) SURJ(RW1,1),JUMPP(01)
A25 063FC 4AC2A00F (00111) LOAD(RW0,AOTDCTS(1),TF)
A26 063FE 4C50007F (00112) LOAD(RW1,126)
A27 06400 4F8A0002 (00113) 02 ADD(RW0,2,TF)
A28 06402 50112701 (00114) SURJ(RW1,1),JUMPP(02)
A29 06404 52C2A00F (00115) LOAD(RW0,ATHIT4S(1),TF)
A2A 06406 5450007F (00116) LOAD(RW1,126)
A2B 06408 56A00002 (00117) 04 ADD(RW0,2,TF)
A2C 0640A 58112701 (00118) SURJ(RW1,1),JUMPP(04)
A2D 0640C 5AC40C34 (00119) LOAD(RW0,MINMS(2),TF)
A2E 0640E 5C500079 (00120) LOAD(RW1,121)
A2F 06410 5F8A0002 (00121) 06 ADD(RW0,2,TF)
A30 06412 60112701 (00122) SURJ(RW1,1),JUMPP(06)
A31 06414 62C42020 (00123) LOAD(RW0,OTDCTS(2),TF)
A32 06416 6450007F (00124) LOAD(RW1,126)
A33 06418 66A00002 (00125) 03 ADD(RW0,2,TF)
A34 0641A 68112701 (00126) SURJ(RW1,1),JUMPP(03)
A35 0641C 6AC42020 (00127) LOAD(RW0,MIRIT4S(2),TF)
A36 0641E 6C50007F (00128) LOAD(RW1,126)
A37 06420 6F8A0002 (00129) 05 ADD(RW0,2,TF)
A38 06422 70112701 (00130) SURJ(RW1,1),JUMPP(05)
      (00131) *
      (00132) *
A39 06424 73420575 (00133) 1MPS0 LOAD(RW0,15AS115(1),TF)
      (00134) *
A3A 06426 74200030 (00135) DNFS0 CLEAR(R0)
A3B 06428 76000020 (00136) 0MPS0 MUP(0)
      000043FF (00137) VMVISA=BC
      0642A (00138) END BA=1
      (00139) *STOPPAGE HLOCK FOR CONSTRUCTED INSTRUCTION
      0642A 00000000 (00140) VMV1$ DATA 1F'0,0'
      0000007A (00141) VMVISZ=01-VMV115
      0642C (00142) END
```

DUMMY FOR EXFC
DUMMY

AOPH OUT

AOTDCT OUT

AIRIT4 OUT

12102 HALFWORDS

NINM OUT

OTDCT1 OUT

MIRIT4 OUT

SET XGD

PAGE 5: LCM 237... "VM001(Y)" MOVE RUNCH OF BUFFERS TO OTHER BUFFERS

AFDTSDPG:	00000	(00014)	(00037)
AMIT4S:	00000	(00031)	(00115)
AOPRS:	00000	(00029)	(00107)
ADTCTIS:	00000	(00040)	(00111)
CSPIUSNUS:	02100	(00015)	(00040)
DMS:	00704	(00016)	
DMSI:	00010	(00100)	
DMSD:	00030	(00135)	
IMPSI:	00010	(00098)	
IMPSD:	00030	(00133)	
IMPS:	00000	(00028)	(00086)
ISAS001:	00503	(00020)	(00098)
ISAS106:	00506	(00021)	
ISAS115:	00575	(00022)	(00133)
ISVTS:	00502	(00019)	(00020)
ISS:	00000	(00018)	(00105)
MINITIS:	02028	(00027)	(00094)
MINIT4S:	02028	(00026)	(00082)
MINPS:	00030	(00032)	(00119)
OPRMS:	02028	(00023)	(00073)
OTDCTIS:	02028	(00024)	(00077)
OTDCTMS:	00030	(00025)	(00090)
SCIPS:	00001	(00064)	(00072)
START:	06300	(00034)	(00042)
VMVIS:	06300	(00038)	(00048)
VMVISA:	06300	(00067)	(00137)
VMVISI:	06470	(00063)	(00140)
VMVISA:	00000	(00046)	(00050)
VMVISA:	00000	(00047)	(00057)
VMVISA:	00070	(00066)	(00141)
VMVISA:	06302	(00039)	(00064)
VMVISA:	00010	(00071)	(00103)

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) E- 0

ORIGINATED: 13-MAY-80
 UPDATED: 20-JUL-80

```

(00001) * FCH 238... "VMOV2(Y)" MOVE RUNCH OF BUFFERS TO OTHER BUFFERS
(00002) *
(00003) *
(00004) *
(00005) * MOVE FROM TO
(00006) * ARQPR ORPRM
(00007) * ARQCTM ORQCTM
(00008) * MIRTH1 MIRTH2
(00009) * IORPR1 IORPR2
(00010) * DCTT1 DCTT2
(00011) * VOUT OUTR
(00012) ** AND SCALAR PAIRS:
(00013) * VAR,DC
(00014) *
(00015) * DEFINE GLOBAL SYMBOLS
(00016) * AFDSOPG=SEHER
(00017) * CSPUSNOS=521FC
(00018) * DMS=5794
(00019) * ISVTS=5502
(00020) * ISAS001=ISVTS+D'1'
(00021) * ISAS104=ISVTS+D'104'
(00022) * ISAS111=ISVTS+D'111'
(00023) * ISAS114=ISVTS+D'114'
(00024) * ISAS116=ISVTS+D'116'
(00025) * M=3
(00026) * MSS=0
(00027) * SVTS=80382
(00028) * SAS100=SVTS+2*D'100'
(00029) * SAS101=SVTS+2*D'101'
(00030) * SAS102=SVTS+2*D'102'
(00031) * SAS103=SVTS+2*D'103'
(00032) * ARQPRMS=38582
(00033) * ARQCTMS=38596
(00034) * MIRTH1S=10536
(00035) * IORPRMS=10024
(00036) * DCTT1S=9000
(00037) * NOUTS=3862
(00038) * ORPRMS=11830
(00039) * ORQCTMS=11844
(00040) * MIRTH2S=10792
(00041) * IORPR2S=10780
(00042) * DCTT2S=9412
(00043) * OUTRS=37456
(00044) *
    
```

PAGE 2: PCN 23R...VMUV2(Y)" MOVE MUNCH OF MUFFERS TO OTHER MUFFERS

000006450 (00045) START=66450
 (00046) *
00000007C (00047) * EXPAND ARRAY FUNCTION DISPATCH TABLE
 (00048) #1=AFDISUPG+3*2*(23R-12R)
0007C 001F6452 (00049) ADDR VMV2S(R7,1)
0007F 001F6464 (00050) ADDR VMV2IS(R7,1)
00080 001021FC (00051) ADDR CSPUSNUS(1,0)
 (00052) FUNCT

PCN 23R

PAGE 3: FOR 23H... "VMCV2(V)" NINE HUNDRED OTHER RUFFERS

[illegible]

CLEAR RG02

A20	064A4	40300032	(00114) *	OUTPUT PCM		
A21	064A6	42500000	(00115)	VMV20S	SET(PA)	DUMMY OP FOR EXEC
A22	064A8	44500000	(00116)		LOAD(RW1,101)	
A23	064AA	46500000	(00117)		LOAD(RW1,MSS)	
A24	064AC	48420576	(00118)		LOAD(RW1,MSS)	
A25	064AE	4AC42F36	(00119)		LOAD(RW0,ISAS116(1),TF)	CLEAR HG02
A26	064B0	4C500005	(00120)		LOAD(RW0,ORPRMS(2),TF)	
A27	064B2	4E4A0007	(00121)		LOAD(RW1,5)	
A28	064B4	50112741	(00122)		ADD(RW0,2,TF)	
A29	064B6	52C42E44	(00123)		SURL(RW1,1),JUMPP(#1)	ADPR OUT
A2A	064B8	5450007F	(00124)		LOAD(RW0,ORDCTMS(2),TF)	
A2B	064BA	564A0002	(00125)		LOAD(RW1,126)	
A2C	064BC	58112841	(00126)		ADD(RW0,2,TF)	
			(00127)		SURL(RW1,1),JUMPP(#2)	ADTCT OUT
			(00128) *			
			(00129) *			
A2D	064BE	5AC42A2H	(00130)		LOAD(RW0,MHIT2S(2),TF)	
A2E	064C0	5C50007F	(00131)		LOAD(RW1,126)	
A2F	064C2	5E4A0007	(00132)		ADD(RW0,2,TF)	
A30	064C4	60112F41	(00133)		SURL(RW1,1),JUMPP(#3)	QDCT1 OUT
A31	064C6	62C42A2H	(00134)		LOAD(RW0,INDR2S(2),TF)	
A32	064C8	6450007F	(00135)		LOAD(RW1,126)	
A33	064CA	664A0002	(00136)		ADD(RW0,2,TF)	
A34	064CC	68113841	(00137)		SURL(RW1,1),JUMPP(#4)	AIRIT4 OUT
A35	064CE	6AC42A2H	(00138)		LOAD(RW0,DCIT2S(2),TF)	
A36	064D0	6C50007F	(00139)		LOAD(RW1,254)	
A37	064D2	6E4A0007	(00140)		ADD(RW0,2,TF)	
A38	064D4	70113741	(00141)		SURL(RW1,1),JUMPP(#5)	MIRIT4 OUT
A39	064D6	72C29444	(00142)		LOAD(RW0,INTMS(1),TF)	123*2 HALFWARDS
A3A	064D8	74500079	(00143)		LOAD(RW1,121)	
A3B	064DA	764A0002	(00144)		ADD(RW0,2,TF)	
A3C	064DC	78113841	(00145)		SURL(RW1,1),JUMPP(#6)	WINM OUT
A3D	064DE	7AC2044A	(00146)		LOAD(RW0,SAS100(1),TF)	
A3E	064E0	7CC2044C	(00147)		LOAD(RW0,SAS101(1),TF)	
A3F	064E2	7F420574	(00148)		LOAD(RW0,ISAS114(1),TF)	SET RGO
A40	064E4	81420571	(00149)		LOAD(RW0,ISAS111(1),TF)	SET OUTFUL
			(00150) *			
A41	064F6	82200030	(00151)	DNFSH	CLEAR(R0)	
A42	064F8	84000020	(00152)		NUP(0)	
	064FA	000064A6	(00153)		VMV2SA=BC	
			(00154)		END BA-1	
			(00155)	*STOPAGE	BLICK FOR CONSTRUCTED INSTRUCTION	
	064FA	00000000	(00156)	VMV2S1	DATA 1F'0.0'	
		00000008	(00157)		VMV2S2=8L-VMV21S	

PAGE 63 PCW 23H...VMUV2(V)* MOVE HUNCH UP RUFFERS TO OTHER RUFFERS

064EC (00158) END

AFVTSUM:	000F4	(00016)	(00048)
ARUNCTS:	096C4	(00033)	(00087)
AROPHS:	096F6	(00032)	(00083)
CSPHMS:	021FC	(00017)	(00051)
DCITIS:	02328	(00036)	(00099)
DCITTS:	02528	(00042)	(00138)
DWYS:	00794	(00018)	
DMPSA:	00003	(00065)	
DMFST:	0001F	(00112)	
DMFSTC:	60043	(00153)	
INRWKIS:	02724	(00035)	(00095)
INRWKTS:	02824	(00041)	(00134)
ISAS001:	00503	(00020)	(00109)
ISAS104:	0056A	(00021)	
ISAS111:	00571	(00022)	(00149)
ISAS114:	00574	(00023)	(00148)
ISAS116:	00576	(00024)	(00119)
ISVTS:	00502	(00019)	(00020)
IMSS:	00000	(00026)	(00117)
MINITIS:	02428	(00034)	(00091)
MLVITTS:	02A28	(00040)	(00130)
MMUTS:	00F16	(00037)	(00103)
MMUTHS:	09414	(00043)	(00142)
ORPCTMS:	02F44	(00039)	(00124)
ORPRMS:	02F36	(00038)	(00120)
SAS100:	0044A	(00028)	(00146)
SAS101:	0044C	(00029)	(00147)
SAS102:	0041F	(00030)	(00107)
SAS103:	00450	(00031)	(00108)
SCIMS:	00001	(00074)	(00082)
START:	06450	(00045)	(00053)
SVTS:	00382	(00027)	(00028)
VMV2S:	06452	(00049)	(00059)
VMV2SA:	064A6	(00077)	(00153)
VMV2ST:	064FA	(00073)	(00156)
VMV2SSA:	00000	(00057)	(00061)
VMV2SS7:	00005	(00058)	(00067)
VMV2S7:	00084	(00076)	(00157)
VMV2IS:	06464	(00050)	(00074)
VMV2US:	00020	(00081)	(00115)
		(00029)	(00030)
		(00022)	(00023)
		(00118)	(00024)

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) P= 0

MOVE RUNCH OF HUFFERS TO OTHER
HUFFERS.
ORIGINATED: 13-MAY-80
UPDATED: 01-JUL-80

(00001) *	FCH 239,"VMHVV3(Y)"	MOVE RUNCH OF HUFFERS TO OTHER
(00002) *		
(00003) *		
(00004) *		
(00005) *		
(00006) *	MOVE FROM	TO
(00007) *	MIRITI	MIRITI
(00008) *	TMPI	ACTITI
(00009) *	TMPI	TMPI
(00010) *	TMPI	TMPI
(00011) *	AND SCALAR PAIRS:	
(00012) *	MMAR,NDC	VAR,DC
(00013) *	DEFINE GLOBAL SYMBOLS	
(00014) *	AFDTSURG=SHPR	
(00015) *	CSPUSMUS=S21FC	
(00016) *	MVS=8794	
(00017) *	M=1	
(00018) *	MSS=0	
(00019) *	SVTS=S0182	
(00020) *	SASHR=SVTS+2*D'RR'	
(00021) *	SASHR=SVTS+2*D'RR'	
(00022) *	SAS102=SVTS+2*D'102'	
(00023) *	SAS103=SVTS+2*D'103'	
(00024) *	ISVTS=S0502	
(00025) *	ISAS1=ISVTS+D'1'	
(00026) *	ISAS116=ISVTS+D'116'	
(00027) *	MIRITS=20RR	
(00028) *	TMPI4S=1072	
(00029) *	TMPI4S=0	
(00030) *	MIRITS=19590	
(00031) *	ACTITIS=9000	
(00032) *	TMPI4S=10024	
(00033) *	MIRITIS=10536	
(00034) *		
(00035) *	START=S6500	
(00036) *		
(00037) *	EXPAND ARRAY FUNCTION DISPATCH TABLE	
(00038) *	BI=AFDTSURG+3*2*(239-12R)	
(00039) *	ANDR VMV3S(47,1)	
(00040) *	ANDR VMV3S(47,1)	
(00041) *	ANDR CSPUSMUS(1,0)	
(00042) *	EJECT	

UNITY
RG02

FCR 239

PAGE: 2:

A00	06502	00000000	0000433
A01	06504	00000000	0000441
A02	06506	00000000	0000445
A03	06508	00000000	0000446
A04	06510	00000000	0000471
A05	06512	00000000	0000480
A06	06514	00000000	0000483
A07	06516	00000000	0000491
A08	06518	00000000	0000501
A09	06520	00000000	0000511
A10	06522	00000000	0000521
A11	06524	00000000	0000531
A12	06526	00000000	0000541
A13	06528	00000000	0000551
A14	06530	00000000	0000561
A15	06532	00000000	0000571
A16	06534	00000000	0000581
A17	06536	00000000	0000591

PAGE 4: FCN 230,"VMV3(Y)" MOVE NUNCH OF BUFFERS TO OTHER

```

A20 06556 40C4272H (00104) LOAD(HW0,INDRIS(2),TF)
A21 06558 4250007F (00105) LOAD(HW1,126)
A22 0655A 44RA0002 (00106) #3 ADD(HW0,2,TF)
A23 0655C 461122H1 (00107) SUM(HW1,1),JUMP(#3)
A24 0655E 48C2044F (00108) LOAD(HW0,SAS102(1),TF)
A25 06560 4AC20450 (00109) LOAD(HW0,SAS103(1),TF)
A26 06562 40420576 (00110) LOAD(HW0,ISAS11A(1),TF)
      (00111) ;
A27 06564 4F200030 (00112) CLEAR(R0)
A28 06566 50000020 (00113) NOP(0)
      00006540 (00114) VMV3SA=RC
      0656H (00115) END #A=1
      *STORAGE HIJCK FOR CONSTRUCTED INSTRUCTION
      0656H 00000000 (00116) VMV3ST DATA 1F'0,0'
      00000054 (00117) VMV3SZ=#1-VMV3IS
      0656A (00118) END
      (00119)

```

INDRIS OUT

SET RG02 TO LEFT CSPH CON
TIME

AFDTSRG: 00000 (00014) (00038)
 CSPUSMUS: 02100 (00015) (00041)
 OCT1118: 02100 (00015) (00041)
 DMS: 00704 (00016)
 THDRIS: 02700 (00032) (00104)
 THDRMS: 00000 (00029) (00082)
 ISAS1: 00503 (00025) (00088)
 ISAS1116: 00576 (00026) (00110)
 LSVTS: 00502 (00024) (00025) (00026)
 ASS: 00000 (00014) (00094) (00095)
 MINITS: 00020 (00027)
 MINTIS: 02020 (00033) (00073)
 RINTIS: 04000 (00030) (00096)
 SAS102: 00400 (00022) (00108)
 SAS103: 00400 (00023) (00109)
 SASR: 00432 (00020) (00086)
 SASRQ: 00434 (00021) (00087)
 SCIPS: 00001 (00064) (00072)
 START: 00500 (00035) (00043)
 SVTS: 00302 (00019) (00020) (00021) (00022) (00023)
 TPF48: 00000 (00020) (00078)
 VMV18: 00502 (00039) (00049)
 VMV18A: 00540 (00067) (00114)
 VMV181: 00560 (00063) (00117)
 VMV185A: 00000 (00047) (00051) (00057)
 VMV185S: 00006 (00048) (00057) (00058)
 VMV18Z: 00054 (00066) (00118)
 VMV18S: 00516 (00040) (00064) (00070) (00118)
 VMV18S: 00014 (00071) (00092)

PAGE 1: FCH 240... "MPFVDM(Y,U,V)" FLOAT,DCRIAS,VAR,REFLECT INPUT VIA MAKHOUT, ALG.

(00001) * FCH 240... "MPFVDM(Y,U,V)" FLOAT,DCRIAS,VAR,REFLECT INPUT VIA MAKHOUT, ALG.
(00002) *
(00003) *
(00004) * DEFINE GLOBAL SYMBOLS
(00005) *
(00006) *
(00007) *
(00008) *
(00009) *
(00010) *
(00011) *
(00012) *
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(00032) *
(00033) *
(00034) *
(00035) *

MPFVDM(Y,U,V) = (17 * L.S. 14) + (12 * L.S. 5) + S1R
DCRIAS = S1R
REFLECT = S1R
INPUT VIA MAKHOUT, ALG.

EXPAND ARRAY FUNCTION DISPATCH TABLE
1. = AFDTSUNG + 1 * 2 * (240 - 128)
ADDR FDS(CH,1)
ADDR G200S(CH,1)
ADDR CSPUSMUS(,1,0)
EJECT

IN FULL

FCH 240

```

(00036) *
(00037) *
(00038) *
(00039) *
(00040) *
(00041) * SPECIAL CONSTANTS
(00042) *
(00043) * ILUGS1 DATA S4000
(00044) * DATA S0042
(00045) * DATA S2000
(00046) * DATA S0042
(00047) * DATA S0A77
(00048) * DATA S6HC0
(00049) * DATA S0Q49
(00050) * DATA S5742
(00051) * DATA S000F
(00052) * DATA S5RC1
(00053) * DATA S0R00
(00054) * DATA S0045
(00055) * DATA S1F85
(00056) * DATA S5541
(00057) * DATA S200F
(00058) * DATA SAH41
(00059) * DATA SDFCC
(00060) * DATA SRF41
(00061) * DATA SRHD9
(00062) * DATA S00C1
(00063) * DATA S0ARF
(00064) * DATA S57C2
(00065) * DATA SRC07
(00066) * DATA S1RC2
(00067) *
(00068) * ISORS1
(00069) * DATA S4000
(00070) * DATA S0040
(00071) * DATA S0H00
(00072) * DATA S0048
(00073) * DATA S1000
(00074) * DATA S0041
(00075) * PJECT

```

128
64
C8
C3
C2
16**4
C7
C1
C6
C0
C5
C4
0.5
16**7
2.0

```

(00075) *
(00076) *
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(00083) *
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(00112) *
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(00114) *
(00115) *
(00116) *
(00117) *
(00118) *

FVFN
DATA FVSSA
DATA FVSSZ
BEGIN APH(FDV)
AA=0
NOP
MOVZF(F0,A0)\NOP
INCLUDE UPDATE SAMPLES IN SUM, THEN FLOAT NEW SAMPLES, OUTPUT,
AND INCLUDE IN SUM
MOVZF(F0,A0)
R(A0)
MOV(IQA,M7)
MOV(IQA,M6)
MOV(IQA,A1)\MOV(H,A0)
MOV(H,A0)\MOV(IQA,A1)
ADD(A0,A1)
JUMP(FUSM1,F+1)

CLEAR(M1)
MOVZF(F0,A1)
JUMP(FISM2)
MOV(P,A1)
MOV(P,M2)
MOV(IQA,A2)\MOV(H,A0)
MOV(H,A0)\MOV(IQA,A2)
MOV(MA2)
MOV(M0),ADD(A0,A1)

MUL(M0,M7)
JUMP(FITSM,F+1)
MOV(P,A1)
MOV(P,M0)
MOV(A0),ADD(A0,A1)
CLEAR(M1)

MOV(H,A0)\MOV(H,F+0)
MOV(F+1,A1)\NOP
ADD(A0,A1)\NOP
MOV(H,M0)
MUL(M0,M6)
MOV(P,M0)\NOP

A00 0R670 00000000
A01 0R672 0R100000
A02 0R674 0R100010
A03 0R676 0R000200
A04 0R678 0R000000
A05 0R67A 0R000000
A06 0R67C 0R000000
A07 0R67E 0R000000
A08 0R680 0R000000
A09 0R682 0R000000
A0A 0R684 20372037
A0B 0R686 0R100011
A0C 0R688 10000000
A0D 0R68A 0R000000
A0E 0R68C 0R000000
A0F 0R68E 0R000000
A10 0R690 0R000000
A11 0R692 32403240
A12 0R694 41004100
A13 0R696 0R000000
A14 0R698 0R000000
A15 0R69A 0R000000
A16 0R69C 0R000000
A17 0R69E 41004110
A18 0R6A0 20372037
A19 0R6A2 0R000000
A1A 0R6A4 0R000000
A1B 0R6A6 41000000
A1C 0R6A8 0R000000
A1D 0R6AA 0R000000
A1E 0R6AC 0R000000

START ADDRESS
MODULE SIZE

<<<<<FOR TIMING
<<<<<CALLING CARD=0
SUM
SA = 2*15
SH = 1/LTH

INCLUDE UPDATE SAMPLES 1
N SUM
FAKE FOR FIRST ADD
AVIDD FAKE OUTPUTS

X\SUM
SUMX
FLOAT X
FLOAT X,SUM OF X FROM LA
ST TIME THRU
FLOAT X*2*15

ADD LAST TERM INTO SUM
GIVE APS LOTS OF TIME FO
R NEXT INPUTS
LEFT SUM+RIGHT SUM
RIGHT SUM
LEFT SUM + RIGHT SUM
SUM
SUM*1/LTH
DCRIAS

```

```

A1F 0R65F 0R640000 (00119)      MOV(P,F0)\NOP
A20 0R660 0R670R57 (00120)      MOV(P,A7)\MOV(FX1,A7)
      (00121) *
A21 0R662 0R120R12 (00122)      *X'=X-DCHIAS,SUM(X')-2 FOR VARIANCE:
A22 0R664 0R200R20 (00123)      MOV(ZERO,A7)
A23 0R666 0R200R25 (00124)      P(A7)
A24 0R668 0R200R25 (00125)      .JUMP(SQ2)
A25 0R66A 0R110R00 (00126)      MOV(P,A7)
A26 0R66C 0R200R21 (00127)      MOV(T0A,A1)\MOV(R,A0)
A27 0R66E 4F204F20 (00128)      MOV(R,A0)\MOV(T0A,A1)
A28 0R670 0R640R68 (00129)      SUB(A1,A7)
A29 0R672 0R640R68 (00130)      MOV(R,M0)
A30 0R674 0R640R68 (00131)      MOV(R,M4)
A31 0R676 421C421C (00132)      MOV(M0,M4)
A32 0R678 0R160R24 (00133)      MOV(M0,M4)
A33 0R67A 0R200R20 (00134)      *
A34 0R67C 0R200R20 (00135)      .JUMP(SQ1,F#1)
A35 0R67E 42004200 (00136)      MOV(P,A2)
A36 0R680 0R200R20 (00137)      MOV(R,A0)
A37 0R682 0R200R20 (00138)      ADD(A0,A2)
A38 0R684 0R200R20 (00139)      MOV(R,A0)\MOV(R,EX0)
A39 0R686 0R200R20 (00140)      MOV(FX1,A2)\NOP
A40 0R688 0R200R20 (00141)      ADD(A0,A2)\NOP
A41 0R68A 0R200R20 (00142)      MOV(R,M0)\NOP
A42 0R68C 0R200R20 (00143)      MOV(M0,M6)\NOP
A43 0R68E 0R200R20 (00144)      MOV(P,M0)\NOP
A44 0R690 0R200R20 (00145)      .JUMP(SQ1,00F) #1
A45 0R692 0R200R20 (00146)      *
A46 0R694 0R200R20 (00147)      CLAP(F#1)
A47 0R696 0R200R20 (00148)      NOP
A48 0R698 0R200R20 (00149)      *
A49 0R69A 0R200R20 (00150)      * VAR=SQRT(SD)
A50 0R69C 0R200R20 (00151)      SSORTS
A51 0R69E 0R200R20 (00152)      MOV(T0A,A7)\NOP
A52 0R6A0 0R200R20 (00153)      MOV(T0A,M7)\NOP
A53 0R6A2 0R200R20 (00154)      MOV(T0A,A6)\NOP
A54 0R6A4 0R200R20 (00155)      MOV(T0A,A5)\NOP
A55 0R6A6 0R200R20 (00156)      MOV(T0A,A2)\NOP
A56 0R6A8 0R200R20 (00157)      MOV(T0A,A0)\NOP
A57 0R6AA 0R200R20 (00158)      ARS(A2)\NOP
A58 0R6AC 0R200R20 (00159)      MOV(M0),ARS(A0)\NOP
A59 0R6AE 0R200R20 (00160)      MOV(R,A0)\NOP
A60 0R6B0 0R200R20 (00161)      SUB(A0,A7)\NOP
A61 0R6B2 0R200R20 (00162)      MOV(R,M2)\NOP
A62 0R6B4 0R200R20 (00163)      MOV(M2,M7)\NOP
A63 0R6B6 0R200R20 (00164)      *
A64 0R6B8 0R200R20 (00165)      *

```

DCHIAS

ZERO SUM
GET 0 IN R
SKIP FAK PRODUCTS
XSUM,ZERO FIRST TIME
SUM,ZERO FIRST TIME
X-RIAS
PREPARE TO SQUARE
SQUARE
OUTPUT X-RIAS,ADD IN SDI
ARE FROM LAST TIME

LAST SQUARE
SUM FROM LOOP
ADD IN LAST SQUARE
LEFT SUM/RIGHT SUM
RIGHT SUM
LEFT SUM + RIGHT SUM

SUM/LTH
VAR-2=SD
WAIT FOR SD TO BE WRITTE
N
THEN LET INPUT GO

A7=\$40000040
M7=0.5
A6=16**7
A5=2.0
A2=X(0,K)
A0=X(1,K)
A0=X(0,K)
M0=ARS(X(0,K)),X(2,K)
A0=X(2,K)
X(3,K)
M2=X(3,K)
X(4,K)

PAGE	S:	PCB 740...	"MPFVW(Y,U,V)"	VL0ATV,DCLIAS,VAP,REFLECT	INPUT VIA	MAKHHML, ALG.
A45	086AA	02000000	(00163)			X(5)
A46	086AC	08400000	(00164)			A0=X(4,K)
A47	086AE	47140000	(00165)			A4=X(5),X(6,K)
A48	086AH	11100000	(00166)			A0=X(6,K),X(7,K)
A49	086AJ	084A0000	(00167)			M2=X(7,K)
A4A	086AK	08400000	(00168)			A0=X(7,K)
A4B	086AL	21000000	(00169)			X(8,K)
A4C	086AM	084C0000	(00170)			M4=X(8,K)
A4D	086AN	45000000	(00171)			X(9,K)
A4E	086AP	08420000	(00172)			A2=X(9,K)
A4F	086AR	44A00000	(00173)			X(10,K)
A50	086AC	084A0000	(00174)			M2=X(10,K)
A51	086C2	85000000	(00175)			X(11,K)
A52	086C4	02000000	(00176)			GET X(7,K)
A53	086C6	084C0000	(00177)			M4=X(11,K)
A54	086C8	084A0000	(00178)			M2=X(7,K)
A55	086CA	45000000	(00179)			X(12,K)
A56	086CC	08420000	(00180)			A2=X(12,K)
A57	086CF	44A00000	(00181)			X(13,K)
A58	086D0	084A0000	(00182)			M2=X(13,K)
A59	086D2	85000000	(00183)			X(14,K)
A5A	086D4	084C0000	(00184)			M4=X(14,K)
A5B	086D6	44000000	(00185)			X(15,K)
A5C	086D8	08420000	(00186)			A2=X(15,K)
A5D	086DA	42000000	(00187)			X(16,K)
A5E	086DC	084A0000	(00188)			M2=X(16,K)
A5F	086DE	85600000	(00189)			X(17,K)
A60	086E0	2A000000	(00190)			X(5)
A61	086E2	08400000	(00191)			A0=X(7,K)
A62	086E4	084A0000	(00192)			M2=X(7,K)
A63	086E6	08400000	(00193)			A4=X(5)
A64	086E8	901F004H	(00194)			DOVE YET?
A65	086EA	02000000	(00195)			YES!
A66	086EC	084C0000	(00196)			VAR=SORT(SD)
A67	086EE	20372037	(00197)			RELEASE APS INPUT
A68	086F0	084F0000	(00198)			
A69	086F2	08400000	(00201)			M4=SC=VAR
A6A	086F4	16A016A0	(00202)			A0=SC=VAR
A6B	086F6	08400000	(00203)			R=2.0;R=2.0
A6C	086F8	21000000	(00204)			A6=2
A6D	086FA	084A0000	(00205)			R1=1/C+DEL(=F0)
A6E	086FC	84400000	(00206)			M0=F0
						P1=F0*0

FLOAT,DCIAS,VAR,REFLECT INPUT VIA MAXIMUM. ALC.

[illegible]

```

A96 0R74C 04740000 (002511)
A97 0R74F 43510000 (00252)
A98 0R750 04500000 (00253)
A99 0R752 04500000 (00254)
A9A 0R754 44200000 (00255)
A9B 0R756 85920000 (00256)
A9C 0R758 40310000 (00258)
A9D 0R75A 85330000 (00259)
A9E 0R75C 43110000 (00260)
A9F 0R75E 04500000 (00261)
A90 0R760 04500000 (00262)
A91 0R762 85900000 (00263)
A92 0R764 44300000 (00264)
A93 0R766 45000000 (00265)
A94 0R768 04520000 (00266)
A95 0R76A 84330000 (00267)
A96 0R76C 43500000 (00268)
A97 0R76E 04500000 (00269)
A98 0R770 04500000 (00270)
A99 0R772 85920000 (00271)
A9A 0R774 44300000 (00272)
A9B 0R776 02400000 (00273)
A9C 0R778 04520000 (00274)
A9D 0R77A 84330000 (00275)
A9E 0R77C 43400000 (00276)
A9F 0R77E 04500000 (00277)
A90 0R780 44550000 (00278)
A91 0R782 49090000 (00279)
A92 0R784 84030000 (00280)
A93 0R786 04500000 (00281)
A94 0R788 44760000 (00282)
A95 0R78A 04020000 (00283)
A96 0R78C 04500000 (00284)
A97 0R78E 25400000 (00285)
A98 0R790 84100000 (00286)
A99 0R792 90160000 (00287)
A9A 0R794 20372037 (00288)
A9B 0R796 04500000 (00289)
A9C 0R798 04500000 (00290)
A9D 0R79A 00000000 (00291)
A9E 0R79C 00000000 (00292)
A9F 0R79E 00000000 (00293)
A90 0R7A0 00000000 (00294)
A91 0R7A2 00000000 (00295)
A92 0R7A4 00000000 (00296)
A93 0R7A6 00000000 (00297)
A94 0R7A8 00000000 (00298)
A95 0R7AA 00000000 (00299)
A96 0R7AC 00000000 (00300)
A97 0R7AE 00000000 (00301)
A98 0R7B0 00000000 (00302)
A99 0R7B2 00000000 (00303)
A9A 0R7B4 00000000 (00304)
A9B 0R7B6 00000000 (00305)
A9C 0R7B8 00000000 (00306)
A9D 0R7BA 00000000 (00307)
A9E 0R7BC 00000000 (00308)
A9F 0R7BE 00000000 (00309)
A90 0R7C0 00000000 (00310)
A91 0R7C2 00000000 (00311)
A92 0R7C4 00000000 (00312)
A93 0R7C6 00000000 (00313)
A94 0R7C8 00000000 (00314)
A95 0R7CA 00000000 (00315)
A96 0R7CC 00000000 (00316)
A97 0R7CE 00000000 (00317)
A98 0R7D0 00000000 (00318)
A99 0R7D2 00000000 (00319)
A9A 0R7D4 00000000 (00320)
A9B 0R7D6 00000000 (00321)
A9C 0R7D8 00000000 (00322)
A9D 0R7DA 00000000 (00323)
A9E 0R7DC 00000000 (00324)
A9F 0R7DE 00000000 (00325)
A90 0R7E0 00000000 (00326)
A91 0R7E2 00000000 (00327)
A92 0R7E4 00000000 (00328)
A93 0R7E6 00000000 (00329)
A94 0R7E8 00000000 (00330)
A95 0R7EA 00000000 (00331)
A96 0R7EC 00000000 (00332)
A97 0R7EE 00000000 (00333)
A98 0R7F0 00000000 (00334)
A99 0R7F2 00000000 (00335)
A9A 0R7F4 00000000 (00336)
A9B 0R7F6 00000000 (00337)
A9C 0R7F8 00000000 (00338)
A9D 0R7FA 00000000 (00339)
A9E 0R7FC 00000000 (00340)
A9F 0R7FE 00000000 (00341)
A90 0R780 44550000 (00278)
A91 0R782 49090000 (00279)
A92 0R784 84030000 (00280)
A93 0R786 04500000 (00281)
A94 0R788 44760000 (00282)
A95 0R78A 04020000 (00283)
A96 0R78C 04500000 (00284)
A97 0R78E 25400000 (00285)
A98 0R790 84100000 (00286)
A99 0R792 90160000 (00287)
A9A 0R794 20372037 (00288)
A9B 0R796 04500000 (00289)
A9C 0R798 04500000 (00290)
A9D 0R79A 00000000 (00291)
A9E 0R79C 00000000 (00292)
A9F 0R79E 00000000 (00293)
A90 0R7A0 00000000 (00294)
A91 0R7A2 00000000 (00295)
A92 0R7A4 00000000 (00296)
A93 0R7A6 00000000 (00297)
A94 0R7A8 00000000 (00298)
A95 0R7AA 00000000 (00299)
A96 0R7AC 00000000 (00300)
A97 0R7AE 00000000 (00301)
A98 0R7B0 00000000 (00302)
A99 0R7B2 00000000 (00303)
A9A 0R7B4 00000000 (00304)
A9B 0R7B6 00000000 (00305)
A9C 0R7B8 00000000 (00306)
A9D 0R7BA 00000000 (00307)
A9E 0R7BC 00000000 (00308)
A9F 0R7BE 00000000 (00309)
A90 0R7C0 00000000 (00310)
A91 0R7C2 00000000 (00311)
A92 0R7C4 00000000 (00312)
A93 0R7C6 00000000 (00313)
A94 0R7C8 00000000 (00314)
A95 0R7CA 00000000 (00315)
A96 0R7CC 00000000 (00316)
A97 0R7CE 00000000 (00317)
A98 0R7D0 00000000 (00318)
A99 0R7D2 00000000 (00319)
A9A 0R7D4 00000000 (00320)
A9B 0R7D6 00000000 (00321)
A9C 0R7D8 00000000 (00322)
A9D 0R7DA 00000000 (00323)
A9E 0R7DC 00000000 (00324)
A9F 0R7DE 00000000 (00325)
A90 0R7E0 00000000 (00326)
A91 0R7E2 00000000 (00327)
A92 0R7E4 00000000 (00328)
A93 0R7E6 00000000 (00329)
A94 0R7E8 00000000 (00330)
A95 0R7EA 00000000 (00331)
A96 0R7EC 00000000 (00332)
A97 0R7EE 00000000 (00333)
A98 0R7F0 00000000 (00334)
A99 0R7F2 00000000 (00335)
A9A 0R7F4 00000000 (00336)
A9B 0R7F6 00000000 (00337)
A9C 0R7F8 00000000 (00338)
A9D 0R7FA 00000000 (00339)
A9E 0R7FC 00000000 (00340)
A9F 0R7FE 00000000 (00341)

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```

SA=DCHIAS
SH=1.0/VAR
SA'=20.0
SH'=VAR

```

FOR I=1,2

RELEASE APS INPUT

```

PAGE      H:      PCN 240... "MPEDVM(Y,U,V)"      FLOAT,DCHIAS,VAR,REFLECT INPUT VIA MAXHOUT ALG.

ARP 0RT9F 8460R460 (00295)      MOV(MU,M/)
AC0 0RTA0 08K0RHC (00296)      MOV(P,00)
AC1 0RTA2 20172037 (00297) ?    CLEAR(WI)
AC2 0RTA4 081C0000 (00298) *    MOV(ZERO,00)\NOP
AC3 0RTA6 20120000 (00300) DNESA  CLEAR(RA)\NOP
AC4 0RTA8 00000000 (00301)      NOP
AC5 0RTAA 10000000 (00302)      JUMP(0)
AC6 0RTAC 20462046 (00303)      SFT(G2)
      00000007 (00304) ?        FDS$Z=EA-FDV$SA
      00306 (00305)            END FDS$Z.
      00307 (00306)            EJECT
      00308 (00307)

```

```

SA*SR:SA'*SR!
SC=SA*SK=DC/VAR:SC'=SA!*
SN'=20.0*ALOG10(VAR)
RELEASE APS INPUT

```

```

CLEAR INFULI.
API DONE!

```

```

>>>> FOR TIMING PURPOSE
S ONLY!

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A40	08H36	80200031	(00397)	DNFSI	CLEAR(R1)	APS INPUT DONE!
A41	08H36	82000020	(00398)		NDP	
			(00399)	*		
A42	08H3A	84300012	(00400)	G200SD	SET(RA)	KNARLE APU
A43	08H3C	8742057F	(00401)		FLOAD(HW0,ISAS125(1),TF)	00=CALLIN CARD=0
A44	08H3F	88701060	(00402)	OFLTS	LOAD(RW3,11)	DUMMY OP FOR EXEC
A45	08H40	8A500000	(00403)		LOAD(RW1,MSS)	NIN(.) SIZE=1
A46	08H42	8C320000	(00404)		SUR(HW3,MSS)	DUMMY OP FOR EXEC
A47	08H44	8E400006	(00405)		FLOAD(HW0,10)	MAPX(.) RA
A48	08H46	90700000	(00406)		FLOAD(RW3,MSS)	MAPX(.) SIZE=1
A49	08H48	92020000	(00407)		SUR(HW0,MSS)	MAPX(.) RA-2
A4A	08H4A	94210010	(00408)		MOVH(RW2,HW0)	SAVE MAPX(.) RA-2
A4B	08H4C	960A0014	(00409)		ADD(HW0,NP2S)	MAPX(.) RA-2+2*NP
A4C	08H4F	988A0002	(00410)	OFLTSJ	ADD(HW0,WS,TF)	00=MAPX(J)
A4D	08H50	9AA00002	(00411)		ADD(HW0,WS,TF)	00=MAPX(J+1)
A4E	08H52	9C114CH2	(00412)		SURL(HW1,2),JUMP(OF1,TSJ)	FOR J=NP,NP+1,...,LTH-1
			(00413)	*		
A4F	08H54	9FC203FA	(00414)	NSUMSS	FLOAD(HW0,SAS52(1),TF)	00=DCRIAS
			(00415)	*		
A50	08H56	A0010014	(00416)	NSUMSD	MOVH(RW0,HW2)	SAVE MAPX(.) RA-2
A51	08H58	A2110016	(00417)		MOVH(RW1,HW1)	SAVE MAPX(.) SIZE=1
A52	08H5A	A4AA0002	(00418)	NSURSJ	ADD(HW0,WS,TF)	00=MAPX(J)
A53	08H5C	A6AA0002	(00419)		ADD(HW0,WS,TF)	00=MAPX(J+1)
A54	08H5E	A8115282	(00420)		SURL(HW1,2),JUMP(NSURSJ)	FOR J=0,1,2,...,LTH-1
			(00421)	*		
A55	08H60	AAC203F0	(00422)	NSOSS	FLOAD(HW0,SAS55(1),TF)	00=VAR**2
			(00423)	*		
A56	08H62	ACC203F0	(00424)	NSORSS	FLOAD(HW0,SAS55(1),TF)	00=SQRT(SD)=VAR
			(00425)	*		
A57	08H64	AF02043C	(00426)	NSIVSS	FLOAD(HW0,SAS53(1),TF)	00=1.0/VAR
			(00427)	*		
A58	08H66	B0402022	(00428)	NDIVSD	FLOAD(HW0,121)	REFLECTION RASE
A59	08H68	B2500000	(00429)		FLOAD(RW1,MSS)	REFLN SIZE=1
A5A	08H6A	B4600000	(00430)		FLOAD(RW2,MSS)	DUMMY
A5B	08H6C	B6210010	(00431)		MOVH(RW2,HW0)	(N-1)*2
A5C	08H6E	B811002A	(00432)		ADDH(RW1,HW1)	(N-1)*4 + REFLN RASE =XN
A5D	08H70	BA21002A	(00433)		ADDH(RW2,HW1)	(N-1)
A5E	08H72	BC21002A	(00434)		ADDH(RW2,HW1)	XI(-1)
			(00435)	*		XI(N)
A5F	08H74	BE010032	(00436)		SURL(HW0,2)	XI(N)
A60	08H76	C021003C	(00437)		ADDL(HW2,4)	XI(K)
A61	08H78	C28A0002	(00438)	UMFLSJ	ADD(HW0,2,TF)	XI(N-K-1)
A62	08H7A	C48A0002	(00439)		ADD(HW0,2,TF)	
A63	08H7C	C6A20002	(00440)		SUR(HW2,2,TF)	

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PAGE 12:      PCN 240... "MPF00VM(V,U,V)"      FLOAT,DCHIAS,VAR,REFLECT INPUT VIA MAKH000L ALG.

A64 0007F CMA20007 (00441)      SUBR(HW2,2,TF)
A65 00080 CAIL161R4 (00442)      SUBR1(HW1,4),JUMPP(ORF1,SJ)
A66 00082 CCC20794 (00443) *      LOAD(HW0,DWYS(1),TF)
A67 00084 CFC203F0 (00445) *      LOAD(HW0,SASS5(1),TF)
A68 00086 D0C203FA (00447) DNRWSS      LOAD(HW0,SASS2(1),TF)
A69 00088 D2C203F0 (00448) *      LOAD(HW0,SASS5(1),TF)
A6A 0008A D5420570 (00449) *      LOAD(HW0,ISAS110(1),TF)
A6B 0008C D6200030 (00451) *      CLEAR(M0)
A6C 0008E D8000020 (00453) *      NOP
      00008466 (00455) *      G200SA= #C
      00008466 (00456) ;      END 8A-1
      00008466 (00457) ;      STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
      00008466 (00458) ;      G200ST DATA 6F'0.0'
      ...
      00008466 (00460) *      G200SZ=81-G200S
      00008466 (00461) *      END

```

XR(N-K-1)
FOR K=0,1,...,N/2-1.

00=7
00=ALOG10(VAR)

00=DC/VAR
00=20.0*ALOG10(VAR)
CLEAR INFULL.

APS OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN
CHOR

PAGE	LINE	PCB 240...	MPFNUM(Y,U,V)	FL0AT,DCIAS,VAR,REFLECT	INPUT VIA	MAKHHUL, ALG.
APDTSORG:	00000	(00007)	(00031)			
CSPUSMUS:	02100	(00000)	(00034)			
INVS:	00700	(00000)	(00306)	(00444)		
INFS:	00000	(00300)				
INFS1:	00000	(00307)				
INFS2:	00000	(00308)				
INFS3:	00000	(00309)				
INFS4:	00000	(00310)				
INFS5:	00000	(00311)				
INFS6:	00000	(00312)				
INFS7:	00000	(00313)				
INFS8:	00000	(00314)				
INFS9:	00000	(00315)				
INFS10:	00000	(00316)				
INFS11:	00000	(00317)				
INFS12:	00000	(00318)				
INFS13:	00000	(00319)				
INFS14:	00000	(00320)				
INFS15:	00000	(00321)				
INFS16:	00000	(00322)				
INFS17:	00000	(00323)				
INFS18:	00000	(00324)				
INFS19:	00000	(00325)				
INFS20:	00000	(00326)				
INFS21:	00000	(00327)				
INFS22:	00000	(00328)				
INFS23:	00000	(00329)				
INFS24:	00000	(00330)				
INFS25:	00000	(00331)				
INFS26:	00000	(00332)				
INFS27:	00000	(00333)				
INFS28:	00000	(00334)				
INFS29:	00000	(00335)				
INFS30:	00000	(00336)				
INFS31:	00000	(00337)				
INFS32:	00000	(00338)				
INFS33:	00000	(00339)				
INFS34:	00000	(00340)				
INFS35:	00000	(00341)				
INFS36:	00000	(00342)				
INFS37:	00000	(00343)				
INFS38:	00000	(00344)				
INFS39:	00000	(00345)				
INFS40:	00000	(00346)				
INFS41:	00000	(00347)				
INFS42:	00000	(00348)				
INFS43:	00000	(00349)				
INFS44:	00000	(00350)				
INFS45:	00000	(00351)				
INFS46:	00000	(00352)				
INFS47:	00000	(00353)				
INFS48:	00000	(00354)				
INFS49:	00000	(00355)				
INFS50:	00000	(00356)				
INFS51:	00000	(00357)				
INFS52:	00000	(00358)				
INFS53:	00000	(00359)				
INFS54:	00000	(00360)				
INFS55:	00000	(00361)				
INFS56:	00000	(00362)				
INFS57:	00000	(00363)				
INFS58:	00000	(00364)				
INFS59:	00000	(00365)				
INFS60:	00000	(00366)				
INFS61:	00000	(00367)				
INFS62:	00000	(00368)				
INFS63:	00000	(00369)				
INFS64:	00000	(00370)				
INFS65:	00000	(00371)				
INFS66:	00000	(00372)				
INFS67:	00000	(00373)				
INFS68:	00000	(00374)				
INFS69:	00000	(00375)				
INFS70:	00000	(00376)				
INFS71:	00000	(00377)				
INFS72:	00000	(00378)				
INFS73:	00000	(00379)				
INFS74:	00000	(00380)				
INFS75:	00					

PAGE 14: FOR 240... "MPEDUM(Y,U,V)" FLIAT,D'RIAS,VAR,REFLECT INPUT VIA MAKHOUL ALG.

OSUNSD:	00050 (00416)		
OSUNSJ:	00052 (00418)	(00420)	
OSUMSS:	00048 (00414)		
OSUSS:	00054 (00422)		
SAS39:	00300 (00018)	(00324)	
SAS52:	00318 (00019)	(00414) (00447)	
SAS54:	00314 (00020)	(00324)	
SAS55:	00310 (00021)	(00354) (00377) (00378) (00393) (00422) (00424) (00445)	
	(00448)		
SAS93:	00430 (00022)	(00391) (00426)	
SAS94:	00434 (00023)	(00392)	
SAS95:	00440 (00024)	(00370)	
SINVS:	00068 (00200)		
SINDCS:	00084 (00233)		
SNPMS:	00088 (00291)		
SO1:	00024 (00126)	(00135)	
SU2:	00025 (00124)	(00127)	
SSORTS:	00019 (00151)		
STANT:	00000 (00025)	(00039)	
SVTS:	00342 (00017)	(00018) (00019) (00020) (00021) (00022) (00023) (00024)	
VMILS:	00078 (00222)		
VSUS:	00021 (00124)		
WS:	00002 (00026)	(00340) (00356) (00357) (00365) (00372) (00373) (00382) (00410) (00411)	
	(00418)	(00419)	
WWS:	00004 (00027)		
ZS:	00003 (00028)		

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) F- 0

PAGE	1:	FCM 241...	"MPDCTM(V,U,V,W)"	FORM TRUE DCT AND ITS REFLECTED MAG SQUARED, VIA
		(00001) *	FCM 241...	"MPDCTM(V,U,V,W)"
		(00002) *		FORM TRUE DCT AND ITS REFLECTED MAG SQUARED, VIA
		(00003) *		ORIGINATED:06-OCT-79
		(00004) *		UPDATED:29-FEB-80
		(00005) *	DEFIN+ GLOBAL SYMBOLS	
		(00006) *	OPADD EXP,(1 ,U.S. 14)+(12 ,U.S. 8)+\$1E	
		(00007) *	AFDTSORG=\$FEM	
		(00008) *	CSPUSNOS=\$21FC	
		(00009) *	IMYS=\$194	
		(00010) *	IME=3	
		(00011) *	MS=1	
		(00012) *	WSS=0	
		(00013) *	NP2S=2*H+10	
		(00014) *	SVTS=\$03W2	
		(00015) *	START=\$H400	
		(00016) *	WS=2	
		(00017) *	WWS=4	
		(00018) *	ZS=3	
		(00019) *	EXPAND ARRAY FUNCTION DISPATCH TABLE	
		(00020) *	EL=AFDTSORG+3*2*(241-128)	FCR 241
		(00021) *	ADDR DCTMS(R7,1)	
		(00022) *	ADDR DCTMS(R7,1)	
		(00023) *	ADDR CSPUSNOS(1,0)	
		(00024) *		
		(00025) *	EL=START	
		(00026) *	FVFN	
		(00027) *	FJFCT	

FCA 2.41


```

A19 08934 08E005E9 (00072) ?
A1A 08936 08E008E9 (00073)
A1B 08938 08E008E9 (00074)
A1C 0893A 43533353 (00075)
A1D 0893C 08E008E9 (00076)
A1E 0893E 08E008E9 (00077)
A1F 08940 08E008E9 (00078)
A20 08942 08E008E9 (00079)
A21 08944 85408540 (00080)
A22 08946 08E008E9 (00081)
A23 08948 08E008E9 (00082)
A24 0894A 08E008E9 (00083)
A25 0894C 08E008E9 (00084)
A26 0894E 08E008E9 (00085)
A27 08950 08E008E9 (00086)
A28 08952 08E008E9 (00087)
A29 08954 90100015 (00088)
A2A 08956 20122037 (00089)
A2B 08958 08E008E9 (00090)
A2C 0895A 10000000 (00091)
      0895C 00000020 (00092)
      0895E 00000020 (00093)
      0895F 00000020 (00094)
      08960 00000020 (00095)
      08962 00000020 (00096)

      *VI
      SIN(K+1)
      GET FACTOR OF 2 IN THERE
      VI(K+1)
      PREPARE TO SQUARE
      SQUARE REAL IMAG PARTS
      D(K)\D(N-K)
      D(K)*2=MR(K)\MI(K)
      MR(N-K)
      MI(N-K)
      MR(2N-K)\MI(2N-K)
      MI(N+K)

      MOV(L0A,M0)
      MOV(L0A,M1)
      MOV(R,A2)
      MOV(A3),ADD(A2,A3)
      MOV(L0A,M4)
      MOV(L0A,M5)
      MOV(R,M2)
      MOV(R,M6)
      MOV(M2,M6)
      MOV(R,M0)
      MOV(P,M0)\MOV(ZERO,M0)
      NOP\MOV(P,M0)
      MOV(ZERO,M0)\NOP
      MOV(P,M0)\MOV(ZERO,M0)
      NOP\MOV(P,M0)
      MOV(ZERO,M0)\NOP
      JMP(C,DCTIP,F1)
      NOP
      JMP(0)

      DCTMSSZ=EA-DCTMSSA
      END DCTMSS7
      EJECT

```

PAGE 4: FCM 241... "MPPD(TM(V,U,V,W)) FIRM TRIP DCT AND ITS REFLECTED MAG SQUARED, VIA

[illegible]

(00141) * MI(N-K),MH(2N-K),MI(2N-K),MH(N+K),MI(N+K),FN.
(00142) * K=(N/2-1,N/2-2,...,1) N=256
(00143) *

A19 0R996 32300032	DCTMSD	SFI(NA)	D(0)
A1A 0R998 3400002A		LOAD(HW0,101)	N-1
A1B 0R99A 36500000		LOAD(HW1,MSS)	DUMMY
A1C 0R99C 38600000		LOAD(HW2,MSS)	MR(0)
A1D 0R99E 3A601006		LOAD(HW2,111)	DUMMY
A1F 0R9A0 3C700000		LOAD(HW3,MSS)	D(0)
A1G 0R9A2 3F700000		LOAD(HW3,MSS)	2*N/2
A20 0R9A4 40810010		MVVA(HW0,HW0,TF)	D(N/2)
A21 0R9A6 42110039		ADDL(HW1,1)	2*N/2+MS (=2*K)
A22 0R9A8 4411002A		ADPH(HW0,HW1,TF)	2N (=4*N/2)
A23 0R9AA 4611002A		ADPH(HW1,HW1)	4*2*N/2 (2*K=2*N/2)
A24 0R9AC 48110012		MVVA(HW3,HW1)	D(-N/2)
A25 0R9AE 4A11002E		ADPH(HW3,HW3)	2*(N/2-1)*MS (=2*K(NEW))
A26 0R9H0 4C010022		SURH(HW0,HW1)	MR(0)
A27 0R9H2 4E01003A		ADDL(HW0,2)	MI(0)
A28 0R9H4 50110034		SURH(HW1,4)	MR(N/2)
A29 0R9H6 52A10014		MVVA(HW2,HW2,TF)	MI(N/2)
A2A 0R9H8 54AA0002		ADD(HW2,2,TF)	MR(N)
A2B 0R9HA 56AA01FF		ADPH(HW2,4*12H-2,TF)	MI(N)
A2C 0R9HC 58AA0002		ADD(HW2,2,TF)	MR(3N/2)
A2D 0R9HE 5AA001FF		ADPH(HW2,4*12H-2,TF)	MI(3N/2)
A2E 0R9H0 5C0A0002		ADD(HW2,2,TF)	4*2*(N/2-1)-1 (-1 FOR L0
A2F 0R9C2 5EAA01FF		ADPH(HW2,4*12H-2,TF)	NP TEST UPDATE)
A30 0R9C4 60AA0002		ADD(HW2,2,TF)	RESTORE FROM LOOP TEST
A31 0R9C6 62320009		SURH(HW3,9)	D(K)
A32 0R9C8 64310030		ADDL(HW3,1)	D(N+K)
A33 0R9CA 66H1002A		ADPH(HW0,HW1,TF)	D(N-K)
A34 0R9CC 680A0200		ADD(HW0,2*256)	D(-K+1)
A35 0R9CE 6A010022		SURH(HW0,HW1,TF)	MR(K)
A36 0R9C0 6C0201FF		SURH(HW0,2*256-2)	MI(K)
A37 0R9D2 6E020406		SURH(HW2,4*256+6,TF)	MR(N+K)
A38 0R9D4 70AA0002		ADD(HW2,2,TF)	MR(N-K)
A39 0R9D6 722A03FF		ADPH(HW2,4*256-2)	MI(N-K)
A3A 0R9D8 74A10026		SURH(HW2,HW3,TF)	MR(2N-K)
A3B 0R9DA 76AA0002		ADD(HW2,2,TF)	MI(2N-K)
A3C 0R9DC 78AA03FF		ADPH(HW2,4*256-2,TF)	MR(N-K)
A3D 0R9DE 7AA00002		ADD(HW2,2,TF)	MI(N+K)
A3E 0R9F0 7C220402		SURH(HW2,4*256+2)	
A3F 0R9F2 7EA1002F		ADPH(HW2,HW3,TF)	
A40 0R9F4 80AA0002		ADD(HW2,2,TF)	

PAGE 6: PCN 241... "MPDCTM(Y,D,V,M)" FIRM TRUE DCT AND ITS REFLECTED MAG SQUARED, VIA

A41 089F6	R2110034	(00185)	SUHL(RW1,4)	2K GETS 2(K-W8)
A42 089F8	R4320004	(00186)	SUHL(RW3,8)	2K GETS 2(K-CS) (COMPLEX
A43 089FA	R4313201	(00187)	SUHL(RW3,1),JUMPP(81)	2K GETS 2K-1 SO TEST FAI
A44 089FC	R4200030	(00188)	CLEAR(R0)	1.5 FOR 0
A45 089FF	W4000070	(00190)	WIP(0)	
	0000899F	(00191)	DCTMSA=RC	
	089F0	(00192)	FND 8A-1	
	089F0	(00193)	DATA 4F'0.0'	
...	00000000	(00194)	DCTMS1	
089F8	00000094	(00195)	DCTMS2=81-DCTMSS	
		(00196)	FND	

PAGE 7: FCH 241... "MPOCTM(Y,U,V,W)" FIRM TRUE OCT AND ITS REFLECTED MAG SQUARED, VIA

AFDTSUNG:	000000	(000000)	(00020)
CSPUSNUS:	0210C	(00007)	(00023)
DOCLP:	00015	(00064)	(00009)
DCTMS:	00002	(00021)	(00043)
DCTMSA:	00006	(00107)	(00142)
DCTMSI:	00000	(00103)	(00194)
DCTMSO:	00019	(00111)	(00144)
DCTMS:	00064	(00022)	(00104)
DCTMSSA:	00000	(00041)	(00046)
DCTMSSZ:	00020	(00042)	(00094)
DCTMSZ:	00004	(00106)	(00195)
DMS:	00794	(00004)	(00134)
MS:	00001	(00010)	
MSS:	00000	(00011)	(00114)
NP2S:	00014	(00012)	
STAP1:	00000	(00014)	(00025)
SVTS:	00182	(00013)	
WS:	00002	(00015)	
WMS:	00004	(00016)	
ZS:	00003	(00017)	
			(00110) (00195)
			(00115) (00117) (00118) (00146) (00147) (00149) (00150)

LINES WITH ERRORS: 0 (MAP VERSION H00101.10) E- 0

QUANTIZE/DEQUANTIZE SIDERAND

W/ K->A TRANSFORM

ORIGINATED:04-AUG-79

UPDATED:12-MAY-80

FCH 242... "MPQDPP(V,U,V)"

```

(00001) * FCH 242... "MPQDPP(V,U,V)"
(00002) *
(00003) *
(00004) *
(00005) * DEFINE GLOBAL SYMBOLS
00000012 (00006)
00000014 (00007)
00000016 (00008)
00000018 (00009)
00000020 (00010)
00000022 (00011)
00000024 (00012)
00000026 (00013)
00000028 (00014)
00000030 (00015)
00000032 (00016)
00000034 (00017)
00000036 (00018)
00000038 (00019)
00000040 (00020)
00000042 (00021)
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00000070 (00035)
00000072 (00036)
00000074 (00037)
00000076 (00038)
00000078 (00039)
00000080 (00040)
00000082 (00041)
00000084 (00042)
00000086 (00043)
00000088 (00044)

```

ATSD=0'1H'

AFDSORC=SRFH

AFSSMF=SFEC

CSPUSNUS=S21FC

PCPUS=0'1q'

MS=3

HS=1

MS=0

MS=0

MPUS=0'10'

PCPUS=0'12'

PDUS1=S2134

PDUS2=S2174

PDUS3=S21H4

PDUS4=S21D4

PDUS5=S21F4

PDUS6=S2204

PDUS7=S2214

PDUS8=S221C

DCSDFO=S2224

VAPSDU=S2244

PCSDFO=S22H4

MSDFO=S228C

SVTS=S0382

PAPCS=0'35'

PSJZFS=0'8'-0'1'

QPMMS=0'11H16'

SAS11=SVTS+2*0'11'

SAS3H=SVTS+2*0'3H'

SAS39=SVTS+2*0'39'

SAS52=SVTS+2*0'52'

SAS55=SVTS+2*0'55'

SAS60=SVTS+2*0'60'

SAS61=SVTS+2*0'61'

SAS62=SVTS+2*0'62'

SAS8H=SVTS+2*0'8H'

SAS90=SVTS+2*0'90'

SAS91=SVTS+2*0'91'

PAGE 2: FCR 242... "MPDPP(Y,U,V)" QUANTIZE/DEQUANTIZE SIDEHAND

```
0000043A (00045) SASQ2=SVTS+2*0'92'
00000452 (00046) SAS104=SVTS+2*0'104'
0000045F0 (00047) START=S65F0
0000090H (00048) VP0S=0'11'
0000000R (00049) VARP0S=0'R'
00000002 (00050) AS=2
00000003 (00051) ZS=3
00000H94 (00052) *
00000H94 (00053) * EXPAND ARRAY FUNCTION DISPATCH TABLE
00H94 001F65F2 (00054) BL=AFDTSORG+12*(242-128)
00H96 001F66F2 (00055) ADDR QUANS(M7,1)
00H98 001021FC (00056) ADDR G105S(M7,1)
00H98 001021FC (00057) ADDR CSPUSNOS(1,0)
0005H (00058) EJECT
```

FCR 242

[illegible]

PAGE 4: FCW 242... "MPQDPP(V,U,V)" QUANTIZE/DEQUANTIZE SIDEHAND

01FR2 287A8D40					
01FR4 352C7BC0					
01FR6 41254640	(000079)	DATA 0.59019E+00,	0.65272E+00,	0.70886E+00,	0.76395E+00
01FR8 468H5RC0					
01FR0 538C5440	(000080)	DATA 0.81878E+00,	0.87625E+00,	0.94152E+00,	0.10123E+01
01FR2 5A8H6CC0					
01FR4 61C91D40	(000081)	DATA 0.10847E+01,	0.11620E+01,	0.12381E+01,	0.13059E+01
01FR6 68C0C840					
01FR8 7024F5C0	(000082)	DATA 0.13777E+01,	0.14357E+01,	0.14920E+01,	0.15468E+01
01FR0 78H38A40					
01FR2 881430C1	(000083)	DATA 0.16017E+01,	0.16527E+01,	0.16951E+01,	0.17323E+01
01FR4 9A147A0C1					
01FR6 9A147A0C1	(000084)	DATA 0.17683E+01,	0.18035E+01,	0.18365E+01,	0.18668E+01
01FR8 9A147A0C1					
01FR0 9A147A0C1	(000085)	DATA 0.18961E+01,	0.19267E+01,	0.19616E+01,	0.10000E+21
01FR2 9A147A0C1					
01FR4 9A147A0C1	(000086) *				
01FR6 9A147A0C1	(000087) *				
01FR8 9A147A0C1	(000088) PVFCS1	DATA 0.31528E+00,	0.45224E+00,	0.56230E+00,	0.65753E+00
01FR0 9A147A0C1					
01FR2 9A147A0C1	(000089)	DATA 0.74494E+00,	0.82867E+00,	0.91130E+00,	0.99458E+00
01FR4 9A147A0C1					
01FR6 9A147A0C1	(000090)	DATA 0.10745E+01,	0.11678E+01,	0.12608E+01,	0.13581E+01
01FR8 9A147A0C1					
01FR0 9A147A0C1					
01FR2 9A147A0C1					
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01FR8 9A147A0C1					
01FR0 9A147A0C1					</

01FD6 0ADDK3C1			
01FDW 0P81EAC1	(00091)	DATA 0.14619E+01, 0.15854E+01, 0.17491E+01, 0.10000E+21	
01FDA 0CAF66A1			
01FDC 0DEF28A1			
01FD6 2H5F3AD1	(00092) *		
	(00093) *	PARCOR(4) W/ 4 BITS	
01FE0 3462E5C0	(00094) PVFCS4	DATA 0.40927E+00, 0.57217E+00, 0.69580E+00, 0.80120E+00	
01FE2 42AC70DC0			
01FE4 50FF69A0			
01FE6 6681D8C0			
01FE8 72C14AC0	(00095)	DATA 0.89689E+00, 0.98763E+00, 0.10764E+01, 0.11650E+01	
01FEA 7F6A8AC0			
01FEC 089C77C1			
01FEF 0951F8C1			
01FF0 0A9A0A01	(00096)	DATA 0.12547E+01, 0.13464E+01, 0.14408E+01, 0.15388E+01	
01FF2 0AC56D41			
01FF4 088AC2A1			
01FF6 0C4F76A1			
01FF8 0D2210C1	(00097)	DATA 0.16420E+01, 0.17540E+01, 0.18815E+01, 0.10000E+21	
01FFA 0E0831A1			
01FFC 0F004FC1			
01FFE 2H5F3AD1	(00098) *		
	(00099) *	PARCOR(5) W/ 3 BITS	
02000 4747D840	(00100) PVFCS5	DATA 0.55688E+00, 0.74670E+00, 0.89149E+00, 0.10220E+01	
02002 5F93DDC0			
02004 721C5840			
02006 082D0F41			
02008 09388641	(00101)	DATA 0.11526E+01, 0.12967E+01, 0.14823E+01, 0.10000E+21	
0200A 0A5FA441			
0200C 0BDD8C041			
0200E 2H5F3AD1	(00102) *		
	(00103) *	PARCOR(6) W/ 3 BITS	
02010 496238C0	(00104) PVFCS6	DATA 0.57331E+00, 0.76317E+00, 0.90902E+00, 0.10407E+01	
02012 61AF8DC0			
02014 7458C440			
02016 0E535AC1			
02018 096147C1	(00105)	DATA 0.11725E+01, 0.13190E+01, 0.15093E+01, 0.10000E+21	
0201A 0AMD4FC1			
0201C 0C130AC1			
0201F 2H5F3AD1	(00106) *		

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(00107) * PARCOR(7) W/ 2 BITS
(00108) PVFCS7 DATA 0.56750E+00, 0.87517E+00, 0.11666E+01, 0.10000E+21

(00109) *
(00110) * PARCOR(8) W/ 2 BITS
(00111) PVFCS8 DATA 0.72194E+00, 0.98497E+00, 0.12430E+01, 0.10000E+21

(00112) *
(00113) * DC BIAS W/ 4 BITS
(00114) DCS DATA 0.12475E+00, 0.25019E+00, 0.37706E+00, 0.50625E+00

(00115) DATA 0.63881E+00, 0.77594E+00, 0.91903E+00, 0.10699E+01

(00116) DATA 0.12309E+01, 0.14053E+01, 0.15977E+01, 0.18155E+01

(00117) DATA 0.20718E+01, 0.23938E+01, 0.28541E+01, 0.10000E+21

(00118) *
(00119) * VARIANCE W/ 5 BITS
(00120) VARS DATA 0.56312E+01, 0.77983E+01, 0.98311E+01, 0.12057E+02

(00121) DATA 0.14548E+02, 0.16994E+02, 0.19276E+02, 0.21426E+02

(00122) DATA 0.23405E+02, 0.25415E+02, 0.27476E+02, 0.29409E+02

(00123) DATA 0.31174E+02, 0.32888E+02, 0.34633E+02, 0.36477E+02

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0206A 1071A9C2			
0206C 115106A2			
0206F 123005A2			
02070 13316A42 (00124)	DATA	0.3R38A4F+02, 0.40204F+02, 0.41864F+02, 0.43443F+02	
02072 141A1CC2			
02074 14F197C2			
02076 15A8H442			
02078 167002A2			
0207A 173002C2 (00125)	DATA	0.44987F+02, 0.46483F+02, 0.47960F+02, 0.49431F+02	
0207C 17A4F142			
0207F 18H72H42			
02080 197334A2 (00126)	DATA	0.50900F+02, 0.52334F+02, 0.53709F+02, 0.55072F+02	
02082 1A2AC0C2			
02084 1A8AC0C2			
02086 1B8437A2			
02088 1C3009A2 (00127)	DATA	0.56491F+02, 0.58016F+02, 0.59760F+02, 0.10000F+21	
0208A 1D020C42			
0208C 1DE147C2			
0208E 2A5E3AD1			
(00128) *			
(00129) *	PITCH GAIN W/ 2 HITS		
(00130) PGS	DATA	0.48797F+00, 0.66711F+00, 0.82399F+00, 0.10000F+21	
(00131) *			
(00132) *	PITCH W/ 6 HITS		
(00133) PITCHS	DATA	160'0,	
(00134)			
DATA	1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19		
0209R 0000			
...			
020AR 0001			
020AQ 0002			
020AA 0003			
020AH 0004			
020AC 0005			
020AD 0006			
020AE 0007			
020AF 0008			
020AO 0009			
020AI 000A			
020AJ 000B			
020AK 000C			
020AL 000D			
020AM 000E			
020AN 000F			

PAGE 8: 4CH 242... "MPUNDP(Y,U,V)" QUANTIZE/DQUANTIZE SIDFRAND

020M7 0010		
020M8 0011		
020M9 0012		
020MA 0013		
020MR 0014	DATA 20,21,22,23,24,25,26,27,28,29,30,31,32,33,34,35	
020MC 0015	(00135)	
020MD 0016		
020ME 0017		
020MF 0018		
020CG 0019		
020C1 001A		
020C2 001H		
020C3 001C		
020C4 001D		
020C5 001E		
020C6 001F		
020C7 0020		
020C8 0021		
020C9 0022		
020CA 0024		
020CM 002A	DATA 36,37,38,39,40,41,42,43,44,45,46,47	
020CC 0025	(00136)	
020CD 0026		
020CF 0027		
020CF 0028		
020D0 0029		
020D1 002A		
020D2 002H		
020D3 002C		
020D4 002D		
020D5 002F		
020D6 002F		
020D7 0030	DATA 48,49,49,49,50,50,51,51,52,52,53,53,54,54,55,55	
020D8 0030	(00137)	
020D9 0031		
020DA 0031		
020DB 0032		
020DC 0032		
020DD 0034		
020DE 0033		
020DF 0034		
020E0 0034		
020E1 0035		
020E2 0035		

QUANTITATIVE/QUALITATIVE: SUFFICIENT

FCM 247... "MP(f)Pv(Y, II, V)"

PAGE: 4:

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02003 0036
02004 0036
02005 0037
02006 0037
02007 0038
02008 0038
02009 0039
0200A 0039
0200B 003A
0200C 003A
0200D 003A
0200E 003A
0200F 003B
02010 003B
02011 003D
02012 003D
02013 003E
02014 003F
02015 003F
02016 003F
02017 0R00
02018 K450
02019 0000
0201A Q250
0201B 0000

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(0013H) DATA 56,56,57,57,58,58,59,59,60,60,61,61,62,62,63,63

(0013Q) FVFN
(00140) INSTPS0 DATA INS0*0'512'+X'50',X'0000'
(00141) INSTPS1 DATA INS1*0'512'+X'50',X'0000'
(00142) * >>>> DEQUANTIZER THRESHOLDS...SEE WDPDPS.TXT <<<
(00143) AUS 1
000065F0 #I=START
(00145) FJCT

[illegible]

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A24 0662A 00000430 (00190)      NOP\MUL(M0,M7)
A25 0662C 00000085 (00191)      NOP\MOV(P,A5)
A26 0662E 00003A00 (00192)      NOP\ATJCN(A5)
A27 06630 0000009C (00193)      NOP\MOV(M0,M0)
A28 06632 901C0024 (00194)      JMPCC(B1,F0)
A29 06634 20370000 (00195)      CLEAR(W1)\NOP
A2A 06636 20500000 (00196)      SET(P0)\NOP
A2B 06638 08FC0000 (00197)      MOV(I0A,I0)\NOP
A2C 0663A 901C002C (00198)      JMPCC(B2,F0)
A2D 0663C 20370000 (00199)      CLEAR(W1)\NOP
A2E 0663E 20500000 (00200)      SET(P0)\NOP
A2F 06640 08DC0000 (00201)      MOV(I0,I0)\NOP
A30 06642 000008FC (00202)      NOP\MOV(I0A,I0)
A31 06644 901C0031 (00203)      JMPCC(B3,F0)
A32 06646 20370000 (00204)      CLEAR(W1)\NOP
A33 06648 20500000 (00205)      SET(P0)\NOP
A34 0664A 08FC0000 (00206)      MOV(I0A,I0)\NOP
A35 0664C 901C0035 (00207)      JMPCC(B4,F0)
A36 0664E 20370000 (00208)      CLEAR(W1)\NOP
A37 06650 20500000 (00209)      SET(P0)\NOP
A38 06652 08FC0000 (00210)      MOV(I0A,I0)\NOP
A39 06654 08F10000 (00212) * K->A TRANSFORM W/ FNG=SUM(1-K(I))**2
A3A 06656 0A200000 (00213)      MOV(I0A,A1)\NOP
A3B 06658 0A880000 (00214)      NEG(A1)\NOP
A3C 0665A 0A8F0000 (00216)      MOV(R,M0)\NOP
A3D 0665C 0A930000 (00217)      MOV(R,M7)\NOP
A3E 0665E 0A600000 (00218)      MOV(R,A1)\NOP
A3F 06660 16801680 (00219)      MUL(M0,M7)\NOP
A40 06662 0A000000 (00220)      K(+1)
A41 06664 0A950000 (00221)      MOV(I0,A0)
A42 06666 08860000 (00222)      MOV(R,A5)\NOP
A43 06668 3FA041A0 (00223)      MOV(P,A6)
A44 0666A 02000200 (00224)      SUM(A5,A6)
A45 0666C 0A8F088F (00225)      MOV(M5),P(A0)
A46 0666E 0A8F088F (00226)      MOV(R,M7)
A47 06670 3000006F (00227)      MOV(R,M3)
A48 06672 3000006F (00228)      CALL(X23RD)
A49 06674 3000006F (00229)      CALL(X23RD)
A4A 06676 3000006A (00230)      CALL(X4TH)
A4B 06678 00000260 (00231)      CALL(X5TH)
A4C 0667A 30000062 (00232)      NOP\RT(A3)
A4D 0667C 30000064 (00233)      CALL(X6TH)
A4E 0667E 30000064 (00234)      CALL(X7TH)

SA*PITCH
NDW "FIX" PITCH
FORCE FXP->0
M->INSTR$0+1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
OVERWRITE INS0
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
NO=Q(M)=QTM
OTM->INSTR$1+1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
OVERWRITE INS1
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT
RELEASE APS OUTPUT
NO=D(Q(M))=DTM

P(1)
MAKE IT K(1)
K(1)**2 FOR ENERGY
SAVE K(1)
SO SQUARE IT
NEED THIS AROUND, TON
P(2)
1.0
K(1)^2
1.0-K(1)^2
PROD(1),GET K(2)
K(2)
K(2)
DO 2ND ITERATION (M=2)
DO 3RD ITER (M=3)

GET A(5)(3) READY
M=6
M=7

```


PC	PC+1	PC+2	PC+3	PC+4	PC+5	PC+6	PC+7	PC+8	PC+9	PC+10	PC+11	PC+12	PC+13	PC+14	PC+15	PC+16	PC+17	PC+18	PC+19	PC+20	PC+21	PC+22	PC+23	PC+24	PC+25	PC+26	PC+27	PC+28	PC+29	PC+30	PC+31	PC+32	PC+33	PC+34	PC+35	PC+36	PC+37	PC+38	PC+39	PC+40	PC+41	PC+42	PC+43	PC+44	PC+45	PC+46	PC+47	PC+48	PC+49	PC+50	PC+51	PC+52	PC+53	PC+54	PC+55	PC+56	PC+57	PC+58	PC+59	PC+60	PC+61	PC+62	PC+63	PC+64	PC+65	PC+66	PC+67	PC+68	PC+69	PC+70	PC+71	PC+72	PC+73	PC+74	PC+75	PC+76	PC+77	PC+78	PC+79	PC+80	PC+81	PC+82	PC+83	PC+84	PC+85	PC+86	PC+87	PC+88	PC+89	PC+90	PC+91	PC+92	PC+93	PC+94	PC+95	PC+96	PC+97	PC+98	PC+99	PC+100	PC+101	PC+102	PC+103	PC+104	PC+105	PC+106	PC+107	PC+108	PC+109	PC+110	PC+111	PC+112	PC+113	PC+114	PC+115	PC+116	PC+117	PC+118	PC+119	PC+120	PC+121	PC+122	PC+123	PC+124	PC+125	PC+126	PC+127	PC+128	PC+129	PC+130	PC+131	PC+132	PC+133	PC+134	PC+135	PC+136	PC+137	PC+138	PC+139	PC+140	PC+141	PC+142	PC+143	PC+144	PC+145	PC+146	PC+147	PC+148	PC+149	PC+150	PC+151	PC+152	PC+153	PC+154	PC+155	PC+156	PC+157	PC+158	PC+159	PC+160	PC+161	PC+162	PC+163	PC+164	PC+165	PC+166	PC+167	PC+168	PC+169	PC+170	PC+171	PC+172	PC+173	PC+174	PC+175	PC+176	PC+177	PC+178	PC+179	PC+180	PC+181	PC+182	PC+183	PC+184	PC+185	PC+186	PC+187	PC+188	PC+189	PC+190	PC+191	PC+192	PC+193	PC+194	PC+195	PC+196	PC+197	PC+198	PC+199	PC+200	PC+201	PC+202	PC+203	PC+204	PC+205	PC+206	PC+207	PC+208	PC+209	PC+210	PC+211	PC+212	PC+213	PC+214	PC+215	PC+216	PC+217	PC+218	PC+219	PC+220	PC+221	PC+222	PC+223	PC+224	PC+225	PC+226	PC+227	PC+228	PC+229	PC+230	PC+231	PC+232	PC+233	PC+234	PC+235	PC+236	PC+237	PC+238	PC+239	PC+240	PC+241	PC+242	PC+243	PC+244	PC+245	PC+246	PC+247	PC+248	PC+249	PC+250	PC+251	PC+252	PC+253	PC+254	PC+255	PC+256	PC+257	PC+258	PC+259	PC+260	PC+261	PC+262	PC+263	PC+264	PC+265	PC+266	PC+267	PC+268	PC+269	PC+270	PC+271	PC+272	PC+273	PC+274	PC+275	PC+276	PC+277	PC+278	PC+279	PC+280	PC+281	PC+282	PC+283	PC+284	PC+285	PC+286	PC+287	PC+288	PC+289	PC+290	PC+291	PC+292	PC+293	PC+294	PC+295	PC+296	PC+297	PC+298	PC+299	PC+300	PC+301	PC+302	PC+303	PC+304	PC+305	PC+306	PC+307	PC+308	PC+309	PC+310	PC+311	PC+312	PC+313	PC+314	PC+315	PC+316	PC+317	PC+318	PC+319	PC+320	PC+321	PC+322	PC+323	PC+324	PC+325	PC+326	PC+327	PC+328	PC+329	PC+330	PC+331	PC+332	PC+333	PC+334	PC+335	PC+336	PC+337	PC+338	PC+339	PC+340	PC+341	PC+342	PC+343	PC+344	PC+345	PC+346	PC+347	PC+348	PC+349	PC+350	PC+351	PC+352	PC+353	PC+354	PC+355	PC+356	PC+357	PC+358	PC+359	PC+360	PC+361	PC+362	PC+363	PC+364	PC+365	PC+366	PC+367	PC+368	PC+369	PC+370	PC+371	PC+372	PC+373	PC+374	PC+375	PC+376	PC+377	PC+378	PC+379	PC+380	PC+381	PC+382	PC+383	PC+384	PC+385	PC+386	PC+387	PC+388	PC+389	PC+390	PC+391	PC+392	PC+393	PC+394	PC+395	PC+396	PC+397	PC+398	PC+399	PC+400	PC+401	PC+402	PC+403	PC+404	PC+405	PC+406	PC+407	PC+408	PC+409	PC+410	PC+411	PC+412	PC+413	PC+414	PC+415	PC+416	PC+417	PC+418	PC+41
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(00278) 2
A70 066C2 42134214
(00279) 2
(00280) 2
(00281) 2
(00282) 2
A71 066C4 00000000
(00283) 2
A72 066C6 00400040
(00284) 2
A73 066C8 00000000
(00285) 2
A74 066CA 41124113
(00286) 2
(00287) 2
(00288) 2
(00289) 2
A75 066CC 85F085F0
(00290) 2
A76 066CE 00000000
(00291) 2
A77 066D0 00400040
(00292) 2
A78 066D2 00010002
(00293) 2
A79 066D4 00F700F7
(00294) 2
A7A 066D6 00F000F0
(00295) 2
A7B 066D8 00000000
(00296) 2
A7C 066DA 4F000001
(00297) 2
A7D 066DC 00F700F7
(00298) 2
A7E 066DE 02F02F0F
(00299) 2
A7F 066E0 85000500
(00300) 2
A80 066F2 00000000
(00301) 2
A81 066F4 00000000
(00302) 2
A82 066F6 00000000
(00303) 2
A83 066F8 00000000
(00304) 2
(00305) 2
(00306) 2
(00307) 2
066FA
END QUANSSZ
EJECT

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A1W 0672H 36H0002 (00352)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A1C 0672A 38H0002 (00353)	ADD(HR0,MS,TF)	IO=PDF(6)
A1D 0672C 3AD42010 (00355)	LOAD(HR1,PVFC56(2),TF)	IO=PTRH(0)
A1E 0672F 3C9A0002 (00356)	ADD(HR1,MS,TF)	IO=PTRH(1)
A1F 06730 3FE42204 (00357)	LOAD(HR3,PDF056(2),TF)	IO=PDFQ(0)
A20 06732 40H0002 (00358)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A21 06734 42H0002 (00359)	ADD(HR0,MS,TF)	IO=PDF(7)
A22 06736 44H42020 (00361)	LOAD(HR1,PVFC57(2),TF)	IO=PTRH(0)
A23 0673H 46H0002 (00362)	ADD(HR1,MS,TF)	IO=PTRH(1)
A24 0673A 48F42214 (00363)	LOAD(HR3,PDF057(2),TF)	IO=PDFQ(0)
A25 0673C 4AH0002 (00364)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A26 0673E 4CH0002 (00365)	ADD(HR0,MS,TF)	IO=PDF(8)
A27 06740 4E4202H (00367)	LOAD(HR1,PVFC58(2),TF)	IO=PTRH(0)
A28 06742 50H0002 (00368)	ADD(HR1,MS,TF)	IO=PTRH(1)
A29 06744 52F4221C (00369)	LOAD(HR3,PDF058(2),TF)	IO=PDFQ(0)
A2A 06746 540002H (00370)	SET(TAF1)	SET "END OF PARCOR" RIAS
A2B 0674H 56H0002 (00371)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A2C 0674A 58C203FA (00372)	LOAD(HR0,SAS52(1),TF)	IO=DC RIAS
A2D 0674C 5AD42030 (00374)	LOAD(HR1,DCS(2),TF)	IO=PTRH(0)
A2E 0674E 5CH0002 (00375)	ADD(HR1,MS,TF)	IO=PTRH(1)
A2F 06750 5F42224 (00376)	LOAD(HR3,DCS050(2),TF)	IO=PDFQ(0)
A30 06752 60H0002 (00377)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A31 06754 62C203FO (00378)	LOAD(HR0,SAS55(1),TF)	IO=VAR
A32 06756 64D42050 (00380)	LOAD(HR1,VAR5(2),TF)	IO=PTRH(0)
A33 0675H 66H0002 (00381)	ADD(HR1,MS,TF)	IO=PTRH(1)
A34 0675A 68F42244 (00382)	LOAD(HR3,VAR500(2),TF)	IO=PDFQ(0)
A35 0675C 6AH0002 (00383)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A36 0675E 6CC203FC (00385)	LOAD(HR0,SAS61(1),TF)	IO=PC
A37 06760 6E42090 (00386)	LOAD(HR1,PGS(2),TF)	IO=PTRH(0)
A38 06762 70H0002 (00387)	ADD(HR1,MS,TF)	IO=PTRH(1)
A39 06764 72F422H4 (00388)	LOAD(HR3,PGS050(2),TF)	IO=PDFQ(0)
A3A 06766 7430002H (00389)	SET(AF3)	GET READY TO TERMINATE
A3H 0676E 76H0002 (00390)	ADD(HR3,MS,TF,C)	IO=PDFQ(1),THEN PCI
A3C 0676A 78C203FF (00392)	LOAD(HR0,SAS62(1),TF)	IO=M
A3D 0676C 7A300037 (00393)	SET(M1)	STALL APS INPUT
A3E 0676F 7C000020 (00394)	NOP	RELEASED BY APU
A3F 06770 7FC4205H (00395)	LOAD(HR0,INSTR50(2),TF)	IO=INSTR50

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A40 06772 H0300037 (00396)
A41 06774 W264709R (00397)
A42 06776 N4500000 (00398) IN50
A43 06778 H729002H (00399)
A44 0677A H9300037 (00400)
A45 0677C H8000020 (00401)
A46 0677E H042004 (00402)
A47 06780 H4300037 (00403)
A48 06782 H064228C (00404)
A49 06784 H2500000 (00405) IN51
A4A 06786 H429002H (00406)
A4B 0678H H6A0002H (00407)
A4C 0678A H4600021 (00408) *
A4D 0678C H4500007 (00409) KAS
A4E 0678E H0400002 (00410)
A4F 06790 H19404H1 (00411) PINCS1
A50 06792 H0C7043A (00412)
A51 06794 A2200031 (00413) *
A52 06796 A4000020 (00414) *
A53 06798 H205540 (00415) DNE51
A54 0679A H005960 (00416) *
A55 0679C A4400002 (00417) *
A56 0679E AC9A0002 (00418) *
A57 067A0 A44A0002 (00419) *
A58 067A2 H0400002 (00420) *
A59 067A4 H2300037 (00421) *
A5A 067A6 H4000020 (00422) *
A5B 067A8 H60055AA (00423) *
A5C 067AA H8200000 (00424) *
A5D 067AC H820546A (00425) *
A5E 067AE H0307C40 (00426) *
A5F 067H0 H1500007 (00427) *
A60 067H2 C0400000 (00428) *
A61 067H4 C2700000 (00429) *
A62 067H6 C4070000 (00430) *
A63 067H8 C6660021 (00431) *
A64 067HA C00A0001 (00432) *
A65 067HC CAA00002 (00433) *
A66 067HE C11A4H1 (00434) *

SFT(W1)
LOAD(HR2,PITCHS(2))
LOAD(HR1,MSS)
ADD(HR2,HR1,TF)
SFT(W1)
NOP
LOAD(HR0,INSTRS1(2),TF)
SFT(W1)
LOAD(HR2,MSDFU(2))
LOAD(HR1,MSS)
ADD(HR2,HR1)
ADD(HR2,HR1,TF)
LOAD(HR0,PARCS-2(3))
LOAD(HR1,PSIZES)
ADD(HR0,MS,TF)
SUB(HR1,1),JUMPP(PINCS1)
LOAD(HR0,SASQ2(1),TF)
CLEAR(R1)
NOP

* O/D0 APS SUBROUTINE
JMN(G105SD,P2)
JUMP(FNTRY5)
ADD(HR1,MS,TF)
ADD(HR1,MS,TF)
ADD(HR3,MS,TF)
ADD(HR3,MS,TF)
SFT(W1)
NOP
JUMPC(ADD5,AF2)
ADD(HR2,MSS,NA,C)
JUMP(TUPSP1,AF2),CLEAR

JMN(G105S3,P3)
LOAD(HW1,PSIZES)
LOAD(HW0,(0))
LOAD(HW3,MSS)
SUB(HW0,MSS)
LOAD(HW2,PARCS-2(3))
ADD(HW0,HS,TF)
ADD(HW2,MS,TF)
SUB(HW1,1),JUMPP(#3)

SET OUTPUT PC(PC3)
P(.) SIZE-1
OPRM(.) HA
DUMMY OP FOR EXEC
OPRM(.) RA-1
P(.) RA-2
NQ=OPRM(1)
NQ=DEFO(OPRM(1))
FOR I=1,2...R

```

```

STALL APS INPUT
PITCH(.) RA
APU SETS MSS->M
IO=UTM=PITCH(M)
STALL APS INPUT
REFLASED BY APU
IO=INSTRS1
STALL APS INPUT
DEFO(.) RA
APU SETS MSS->DTM
DEFO(DTM)
IO=DRM=DEFO(2*DTM)

PARC(.) RA-2
PARC(.) SIZE-1
IO=PARC(1)
FOR I=0,1...7
IO=I,TH2

INPUT DONE!

SET OUTPUT PC(P2)
ENTER # STALL!
IO=D(K)
IO=D(K+1)
IO=E(K)
IO=E(K+1)
WAIT FOR THRESHOLD ...
DECISION
AF2=0 THEN K+2,K+3
AF2=1, THEN PC0->PC
RESET LOOP PC & AF2

SET OUTPUT PC(PC3)
P(.) SIZE-1
OPRM(.) HA
DUMMY OP FOR EXEC
OPRM(.) RA-1
P(.) RA-2
NQ=OPRM(1)
NQ=DEFO(OPRM(1))
FOR I=1,2...R

```


QUANTIZE/DEQUANTIZE SIDERAND

PAGE 18: FCW 242... "MDDDDP(V,U,V)"

AIS:	00012 (00006)	(00455)
ADST:	00055 (00421)	(00427)
AFIST:	00022 (00181)	(00188)
AFUTSUC:	00044 (00007)	(00054)
APSSMFM:	1FFC0 (00008)	(00448)
BIASS:	00008 (00160)	(00162)
CSPUSMIS:	021FC (00009)	(00057)
DCS:	02030 (00114)	(00174)
DCSDFU:	02224 (00025)	(00176)
DCVUS:	00009 (00010)	(00440)
DMSA:	00054 (00249)	
DMSI:	00051 (00415)	
DMSO:	0007A (00461)	
ENTHYS:	00059 (00420)	(00425)
G105S:	066P2 (00056)	(00314)
G105S1:	00053 (00321)	(00419)
G105S4:	0007C (00431)	(00465)
G105SA:	067P2 (00317)	(00470)
G105S1:	067P2 (00312)	(00474)
G105S0:	0005F (00419)	(00431)
G105S2:	00102 (00316)	(00475)
MS:	00001 (00012)	(00437)
MSO:	00042 (00140)	(00398)
MSI:	00049 (00141)	(00405)
INCSJ:	00005 (00159)	(00188)
INSTRS0:	020FH (00140)	(00395)
INSTRS1:	020FA (00141)	(00402)
KAS:	0004C (00409)	
KTOAS:	00039 (00213)	
LOOPS:	00006 (00165)	(00176)
MS:	00000 (00013)	
MS:	00000 (00014)	(00398)
MSDFO:	022HC (00028)	(00404)
MPOS:	0000A (00015)	(00450)
MOUANS:	00023 (00189)	
PARCS:	00023 (00030)	(00324)
PDFUS1:	02134 (00017)	(00327)
PDFUS2:	02174 (00014)	(00333)
PDFUS3:	021H4 (00019)	(00339)
PDFUS4:	021D4 (00020)	(00345)
PDFUS5:	021F4 (00021)	(00351)
PDFUS6:	02204 (00022)	(00357)
PDFUS7:	02214 (00023)	(00363)
PDFUSR:	0221C (00024)	(00369)
		(00452) (00449) (00435) (00434) (00428) (00405) (00405) (00467)

PGS:	02090 (00130) (00386)	
PGSDFO:	02284 (00027) (00388)	
PGPUS:	0000C (00016) (00448)	
PINGS1:	0004A (00411) (00412)	
PINGSJ:	00078 (00458) (00459)	
PITCHS:	02098 (00131) (00397)	
PS1ZAS:	00007 (00011) (00410)	(00432) (00456)
PVFC51:	01F40 (00068) (00325)	
PVFC52:	01F80 (00078) (00331)	
PVFC53:	01FC0 (00088) (00337)	
PVFC54:	01F80 (00094) (00341)	
PVFC55:	02000 (00100) (00349)	
PVFC56:	02010 (00104) (00355)	
PVFC57:	02020 (00108) (00361)	
PVFC58:	02028 (00111) (00367)	
UPHMS:	02F28 (00032) (00401)	(00442) (00444) (00450)
QUANS:	065F2 (00055) (00152)	
QUANSSA:	00000 (00150) (00154)	(00305)
QUANSSZ:	00084 (00151) (00305)	(00306)
QAS104:	00452 (00046)	
SAS11:	00398 (00033)	
SAS18:	0030F (00034) (00321)	
SAS19:	00300 (00035)	
SAS2:	004FA (00036) (00373)	
SAS5:	003F0 (00037) (00379)	
SAS6:	003FA (00038) (00457)	
SAS11:	003FC (00039) (00385)	
SAS62:	003FF (00040) (00392)	
SAS8:	00432 (00041) (00441)	
SAS80:	00434 (00042) (00443)	
SAS90:	00436 (00043) (00445)	
SAS91:	00434 (00044) (00453)	
SAS92:	0043A (00045) (00411)	
SCLMS:	00002 (00114) (00321)	
START:	065F0 (00047) (00144)	
SVTS:	00382 (00029) (00033)	(00034) (00035) (00036) (00037) (00038) (00039) (00040) (00041)
	(00042) (00043) (00044) (00045) (00046)	
TIS:	00017 (00172) (00177)	
T2S:	0001C (00173) (00182)	
TOPSP1:	00054 (00420) (00429)	
TOPSP2:	0005F (00432)	
TOPSP3:	00070 (00466) (00468)	
VAHS:	02050 (00120) (00380)	
VAHSDF0:	02244 (00026) (00382)	

VARPOS:	00000 (00040) (00442)
VPUS:	00000 (00040)
WS:	00002 (00050) (00326) (00328) (00330) (00332) (00334) (00336) (00338) (00340) (00342)
	(00344) (00346) (00348) (00350) (00352) (00354) (00356) (00358) (00360) (00362)
	(00364) (00366) (00368) (00370) (00372) (00374) (00376) (00378) (00380) (00382)
	(00384) (00386) (00388) (00390) (00392) (00394) (00396) (00398) (00400) (00402)
X23RD:	0006F (00227) (00228) (00277)
X4TH:	00069 (00229) (00268)
X5TH:	0006A (00230) (00267) (00269)
X6TH:	00062 (00232) (00259)
X7TH:	00064 (00234) (00258) (00261)
X8TH:	00050 (00235) (00253)
ZS:	00003 (00051)

DEQUANTIZE SIDERAND
W/ K->A TRANSFORM
ORIGINATED:13-AUG-79
UPDATED:16-MAY-80

FCH 243... "MPHOPP(Y,U,V)"

(000001) *

(000002) *

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DEFINE GLOBAL SYMBOLS

ATS=D*1R

AFTSORG=SRH

APSSMFM=STFC

CSPUSNUS=STFC

RM=3

HS=1

MS=0

WS=0

PARCS=D*3A

PSIZFS=D*H'-D*1

OPRMS=>OPRM

OPRMS=D*11R30

SVTS=S03R2

SAS11=SVTS+2*D*11

SAS30=SVTS+2*D*30

SAS60=SVTS+2*D*60

SASR9=SVTS+2*D*90

SASR9=SVTS+2*D*90

SAS90=SVTS+2*D*90

SAS91=SVTS+2*D*91

SAS92=SVTS+2*D*92

START=SRH00

WS=2

WS=3

EXPAND ARRAY FUNCTION DISPATCH TABLE

R1=AFTSORG+4*2*(243-12R)

ADDR DFQHS(R,1)

ADDR G106S(R,1)

ADDR CSPUSNUS(1,0)

JECT

PAGE 4: FCH 243... "MPDPP(V,U,V)" DE-QUANTIZED SINEWAVE

02100 04120441	(00068)	DATA	0.14075E+01,	0.15162E+01,	0.16545E+01,	0.18418E+01
02101 04120441						
02102 04120441						
02103 04120441						
02104 24120441	(00069) *					
02105 24120441	(00070) *	PARCOR(4) W/ 4 BITS				
02106 24120441	(00071) PDPC54	DATA	0.31411E+00,	0.50442E+00,	0.61992E+00,	0.75167E+00
02107 24120441						
02108 24120441						
02109 24120441						
02110 24120441						
02111 24120441	(00072)	DATA	0.485074E+00,	0.94305E+00,	0.10122E+01,	0.11205E+01
02112 24120441						
02113 24120441	(00073)	DATA	0.12094E+01,	0.13000E+01,	0.13929E+01,	0.14888E+01
02114 24120441						
02115 24120441	(00074)	DATA	0.15888E+01,	0.16951E+01,	0.18129E+01,	0.19500E+01
02116 24120441						
02117 24120441	(00075) *					
02118 24120441	(00076) *	PARCOR(5) W/ 3 BITS				
02119 24120441	(00077) PDPC55	DATA	0.44510E+00,	0.66865E+00,	0.82476E+00,	0.95821E+00
02120 24120441						
02121 24120441						
02122 24120441	(00078)	DATA	0.10859E+01,	0.12194E+01,	0.13739E+01,	0.15907E+01
02123 24120441						
02124 24120441	(00079) *					
02125 24120441	(00080) *	PARCOR(6) W/ 3 BITS				
02126 24120441	(00081) PDPC56	DATA	0.46200E+00,	0.68463E+00,	0.84172E+00,	0.97611E+00
02127 24120441						
02128 24120441						
02129 24120441						
02130 24120441	(00082)	DATA	0.11050E+01,	0.12400E+01,	0.13980E+01,	0.16206E+01
02131 24120441						
02132 24120441	(00083) *					
02133 24120441	(00084) *	PARCOR(7) W/ 2 BITS				

PAGE 5: 800 243... "MPDUP(V,U,V)" DEQUANTIZE SIDEHAND

02213 120DAC0	(00005) PDPCS7 DATA	0.19130E+00,	0.13761E+00,	0.10127E+01,	0.13204E+01	
02214 546A0140						
02215 041A0241						
02216 0A9020C1						
	(00006) *					
	(00007) *	PARCOURCH W/ 2 HITS				
0221C 4A004140	(00008) PDPCS8 DATA	0.57086E+00,	0.86541E+00,	0.11045E+01,	0.13815E+01	
0221E 6E05C140						
02220 04060331						
02222 06004E01						
	(00009) *					
	(00000) *	DC RIAS W/ 4 HITS				
	(00001) PCS	DATA	0.62203E+01,	0.18721E+00,	0.31311E+00,	0.44096E+00
02224 7E93780E						
02226 17E67E40						
02228 2815E340						
0222A 387180C0						
0222C 492848C0	(00002) DATA	0.57154E+00,	0.70609E+00,	0.84579E+00,	0.99277E+00	
0222E 5A617840						
02230 6C208C0						
02232 7E0783C0						
02234 092F1441	(00003) DATA	0.11475E+01,	0.13143E+01,	0.14964E+01,	0.16991E+01	
02236 0A935E01						
02238 08E8A001						
0223A 0097C1C1						
0223C 0E74E1C1	(00004) DATA	0.10319E+01,	0.22117E+01,	0.25759E+01,	0.31324E+01	
0223E 11018FC1						
02240 14087141						
02242 190E71C1						
	(00005) *					
	(00006) *	VARIANCE W/ 5 HITS				
	(00007) VARS	DATA	0.43600E+01,	0.68023E+01,	0.87943E+01,	0.10868E+02
02244 23AE1341						
02246 366E1C41						
02248 465880C1						
0224A 56E1A9C1						
0224C 69E90041	(00008) DATA	0.13247E+02,	0.15849E+02,	0.18140E+02,	0.20412E+02	
0224E 7E0AC041						
02250 09111E0C2						
02252 0A340C42						
02254 08381142	(00009) DATA	0.22410E+02,	0.24372E+02,	0.26458E+02,	0.28495E+02	
02256 0C2E90C2						
02258 003A9FC2						
0225A 0E4E5C32						
0225C 0F294742	(00100) DATA	0.30327E+02,	0.32035E+02,	0.33741E+02,	0.35525E+02	
0225E 10047AC2						

AD-A091 663

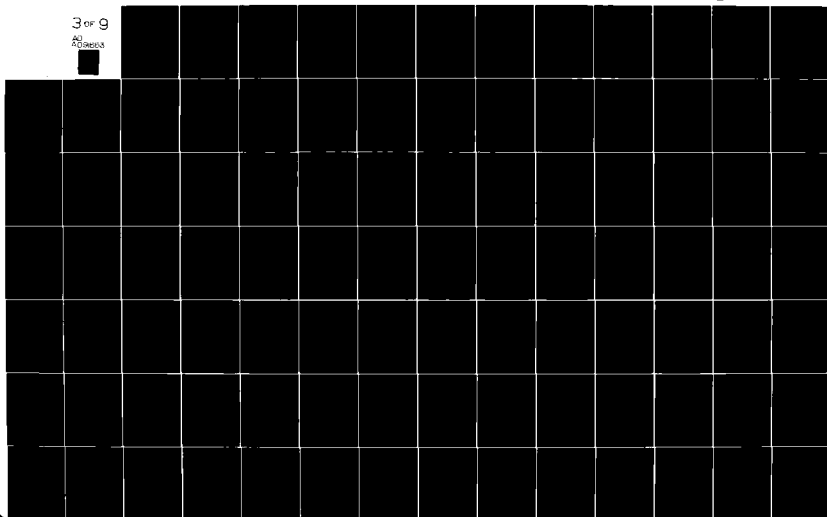
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

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PAGE 6: FCN 243... "MPDOPP(Y,U,V)" DEQUANTIZE SINDRAN

02260 100F0942			
02262 11C33342			
02264 12H70A42	(00101)	DATA 0.37430E+02, 0.39343E+02, 0.41065E+02, 0.42664E+02	
02266 13AF742			
02268 14RHS1C2			
0226A 154F0C2			
0226C 161CA42	(00102)	DATA 0.44222E+02, 0.45741E+02, 0.47224E+02, 0.48695E+02	
0226E 160F0942			
02270 179CA1C2			
02272 185RF5C2			
02274 19156042	(00103)	DATA 0.50167E+02, 0.51633E+02, 0.53034E+02, 0.54385E+02	
02276 19010642			
02278 1A45A42			
0227A 1P3147C2			
0227C 1HF10642	(00104)	DATA 0.55755E+02, 0.57223E+02, 0.58810E+02, 0.60709E+02	
0227E 1C9CRH42			
02280 1D67AF42			
02282 1E5AC0C2	(00105) *		
	(00106) *	PITCH GAIN W/ 2 BITS	
02284 32219640	(00107) PCAINS	DATA 0.59165E+00, 0.58430E+00, 0.74993E+00, 0.89805E+00	
02286 4ACA57C0			
02288 5FV0H4C0			
0228A 72F34040	(00108) *		
	(00109) *	PITCH W/ 6 BITS	
0228C 78000041	(00110) #SDFC	DATA 15.	
0228E 08000042	(00111)	DATA 16.,17.,18.,19.,20.,21.,22.,23.,24.,25.,26.,27.,28.,29.,30.,31.	
02290 08H00042			
02292 09000042			
02294 09H00042			
02296 0A000042			
02298 0AR00042			
0229A 0R000042			
0229C 0RH00042			
0229E 0C000042			
022A0 0CR00042			
022A2 0I000042			
022A4 0DR00042			
022A6 0F000042			
022A8 0FH00042			
022AA 0F000042			
022AC 0FR00042	(00112)	DATA 32.,33.,34.,35.,36.,37.,38.,39.,40.,41.,42.,43.,44.,45.,46.,47.	
022AF 10000042			

022H0 10R00042	
022H2 11R00042	
022H4 11R00042	
022H6 12R00042	
022H8 12R00042	
022HA 13R00042	
022HC 13R00042	
022HE 14R00042	
022H0 14R00042	
022C2 15R00042	
022C4 15R00042	
022C6 16R00042	
022C8 16R00042	
022CA 17R00042	
022CC 17R00042	
022CF 18R00042	
022D0 18R00042	
022D2 19R00042	
022D4 19R00042	
022D6 1A000042	
022DH 1A000042	
022DA 1B000042	
022DC 1B000042	
022DE 1C000042	
022E0 1C000042	
022E2 1D000042	
022E4 1D000042	
022E6 1E000042	
022E8 1E000042	
022EA 1F000042	
022EC 1F000042	
022EE 20R00042	
022F0 21R00042	
022F2 22R00042	
022F4 23R00042	
022F6 24R00042	
022F8 25R00042	
022FA 26R00042	
022FC 27R00042	
022FE 28R00042	
02300 29R00042	
02302 2AR00042	
02304 2AR00042	
02306 2CR00042	

(00113) DATA 4R..49..50..51..52..53..54..55..56..57..58..59..60..61..62.

(00114) DATA 63..65..67..69..71..73..75..77..79..81..83..85..87..89..91..93.

PAGE 11: FCH 243... "APDQPP(V,U,V)" DEQUANTIZE SIDEHAND

02308 20000042		
0230A 20000042	(00115)	EVFN
0230C 5450	(00116)	INSTRSO DATA INSO*0'S12'+X'50',X'0000'
0230D 0000	(00117)	RHS 1
	(00118)	PI-START
00000600	(00119)	PI-CT

(00120) *	FOR TIMING PULSE OR CSPII
(00121) *	R=1.0
(00122) *	A1=1.0(MIAS FOR PARCORS)
(00123) *	LEVEL->INSTR0+1
(00124)	WAIT FOR OUTPUT TO CLEAR
(00125)	RELEASE APS INPUT
(00126)	RELEASE APS OUTPUT
(00127)	OVERWRITE INSO
(00128)	WAIT FOR OUTPUT TO CLEAR
(00129)	RELEASE APS INPUT
(00130)	A0=DEQ(1)
(00131)	END OF PARCORS?
(00132)	YES.REMOVE RIAS
(00133)	DEQ(1)-BIAS
(00134)	RELEASE APS OUTPUT
(00135)	NO=D(OPRM(1))
(00136)	FOR K(J),J=1,...B2DC,VAR
(00137)	.PG,M
(00138)	RELEASE APS INPUT
(00139)	
(00140)	P(1)
(00141)	MAKE IT K(1)
(00142)	K(1)*2 FOR ENERGY
(00143)	SAVE K(1)
(00144)	SO SQUARE IT
(00145)	NEED THIS AROUND, TON
(00146)	P(2)
(00147)	1.0
(00148)	K(1)*2
(00149)	1.0-K(1)*2
(00150)	PROD(1),GET K(2)
(00151)	K(2)
(00152)	DO 2ND ITERATION (M=2)
(00153)	DO 3RD ITER (M=3)
(00154)	
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(00199)	
(00200)	

A22 06846 30000042 (00164)	CALL(X4TH)	GET A(5)(3) READY
A23 06848 30000043 (00165)	CALL(X5TH)	M=6
A24 0684A 00000260 (00166)	MOV(R,A3)	M=7
A25 0684C 3000003H (00167)	CALL(X6TH)	GET A(7)(4) READY
A26 0684F 3000003D (00168)	CALL(X7TH)	LTH2 (=SA)
A27 06850 00000280 (00169)	MOV(R,A4)	PROD(R)*LTH2
A28 06852 30000036 (00170)	CALL(X8TH)	OUTPUT ENERGY = LTH2*PRD
A29 06854 08C00000 (00171)	MOV(IQ,M3)\NOP	D(R)
A2A 06856 85A00000 (00172)	MUL(M3,M5)\NOP	A(1) OUT
A2B 0685H 02200000 (00173)	R(A1)\NOP	A(2) OUT
A2C 0685A 08AC0000 (00174)	MOV(P,IQ)\NOP	A(3) OUT
		A(4) OUT,A(5) OUT
A2D 0685C 025C0000 (00176)	MOV(00),R(A2)\NOP	A(6) OUT
A2E 0685F 027C0000 (00177)	MOV(00),P(A3)\NOP	A(7) OUT
A2F 06860 029C0280 (00178)	MOV(00),P(A4)\R(A4)	A(8) OUT
A30 06862 089C027C (00179)	MOV(R,00)\MOV(00),R(A3)	
A31 06864 0000025C (00180)	NOP\MOV(00),P(A2)	
A32 06866 0000023C (00181)	NOP\MOV(00),R(A1)	
A33 06868 0000089C (00182)	NOP\MOV(R,00)	
A34 0686A 20320000 (00184)	CFAR(RA)\NOP	APU DONE!
A35 0686C 00000600 (00185)	NOP	
		\A(7)(4)
A36 0686E 0000089H (00188)	* K->A APU SURROUTINES	A(7)(4)
A37 06870 08540854 (00189)	NOP\MOV(R,FXU)	A(M-1)(M-4)*K(M) ((M=R))
A38 06872 85800000 (00190)	MOV(FXI,A4)	
A39 06874 08800880 (00191)	MUL(M3,M4)\NOP	
	MOV(P,A0)	
A3A 06876 1000003D (00192)	* JUMP(X7TH)	
A3B 06878 00000898 (00194)	NOP\MOV(R,FXU)	A(5)(3)
A3C 0687A 08530000 (00195)	MOV(FXI,A3)\NOP	A(5)(3)
A3D 0687C 85608560 (00196)	MUL(M2,M7)\MUL(M2,M7)	A(M-1)(M-3)*K(M)\A(M-1)(3)*K(M)
		SUM FOR A(M)(4)
A3E 0687E 44000000 (00197)	ADD(A0,A4)\NOP	STORE A(M)(4)
A3F 06880 08940000 (00199)	MOV(R,A4)\NOP	
A40 06882 08800880 (00200)	MOV(P,A0)	
A41 06884 10000043 (00202)	* JUMP(X5TH)	
A42 06886 08520000 (00203)	MOV(FXI,A2)\NOP	A(1)(2)
A43 06888 84F084F0 (00204)	MUL(M1,M7)\MUL(M1,M7)	A(M-1)(M-2)*K(M)\A(M-1)(2)*K(M)
		SUM FOR A(M)(3)\A(M)(M-3)
A44 0688A 43004300 (00206)	ADD(A0,A3)\ADD(A0,A3)	

INFLUENZ: SIDEHAND

A45	06RHC	00R00000M	(00270H)	MOV(R,FX0)\MOV(R,FX0)	STORE A(M)(M-3)\A(M)(3)
A46	06RHF	004C'004A	(00200Q)	MOV(FXT,M4)\MOV(FXT,M2)	
A47	06R90	00R000000	(00210)	MOV(P,A0)	
			(00211) 0		
A48	06R92	04K000460	(00212) X23H0	MUL(M0,M7)\MUL(M0,M7)	A(M-1)(M-1)*K(M)\A(M-1)(1)*K(M)
			(00214) :		STORE A(M)(3),SUM FOR A(M)(2)\A(M)(M-3),SUM A(M)(M-2)
A49	06R94	47134214	(00215) :	MOV(A3),ADD(A0,A2)\MOV(A4),ADD(A0,A2)	
			(00216) :		
A4A	06R96	00R00000M	(00217)	MOV(R,FX0)\MOV(R,FX0)	STORE A(M)(M-2)\A(M)(2)
A4B	06R98	00A400R40	(0021H)	MOV(FXT,M2)\MOV(FXT,M1)	
A4C	06R9A	00R000000	(00214)	MOV(P,A0)\MOV(P,A0)	STORE A(M)(2),SUM FOR A(M)(1)\A(M)(M-2),SUM A(M)(M-1)
A4D	06R9C	41124113	(00220) :	MOV(A2),ADD(A0,A1)\MOV(A3),ADD(A0,A1)	
			(00221) :		
			(00222) :		
			(00223) *		
A4F	06R9E	05F005F0	(00224)	MUL(M3,M7)	K(M)^2
A4F	06RA0	00R00000R	(00225)	MOV(R,FX0)\MOV(R,FX0)	
A50	06RA2	00R000R40	(00226)	MOV(FXT,M1)\MOV(FXT,M0)	A(M)(M-1)\A(M)(1)
A51	06RA4	000100R2	(00227)	MOV(R,A1)\MOV(R,A2)	A(M)(1)\A(M)(M-1)
A52	06RA6	00F700F7	(0022H)	MOV(T0A,A7)	-A(M)(M) (=P(M))
A53	06RA8	00F000F0	(00229)	NFG(A7)	MAKE IT REFIN COEF
A54	06RAA	00R600R6	(00230)	MOV(P,A6)	K(M)^2
A55	06RAC	4FAR00R4	(00231)	MOV(M0),SUR(A5,A6)\MOV(R,A1)	K(M)=A(M)(M),1-K(M)-2
A56	06RAE	00D700D7	(00232)	MOV(I0,A7)	P(M+1)
A57	06RH0	02F002ER	(00233)	MOV(M3),R(A7)	1-K(M)^2,GET K(M+1)
A59	06RH4	00A100AD	(00235)	MOV(P,M5)	(1-K(M)-2)*PROD
A5A	06RH6	00R000RR	(00236)	MOV(R,M3)	NEW PROD
A5R	06RR8	00RF00RF	(00237)	MOV(R,M7)	K(M+1)
				RETURN	K(M+1)
A5C	06RHA	0000A000	(0023H)		
		00000050	(00239)	DEQUSS2=BA-DFOUSSA	
			(00240)	END DEQUSSZ	
			(00241)	FJECT	
	06RHC				

A1F 06900 3C500007 (00284)	LOAD(RW1,PSIZES)	PARC(.) SIZE=1
A1F 06902 4F400007 (00287)	ADD(RW0,MS,TF)	IO=PARC(1)
A20 06904 40191F41 (00288)	SUBL(RW1,1),JUMPP(PINCS1)	FOR I=0,1...7
A21 06906 42C2043A (00289)	LOAD(RW0,SAS92(1),TF)	IO=I*H7
A22 06908 44200031 (00291)	DNFST	INPUT DONE!
A23 0690A 46000020 (00292)	NOP	
A24 0690C 48202F40 (00293)	JSP(G106SD,P2)	SET OUTPUT PC(P2)
A25 0690E 4A300037 (00295)	TOPSP1	STALL APS INPUT
A26 06910 4C000020 (00296)	NOP	RELEASED BY API
A27 06912 4FF4230C (00297)	LOAD(RW3,INSTRS0(2),TF)	IO=INSTRS0
A28 06914 50300037 (00298)	SFT(W1)	STALL APS INPUT
A29 06916 52000020 (00299)	NOP	RELEASED BY API
A2A 06918 54500000 (00300)	INSD	API SFTS MSS->LEVEL
A2B 0691A 5629002H (00301)	LOAD(RW1,MSS)	D(OPRM(1))
A2C 0691C 58A9002H (00302)	ADDR(RW2,RW1)	IO=D(OPRM(21))
A2D 0691E 5A3F0000 (00303)	ADD(RW3,MSS,NA,C)	FOR PC1->PC0
A2E 06920 5C002560 (00304)	JUMP(TOPSP1)	RESET TOP-OF-I,NOOP
A2F 06922 5F304640 (00305)	JSP(G106S3,P3)	SET OUTPUT PC(PC3)
A30 06924 60400000 (00307)	LOAD(RW0,T0)	PARC(.) HA
A31 06926 62500000 (00308)	LOAD(RW1,MSS)	PARC(.) SIZE=1
A32 06928 64020000 (00309)	SUB(RW0,MSS)	PARC(.) HA=1
A33 0692A 663F0000 (00310)	PARCS0	PC3->PC
A34 0692C 68A00002 (00311)	ADD(RW0,MS,TF)	NO=PARC(1)
A35 0692E 6A1133H1 (00312)	SUBL(RW1,1),JUMPP(PARCS0)	ETC FOR I=1,2...8
A36 06930 6C300029 (00313)	SFT(W1)	SET "END-OF-PARCS0"
A37 06932 6F3F0000 (00314)	VARSD	PC3->PC
A38 06934 70C20434 (00315)	LOAD(RW0,SASHQ(1),TF)	NO=D(OTVAR)=DTVAR
A39 06936 723F0000 (00316)	ADD(RW3,MSS,NA,C)	PC3->PC
A3A 06938 73C20432 (00317)	LOAD(RW0,SASHQ(1),TF)	NO=D(OTDC)=DTDC
A3B 0693A 763F0000 (00318)	MSD	PC3->PC
A3C 0693C 78C20438 (00319)	LOAD(RW0,SASHQ(1),TF)	NO=D(OTM)=DTM
A3D 0693E 7A3F0000 (00320)	PGSD	PC3->PC
A3F 06940 7C200436 (00321)	LOAD(RW0,SASHQ(1),TF)	NO=D(OTG)=DTG
A3F 06942 7F460010 (00322)	*	
A40 06944 80500007 (00323)	LOAD(RW0,AIS-2(3))	A1(.) BA=2
A41 06946 82F203FA (00324)	LOAD(RW1,PSIZES)	A1(.) SIZE=1
A42 06948 84A00002 (00325)	LOAD(RW2,SASHQ(1),TF)	NO=PR0D(R)=ENG
A43 0694A 861142H1 (00326)	PINCSJ	NO=A(J)
A44 0694C 88200030 (00327)	SUBL(RW1,1),JUMPP(PINCSJ)	FOR J=0,1...7
A44 0694C 88200030 (00328)	DNFSD	OUTPUT DONE!

A45	0694F	HA000020	(00330)		NIP	
			(00331)	*		
A46	06950	MC300032	(00332)	G10A51	SET(MA)	
A47	06952	81642300	(00333)	TUPSP3	LOAD(HW2,INSTRSU+1(2),TF)	ENABLE APU
A48	06954	90200030	(00334)		CLFAR(RU)	OVERWRITE INSTRSU+1
			(00335)		NIP	STALL APS OUTPUT
A49	06956	97000020	(00336)		CLFAR(RU)	RELEASED BY APU
A4A	06958	9443F5C0	(00337)		CLFAR(RU)	OVERWRITE INSO
A4M	0695A	96200030	(00338)		ADD(HW3,MSS,NA,C)	STALL APS OUTPUT
A4C	0695C	943B0000	(00339)		JUMP(TUPSP3)	PC2->PC
A4D	0695F	9A004760	(00340)	*		RESET TOP-OF-LOOP
			(00341)		G10A5A=8C	
		00006974	(00342)	3		ASSIGN VALUE TO CHAIN AN
			(00343)			CHOR
06960			(00344)	2	END BA=1	
			(00345)		STORAGE HURICK FOR CONSTRUCTED INSTRUCTIONS	
06960	00000000		(00346)	G10A51	DATA IF'0.0'	
	0000009F		(00347)		G10A57=81-G10A5	
06962					END	

AIS:	00017 (00006)	(00323)
AFDSORG:	00067 (00007)	(00032)
APSSBFW:	1FEC0 (00000)	(00336)
RTASS:	00000 (00139)	(00141)
CSPHNS:	021FC (00009)	(00035)
ICS:	02224 (00091)	(00276)
DCS0:	00039 (00116)	
DFUUS:	00002 (00033)	(00126)
DFUUS5A:	00000 (00124)	(00128)
DFUUS57:	00050 (00125)	(00239) (00240)
DNFSA:	00014 (00184)	
DNF51:	00022 (00291)	
DNF50:	00044 (00329)	
G1065:	00004 (00034)	(00248) (00254) (00346)
G10651:	00024 (00255)	(00294)
G10653:	00046 (00306)	(00332)
G1065A:	00024 (00251)	(00341)
G10651:	00060 (00246)	(00345)
G10650:	0002F (00294)	(00306)
G1065Z:	0000F (00250)	(00346)
MS:	00001 (00011)	(00259) (00261) (00263) (00265) (00267) (00269) (00271) (00273) (00275)
INS0:	0002A (00116)	(00279) (00280) (00282)
INCSJ:	00003 (00131)	(00300)
INSTRSD:	0230C (00116)	(00144) (00297) (00333)
KAS:	00010 (00285)	
KT0AS:	00012 (00148)	
MS:	00000 (00012)	
MS5:	00000 (00013)	(00300) (00303) (00308) (00309) (00310) (00314) (00316) (00318) (00320)
MS5:	00000 (00338)	
MS0FC:	0228C (00110)	(00278)
MS0:	0001A (00318)	
PARCS:	00023 (00014)	(00285)
PARCS0:	00033 (00310)	(00312)
PDFCS1:	02134 (00045)	(00258)
PDFCS2:	02174 (00055)	(00260)
PDFCS3:	02144 (00065)	(00262)
PDFCS4:	02104 (00071)	(00264)
PDFCS5:	02154 (00077)	(00266)
PDFCS6:	02204 (00081)	(00268)
PDFCS7:	02214 (00085)	(00270)
PDFCS8:	0221C (00088)	(00272)
PGS0:	00030 (00320)	
PGAINS:	02284 (00107)	(00281)

PINCS1: 0001F (00287) (00288)
 PIMCSJ: 00042 (00326) (00327)
 PS1ZFS: 00007 (00015) (00286) (00324)
 QPRMS: 02F36 (00017) (00257)
 SAS11: 00398 (00019)
 SAS30: 00300 (00026)
 SAS60: 003FA (00021) (00325)
 SAS80: 00432 (00022) (00317)
 SASR0: 00434 (00023) (00315)
 SAS90: 00436 (00024) (00321)
 SAS91: 00438 (00025) (00319)
 SASQ2: 0043A (00026) (00289)
 SCLMS: 00000 (00248) (00255)
 START: 06800 (00027) (00118)
 SVTS: 00342 (00018) (00019)
 TOPSP1: 00025 (00295) (00304)
 TOPSP3: 00047 (00133) (00339)
 VARS: 02244 (00097) (00274)
 VARS0: 00037 (00314)
 WS: 00002 (00028) (00287) (00311) (00326)
 X23MD: 00048 (00162) (00163) (00212)
 X4TH: 00042 (00164) (00203)
 X5TH: 00043 (00165) (00202) (00204)
 X6TH: 00034 (00167) (00194)
 X7TH: 00030 (00168) (00193) (00196)
 X8TH: 00036 (00170) (00188)
 ZS: 00003 (00029)

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PAGE 1: FOR 244... "MPMWCX(Y,U,V,W)" EXTRACT M,PG,MOVE X
(00001) * FOR 244... "MPMWCX(Y,U,V,W)" EXTRACT M,PG,MOVE X
(00002) * ORIGINATED:04-OCT-79
(00003) * UPDATED:29-MAY-80
(00004) * Y R10 IS AUTOCORRELATIONS, USED AS BOTH OUTPUT AND INPUT.
(00005) * THESE ARE MOVED FROM BUFFER X1R INTO Y.
(00006) * U R10 IS INPUT SAMPLES FROM THE A/D. THE LAST
(00007) * FEW POINTS OF THIS BUFFER ARE MOVED TO THE BEGINNING OF BUFFER
(00008) * X0M, TO PROVIDE FRAME OVERLAP.
(00009) * V R10 IS THE ARRAY OF PARCOR COEFFICIENTS (OUTPUT)
(00010) * W R10 IS ARRAY OF PREDICT COEFFICIENTS(OUTPUT BUT UNUSED
(00011) * SUBSEQUENTLY).
(00012) *
(00013) * THIS ROUTINE FINDS THE LARGEST AUTOCORRELATION IN THE RANGE 0
(00014) * INTERSTAND STORES IT IN THE PITCH PERIOD. IT ALSO
(00015) * CALCULATES THE PITCH GAIN R(PITCH)/R(0). FINALLY, IT FINDS
(00016) * PARCOR COEFFICIENTS USING THE WMLD FUNCTION.
(00017) * DEFINE GLOBAL SYMBOLS
(00018) * APTSORC=SRPR
(00019) * CSPUSMUS=S21FC
(00020) * DAYS=S794
(00021) * DPT=S1H
(00022) * W=3
(00023) * HS=1
(00024) * LIMITS=D'94
(00025) * MSS=0
(00026) * NPS=D'10
(00027) * OFFSETS=D'15
(00028) * RANGES=LIMITS-OFFSETS
(00029) * SVTS=S01H2
(00030) * SAS39=SVTS+2*D'39
(00031) * SAS61=SVTS+2*D'61
(00032) * SAS62=SVTS+2*D'62
(00033) * SAS66=SVTS+2*D'66
(00034) * START=S6CH0
(00035) * PS=2
(00036) * ZS=1
(00037) * DEFINE HARD WOULD BUFFERS
(00038) * X1PS=D'204H
(00039) * X0MS=D'206H
(00040) *
(00041) * EXPAND ARRAY FUNCTION DISPATCH TABLE
(00042) * X1=AFDISORC+1+2*(244-128)
(00043) * ADDR MWCXS(07,1)
(00044) * ADDR G110S(07,1)

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FOR 244

PAGE 2: FCH 244... "MPMNGI(Y,H,V,W)" EXTRACT M,PG,MOVE X

00RA4 0010215C (00045) ADDM CSPUSMIS(,1,0)
(00046) FJFCT

EXTRACT M, PG, MOVE X

000004CR0 (00047) *

78000041 (00048) *

00000041 (00049) *

00000041 (00050) *

00000041 (00051) *

00000041 (00052) *

00000041 (00053) *


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A23 06CCA 00000000C MXS6  NOP\MOV(R,00)
A24 06CCC 10000003R  JUMP(INVSR)

A25 06CCE 020000000 MXS3  SURROUTINES:MXS3,MXS4
A26 06CCE 040000000 MXS3  R(A4)\NOP
A27 06CCE 060000000 MXS3  MOV(R,A4)\NOP
A28 06CCE 080000000 MXS3  RETURN
A29 06CCE 0A0000000 MXS4  NOP\MOV(R,A4)
A30 06CCE 0C0000000 MXS4  NOP\MOV(R,A6)
A31 06CCE 0E0000000 MXS4  RETURN
A32 06CCE 100000000 MXS4  INVERSE SURR
A33 06CCE 120000000 MXS4  R(2)
A34 06CCE 140000000 MXS4  MOV(P,A0)
A35 06CCE 160000000 MXS4  MOV(A1),SUR(A1,A0)
A36 06CCE 180000000 MXS4  MOV(R,M6)
A37 06CCE 1A0000000 MXS4  MUL(M1,M6)
A38 06CCE 1C0000000 MXS4  MOV(P,M1)
A39 06CCE 1E0000000 MXS4  MOV(A4),MUL(M1,M5)
A40 06CCE 200000000 MXS4  MOV(P,A0)
A41 06CCE 220000000 MXS4  SUR(A1,A0)
A42 06CCE 240000000 MXS4  MOV(R,M6)
A43 06CCE 260000000 MXS4  MUL(M1,M6)
A44 06CCE 280000000 MXS4  RETURN
A45 06CCE 2A0000000 MXS4  CLEAR(W1)\NOP
A46 06CCE 2C0000000 MXS4  MOV(IQ,A0)\NOP
A47 06CCE 2E0000000 MXS4  PCP(A0)\NOP
A48 06CCE 300000000 MXS4  MOV(IQ,A5)\NOP
A49 06CCE 320000000 MXS4  CALL(INV)
A50 06CCE 340000000 MXS4  MOV(IQ,M5)\NOP
A51 06CCE 360000000 MXS4  MOV(P,M0)\NOP
A52 06CCE 380000000 MXS4  MUL(M0,M5)\NOP
A53 06CCE 3A0000000 MXS4  MOV(P,00)\NOP
A54 06CCE 3C0000000 MXS4  I=0,1,2...NP-1
A55 06CCE 3E0000000 MXS4  MOV(IQ,M7)
A56 06CCE 400000000 MXS4  MOV(IQ,A2)\NOP
A57 06CCE 420000000 MXS4  NOP\MOV(IQ,A2)
A58 06CCE 440000000 MXS4  MOV(R,A2)
A59 06CCE 460000000 MXS4  MOV(R,M0)
A60 06CCE 480000000 MXS4  MOV(R,M0)
A61 06CCE 4A0000000 MXS4  MOV(R,M0)
A62 06CCE 4C0000000 MXS4  MOV(R,M0)
A63 06CCE 4E0000000 MXS4  MOV(R,M0)
A64 06CCE 500000000 MXS4  MOV(R,M0)
A65 06CCE 520000000 MXS4  MOV(R,M0)
A66 06CCE 540000000 MXS4  MOV(R,M0)
A67 06CCE 560000000 MXS4  MOV(R,M0)
A68 06CCE 580000000 MXS4  MOV(R,M0)
A69 06CCE 5A0000000 MXS4  MOV(R,M0)
A70 06CCE 5C0000000 MXS4  MOV(R,M0)
A71 06CCE 5E0000000 MXS4  MOV(R,M0)
A72 06CCE 600000000 MXS4  MOV(R,M0)
A73 06CCE 620000000 MXS4  MOV(R,M0)
A74 06CCE 640000000 MXS4  MOV(R,M0)
A75 06CCE 660000000 MXS4  MOV(R,M0)
A76 06CCE 680000000 MXS4  MOV(R,M0)
A77 06CCE 6A0000000 MXS4  MOV(R,M0)
A78 06CCE 6C0000000 MXS4  MOV(R,M0)
A79 06CCE 6E0000000 MXS4  MOV(R,M0)
A80 06CCE 700000000 MXS4  MOV(R,M0)
A81 06CCE 720000000 MXS4  MOV(R,M0)
A82 06CCE 740000000 MXS4  MOV(R,M0)
A83 06CCE 760000000 MXS4  MOV(R,M0)
A84 06CCE 780000000 MXS4  MOV(R,M0)
A85 06CCE 7A0000000 MXS4  MOV(R,M0)
A86 06CCE 7C0000000 MXS4  MOV(R,M0)
A87 06CCE 7E0000000 MXS4  MOV(R,M0)
A88 06CCE 800000000 MXS4  MOV(R,M0)
A89 06CCE 820000000 MXS4  MOV(R,M0)
A90 06CCE 840000000 MXS4  MOV(R,M0)
A91 06CCE 860000000 MXS4  MOV(R,M0)
A92 06CCE 880000000 MXS4  MOV(R,M0)
A93 06CCE 8A0000000 MXS4  MOV(R,M0)
A94 06CCE 8C0000000 MXS4  MOV(R,M0)
A95 06CCE 8E0000000 MXS4  MOV(R,M0)
A96 06CCE 900000000 MXS4  MOV(R,M0)
A97 06CCE 920000000 MXS4  MOV(R,M0)
A98 06CCE 940000000 MXS4  MOV(R,M0)
A99 06CCE 960000000 MXS4  MOV(R,M0)
A100 06CCE 980000000 MXS4  MOV(R,M0)
A101 06CCE 9A0000000 MXS4  MOV(R,M0)
A102 06CCE 9C0000000 MXS4  MOV(R,M0)
A103 06CCE 9E0000000 MXS4  MOV(R,M0)
A104 06CCE A00000000 MXS4  MOV(R,M0)
A105 06CCE A20000000 MXS4  MOV(R,M0)
A106 06CCE A40000000 MXS4  MOV(R,M0)
A107 06CCE A60000000 MXS4  MOV(R,M0)
A108 06CCE A80000000 MXS4  MOV(R,M0)
A109 06CCE AA0000000 MXS4  MOV(R,M0)
A110 06CCE AC0000000 MXS4  MOV(R,M0)
A111 06CCE AE0000000 MXS4  MOV(R,M0)
A112 06CCE B00000000 MXS4  MOV(R,M0)
A113 06CCE B20000000 MXS4  MOV(R,M0)
A114 06CCE B40000000 MXS4  MOV(R,M0)
A115 06CCE B60000000 MXS4  MOV(R,M0)
A116 06CCE B80000000 MXS4  MOV(R,M0)
A117 06CCE BA0000000 MXS4  MOV(R,M0)
A118 06CCE BC0000000 MXS4  MOV(R,M0)
A119 06CCE BE0000000 MXS4  MOV(R,M0)
A120 06CCE C00000000 MXS4  MOV(R,M0)
A121 06CCE C20000000 MXS4  MOV(R,M0)
A122 06CCE C40000000 MXS4  MOV(R,M0)
A123 06CCE C60000000 MXS4  MOV(R,M0)
A124 06CCE C80000000 MXS4  MOV(R,M0)
A125 06CCE CA0000000 MXS4  MOV(R,M0)
A126 06CCE CC0000000 MXS4  MOV(R,M0)
A127 06CCE CE0000000 MXS4  MOV(R,M0)
A128 06CCE D00000000 MXS4  MOV(R,M0)
A129 06CCE D20000000 MXS4  MOV(R,M0)
A130 06CCE D40000000 MXS4  MOV(R,M0)
A131 06CCE D60000000 MXS4  MOV(R,M0)
A132 06CCE D80000000 MXS4  MOV(R,M0)
A133 06CCE DA0000000 MXS4  MOV(R,M0)
A134 06CCE DC0000000 MXS4  MOV(R,M0)
A135 06CCE DE0000000 MXS4  MOV(R,M0)
A136 06CCE E00000000 MXS4  MOV(R,M0)
A137 06CCE E20000000 MXS4  MOV(R,M0)
A138 06CCE E40000000 MXS4  MOV(R,M0)
A139 06CCE E60000000 MXS4  MOV(R,M0)
A140 06CCE E80000000 MXS4  MOV(R,M0)
A141 06CCE EA0000000 MXS4  MOV(R,M0)
A142 06CCE EC0000000 MXS4  MOV(R,M0)
A143 06CCE EE0000000 MXS4  MOV(R,M0)
A144 06CCE F00000000 MXS4  MOV(R,M0)
A145 06CCE F20000000 MXS4  MOV(R,M0)
A146 06CCE F40000000 MXS4  MOV(R,M0)
A147 06CCE F60000000 MXS4  MOV(R,M0)
A148 06CCE F80000000 MXS4  MOV(R,M0)
A149 06CCE FA0000000 MXS4  MOV(R,M0)
A150 06CCE FC0000000 MXS4  MOV(R,M0)
A151 06CCE FE0000000 MXS4  MOV(R,M0)
A152 06CCE 000000000 MXS4  MOV(R,M0)
A153 06CCE 020000000 MXS4  MOV(R,M0)
A154 06CCE 040000000 MXS4  MOV(R,M0)
A155 06CCE 060000000 MXS4  MOV(R,M0)
A156 06CCE 080000000 MXS4  MOV(R,M0)
A157 06CCE 0A0000000 MXS4  MOV(R,M0)
A158 06CCE 0C0000000 MXS4  MOV(R,M0)
A159 06CCE 0E0000000 MXS4  MOV(R,M0)
A160 06CCE 100000000 MXS4  MOV(R,M0)
A161 06CCE 120000000 MXS4  MOV(R,M0)
A162 06CCE 140000000 MXS4  MOV(R,M0)
A163 06CCE 160000000 MXS4  MOV(R,M0)
A164 06CCE 180000000 MXS4  MOV(R,M0)
A165 06CCE 1A0000000 MXS4  MOV(R,M0)
A166 06CCE 1C0000000 MXS4  MOV(R,M0)
A167 06CCE 1E0000000 MXS4  MOV(R,M0)
A168 06CCE 200000000 MXS4  MOV(R,M0)
A169 06CCE 220000000 MXS4  MOV(R,M0)
A170 06CCE 240000000 MXS4  MOV(R,M0)
A171 06CCE 260000000 MXS4  MOV(R,M0)
A172 06CCE 280000000 MXS4  MOV(R,M0)
A173 06CCE 2A0000000 MXS4  MOV(R,M0)
A174 06CCE 2C0000000 MXS4  MOV(R,M0)
A175 06CCE 2E0000000 MXS4  MOV(R,M0)
A176 06CCE 300000000 MXS4  MOV(R,M0)
A177 06CCE 320000000 MXS4  MOV(R,M0)
A178 06CCE 340000000 MXS4  MOV(R,M0)
A179 06CCE 360000000 MXS4  MOV(R,M0)
A180 06CCE 380000000 MXS4  MOV(R,M0)
A181 06CCE 3A0000000 MXS4  MOV(R,M0)
A182 06CCE 3C0000000 MXS4  MOV(R,M0)
A183 06CCE 3E0000000 MXS4  MOV(R,M0)
A184 06CCE 400000000 MXS4  MOV(R,M0)
A185 06CCE 420000000 MXS4  MOV(R,M0)
A186 06CCE 440000000 MXS4  MOV(R,M0)
A187 06CCE 460000000 MXS4  MOV(R,M0)
A188 06CCE 480000000 MXS4  MOV(R,M0)
A189 06CCE 4A0000000 MXS4  MOV(R,M0)
A190 06CCE 4C0000000 MXS4  MOV(R,M0)
A191 06CCE 4E0000000 MXS4  MOV(R,M0)
A192 06CCE 500000000 MXS4  MOV(R,M0)
A193 06CCE 520000000 MXS4  MOV(R,M0)
A194 06CCE 540000000 MXS4  MOV(R,M0)
A195 06CCE 560000000 MXS4  MOV(R,M0)
A196 06CCE 580000000 MXS4  MOV(R,M0)
A197 06CCE 5A0000000 MXS4  MOV(R,M0)
A198 06CCE 5C0000000 MXS4  MOV(R,M0)
A199 06CCE 5E0000000 MXS4  MOV(R,M0)
A200 06CCE 600000000 MXS4  MOV(R,M0)

```

PAGE 6: FOR 244... "MPWGX(Y,U,V,W)" EXTRACT M,PG,MOVE X

```
A47 06012 R47C47C (00142)
      (00143) 2
A48 06014 90140043 (00144)
A49 06016 00140000C (00145)
A4A 06018 9014004A (00146) 01
A4B 0601A 20142037 (00147)
      (00148) *
      (00149) * GO ON TO NEXT PORTION OF FUNCTION.
      (00150)
      MOV(00),MUL(NO,M7)
      JUMP(C(PTS,F=1)
      MOV(00)
      JUMP(C(01,PO)
      CLEAR(M1)
      (00142)
      (00143)
      (00144)
      (00145)
      (00146)
      (00147)
      (00148)
      (00149)
      (00150)
      WAIT FOR OUTPUT TO CLEAR
      LET AFS CONTINUE
```



```

(00151) *
(00152) *
(00153) *
(00154) *
(00155) *
(00156) *
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(00192) *
(00193) *
(00194) *

```

M=LD-APU PROGRAM
 WFINER LEVINSON DURRAN INVERSE
 MATHEMATICS
 SPF J. MAKHOL, LINEAR PREDICTION REVIEW
 PROC. IEEE, VOL 63, PPS66, APR 1975
 BUFFER DEFINITIONS
 V(K), CORRELATION COEFFICIENTS, V(K)=R(K)
 V(K) MUST BE COMPACT 32 BIT FLOATING POINT
 VSIZE NOT USED IN COMPUTATION, BUT
 VSIZE > USIZE FOR VALID RESULTS.
 U(K), ACK COEFFICIENTS
 U(K) MUST BE COMPACT 32 BIT FLOATING POINT
 U(K)=A(K-1), IF A(0)=1 NOT IN BUFFER
 Y(K), PARTIAL CORRELATIONS AND ERROR
 Y(K) MUST BE COMPACT 32 BIT FLOATING POINT
 YSIZE=2*USIZE
 Y(K)=P(K-1), 0<K<USIZE
 Y(K+USIZE)=F(K-1), USIZE<K<VSIZE
 A, STABILITY TEST PARAMETER
 IF /P(P)> CONTENTS OF (A), THEN
 FOR J>0
 A(N+J)=P(N+J)=0, F(V+J)=F(N)
 M=LDSSA MOV(108,A7)\NOP
 MOV(ZERO,M4)
 MOV(10,M5)\NOP
 MOV(108,A5)\NOP
 M5=A5=F(0)=R(0)
 M=LD-APU CALCULATION OF S(N)
 S(N)=P(N) + SUM(I,N-1) OF R(N-J)A(N-1,J)
 REGISTER CONTENTS AT START
 M4=P(N-1), M5=A5=F(N-1), A6=1, A7=TEST

A4C 06D1C 08F20000 M=LDSSA MOV(108,A7)\NOP A7=STABILITY TEST VALUE

A4D 06D1E 08C080C MOV(ZERO,M4)

A4E 06D20 08C0000 MOV(10,M5)\NOP

A4F 06D22 08F50000 MOV(108,A5)\NOP M5=A5=F(0)=R(0)

```

A50 06D24 2FA02FA0 (00195) 01      MOV(A5)
A51 06D26 08F20000 (00196)      MOV(10A,A2)\NOP
A52 06D28 08000000 (00197)      MOV(2F00,M0)
A53 06D2A 04000400 (00198)      MUL(M0,M4)
A54 06D2C 02400240 (00199)      MOV(M1),K(A2)
A55 06D2E 90000000 (00200)      JUMPC(MULDRF,AF2)
A56 06D30 9040005C (00201) *
A57 06D32 20202020 (00202) *      JUMPC(MULDRF,AF3),SFT
A58 06D34 08FA0000 (00203) *      CLEAR(AF3)
A59 06D36 08FA0000 (00204) *
A60 06D38 08FA0000 (00205) *
A61 06D3A 08FA0000 (00206) *      MOV(10A,M2)\NOP
A62 06D3C 08FA0000 (00207) *      MOV(10A,M6)\NOP
A63 06D3E 08FA0000 (00208) *      MOV(A0),MUL(M2,M6)
A64 06D40 42124212 (00209) *      MOV(A2),ADD(A0,A2)
A65 06D42 08FA0000 (00210) *      MOV(10A,M3)\NOP
A66 06D44 08FA0000 (00211) *      MOV(10A,M7)\NOP
A67 06D46 08FA0000 (00212) *      MOV(A1),MUL(M3,M7)
A68 06D48 42124212 (00213) *      MOV(A2),ADD(A1,A2)
A69 06D4A 08FA0000 (00214) *      JUMPC(2F01)
A70 06D4C 08FA0000 (00215) *
A71 06D4E 08FA0000 (00216) *      MOV(P,A0)
A72 06D50 42124212 (00217) *      MOV(A2),ADD(A0,A2)
A73 06D52 08FA0000 (00218) *
A74 06D54 08FA0000 (00219) *
A75 06D56 08FA0000 (00220) *      MULDRF CLEAR(W1)
A76 06D58 08FA0000 (00221) *
A77 06D5A 08FA0000 (00222) *
A78 06D5C 08FA0000 (00223) *
A79 06D5E 08FA0000 (00224) *      MULDRF CALCULATION OF P(N)
A80 06D60 08FA0000 (00225) *      P(N)=S(N)/F(N-1)
A81 06D62 08FA0000 (00226) *      REGISTER CONTENTS AT START
A82 06D64 08FA0000 (00227) *      M4=P(N-1), A5=M5=F(N-1), A6=1, A7=TEST
A83 06D66 08FA0000 (00228) *      M1=RCPI(F(N-1)), R=S(N)
A84 06D68 08FA0000 (00229) *
A85 06D6A 08FA0000 (00230) *
A86 06D6C 08FA0000 (00231) *      MOV(R,M2)\NOP
A87 06D6E 08FA0000 (00232) *      CALC(INV)
A88 06D70 08FA0000 (00233) *      MOV(P,M6)
A89 06D72 08FA0000 (00234) *      MUL(M2,M6)
A90 06D74 08FA0000 (00235) *      K(1)
A91 06D76 08FA0000 (00236) *      MOV(R,A6)
A92 06D78 08FA0000 (00237) *      MOV(P,M0)
A93 06D7A 08FA0000 (00238) *

```

S(N)
GET 1.0/F(N-1)
1.0/F

FOLLOWING CODE NEEDS A6=1

M0=M4=A4=P(N)

```

A6R 06D5A 0HAC0RAC (00240)
A6C 06D5C 0HAC0RAC (00240)
A6D 06D5E 0HAC0RAC (00241)
      MOV(P,M4)
      MOV(P,A4)
      MOV(P,00)\NOP      00 = P(0) = A(N,M)
      (00242) *
      (00243) *
      (00244) *
      (00245) * MWID-APU CALCULATION OF A(N,J)
      (00246) * A(N-1,J)=A(J)
      (00247) * A(N,J)=A(J)+P(N)A(N-J)
      (00248) * A(N,N-J)=A(N-J)+P(N)A(J)
      (00249) * LOOP COMPUTES BOTH A(N,J) AND A(N,N-J)
      (00250) * FOR N=2M, A(N,M) COMPUTED TWICE
      (00251) *
      (00252) * REGISTER CONTENTS AT START
      (00253) * M0=M4=A4=P(N), M5=A5=E(N-1), A6=1, A7=TEST
      (00254) *
      (00255) * DATA SEQUENCE
      (00256) * INPT, IA(N-J+1),A(N-J),A(J) FOR [1,N/2],M1, A(N/2+1)
      (00257) * DTPT, DUMP, DUMP [A(N,J), A(N,N-J)]
      (00258) *
      (00259) *
      (00260) * FIRST TIME AF2 CLEAR
      (00261) *
      (00262) *
      (00263) *
      (00264) *
      (00265) *
      (00266) *
      (00267) *
      (00268) *
      (00269) *
      (00270) *
      (00271) *
      (00272) *
      (00273) *
      (00274) *
      (00275) *
      (00276) * MWID-APU CALCULATION OF F(N)
      (00277) *
      (00278) *
      (00279) *
      (00280) *
      (00281) *
      (00282) * F(N)=1-P(N)P(N)A(N-1)

```

```

(002H3) * REGISTER CONTENTS AT START
(002M4) *
(002R5) *
(002M6) * M0=M4=A=P(N), M5=A5=F(N-1), A6=1, A7=TEST
(002M7) * P=P(N), R=P(N)=A(N,N)
(002M8) * DATA SEQUENC
(002M9) * NEXT P(N), F(N)
(002Q0) MOV(P,A0)
(002Q1) MOV(U0), SUB(A6,A0)\NOP
(002Q2) MOV(W7), R(A4)
(002Q3) MUL(M2,M5)
(002Q4) MOV(U0), MAXARS(A7,A4)\NOP
(002Q5) MOV(R,NULL)
(002Q6) MOV(P,U0)\NOP
(002Q7) SETTG(AF1)
(002Q8) MOV(P,A5)
(002Q9) MOV(P,M5)
(002Q0) SET(AF0)
(00301) JUMPS(MWLD0,F1)
(00302) *
(00303) * JUMPC(B1,T1)
(00304) *
(00305) *
(00306) * MWLD-API ARPT ROUTINE
(00307) * P REGISTER=E(N)
(00308) *
(00309) *
(00310) * M0V(ZF0,U0)\NOP
(00311) * M0V(ZF0,U0)\NOP
(00312) * M0V(P,U0)\NOP
(00313) * JUMPC(R4,FU)
(00314) *
(00315) * CLFAR(CA)
(00316) * JUMP(MWLDSSA)
(00317) * JUMPC(MWLDRM,F+1)
(00318) * JUMP(MWLDRF)
(00319) * M0V(LDA,RULL)\NOP
(00320) * JUMP(MWLDRF)
(00321) *
(00322) * MWGXSZ=B-A-P1T(CSA
(00323) * END
(00324) *
(00325) *
(00326) *

```

06DAE 000006F2	(00327)	EVFN	ANDR MGCXS1	POINTER TO CONSTRUCTED I
06DAF 000006F20	(00328)			NSTRUCTION BLOCK
06DB0 0000	(00329)			POINTER TO SCALAR BLOCK
06DB1 0110	(00330)	ADDR G110S+2*MGCXS		NUMBER OF SCALARS
06DB2 000006F0C	(00331)	DATA 0		MODULE SIZE
	(00332)	DATA MGCXS2		POINTER TO CHAIN ANCHOR
	(00333)	ADDR MGCXS4		END ON WORD BOUNDARY
	(00334)	EVFN		
	(00335)			
	(00336)	MFCIN APS(G110)		SET OUTPUT PC(P2)
A00 06DB4 00202440	(00337)	JSN(G110S0,P2)		ENABLE OUTPUT ADDRESSING
A01 06DB6 02300030	(00338)	SFT(M0)		
A02 06DB8 04000020	(00339)	NOP		
A03 06DBA 06460000	(00340)	LOAD(MR0,X1PS(3))		
A04 06DBC 08020004	(00341)	SUR(MR0,4)		
A05 06DBF 0A600000	(00342)	LOAD(MR2,(0))		
A06 06DC0 0C500000	(00343)	LOAD(MR1,MSS)		
A07 06DC2 0E200000	(00344)	SUR(MR2,MSS)		
A08 06DC4 104A0004	(00345)	ADD(MR0,4,TF)		
A09 06DC6 12190041	(00346)	SUR(MR1,1),JUMPP(M1)		
A0A 06DC8 14300037	(00347)	SFT(M1)		
A0B 06DCA 16000020	(00348)	NOP		
	(00349)			
A0C 06DCC 1A460000	(00350)	LOAD(MR0,X1PS(3))		
A0D 06DCE 1A020004	(00351)	SUR(MR0,4)		
A0E 06DB0 1C390011	(00352)	MOVH(MR3,MR0)		
A0F 06DB2 1E0A003C	(00353)	ADD(MR0,4*OFFSETS)		
A10 06DB4 2050004F	(00354)	LOAD(MR1,RANGES)		
A11 06DB6 22F26C80	(00355)	LOAD(MR2,OFFSETS(1),TF)		
A12 06DB8 244A0004	(00356)	ADD(MR0,4,TF)		
A13 06DBA 261912H1	(00357)	SUR(MR1,1),JUMPP(M2)		
A14 06DBC 28300037	(00358)	SFT(M1)		
A15 06DBF 2A000020	(00359)	NOP		
	(00360)			
A16 06DB0 2CBA0004	(00361)	ADD(MR3,4,TF)		
A17 06DB2 2FC203FC	(00362)	LOAD(MR0,SAS41(1),TF)		
	(00363)			
A18 06DB4 30C20300	(00364)	LOAD(MR0,SAS49(1),TF)		
A19 06DB6 32401028	(00365)	LOAD(MR0,11)		
A1A 06DB8 34500000	(00366)	LOAD(MR1,MSS)		
A1B 06DBA 36220000	(00367)	SUR(MR2,MSS)		
A1C 06DBC 38090028	(00368)	ADD(MR0,MR1)		
A1D 06DBE 3A02000A	(00369)	SUR(MR0,NPS)		
A1E 06DB0 3C600009	(00370)	LOAD(MR2,NPS-1)		

```

A1F 06B22 3F0A0001 (00371) IFLTST ADD(HW0,WS,JK)
A20 06B24 402A1F01 (00372) SUML(HW2,I),JUMPP(IFLTST)
A21 06B26 42300037 (00374) SET(WI)
A22 06B28 44000020 (00376) NOP
A23 06B2A 46003A00 (00377) INPST JUMPP(MCGXSS)
A24 06B2C 48300032 (00379) SET(IRA)
A25 06B2E 4A400018 (00380) LOAD(HW0,(01)
A26 06B30 4C500000 (00381) LOAD(HW1,MSS)
A27 06B32 4E020000 (00382) SUM(HW0,MSS)
A28 06B34 50A00006 (00383) ADD(HW0,(H1,TF)
A29 06B36 521120A1 (00384) SUML(HW1,I),JUMPP(H1)
A2A 06B38 54C203FC (00385) *
A2B 06B3A 56C203FE (00387) SCLRS LOAD(HW0,SAS61(1),TF)
A2C 06B3C 58C203FC (00389) * LOAD(HW0,SAS62(1),TF)
A2D 06B3E 5AC20794 (00391) * LOAD(HW0,SAS61(1),TF)
A2E 06B40 5CC20794 (00392) * LOAD(HW0,PMYS(1),TF)
A2F 06B42 5E440814 (00393) * LOAD(HW0,XOMS(2))
A30 06B44 60500009 (00394) * LOAD(HW1,NPS-1)
A31 06B46 62020002 (00395) * SURC(HW0,2)
A32 06B48 648A0002 (00396) UFLTSJ ADD(HW0,WS,TF)
A33 06B4A 661132A1 (00397) * SURL(HW1,I),JUMPP(UFLTSJ)
A34 06B4C 68200030 (00399) DNFSN CLEAR(RU)
A35 06B4E 6A000020 (00401) * NOP
A36 06B50 6CF2042F (00403) * WFINER - APS INITIALIZATION ROUTINES
A37 06B52 6FC0001F (00405) * MCGXSS LOAD(HW2,SAS6K(1),I,TF)
A38 06B54 70500000 (00407) * INPUT STABILITY TEST
A39 06B56 720A0000 (00409) * INPT R(0)
A3A 06B58 74500000 (00411) * NOT USED
A3B 06B5A 76010013 (00413) * RW0=H(1)
A3C 06B5C 78010013 (00414) * RW0=0=2(N-1)

```

```

TO=NM(1)
FOR I=NTUPS-NP+1,...,NTUPS
-1
STALL.

GO DO MWLD PORTION

ENABLE API
W(.) RA
R(.) SIZE-1
R(.) RA-2
NO=R(1)
FOR I=0,1,2,...,LPCN+1
NO=XIR(MAX)
NO=M
NO=XIR(MAX)
NO=2
NO=?
XOM(. ) RA
XOM(. ) SIZE-1
XOM(. ) RA-2
NO=XOM(J)
FOR J=0,1,...,NP-1
OUTPUT DONE!

```



```

A52 06F58 A4194F44 (00459) *      SUML(RW1,4), JUMPP(83)      HR1=A(1)=AH
      (00461) *      (00462) *      ASIZE=N,DONE
A53 06F5A A670302F (00463) *      LOAD(RW1,{31,L})      HR1=A(0)=AH-2
A54 06F5C A8500000 (00464) *      LOAD(RW1,MSS)      HR1=2N-2, DONE
A55 06F5E AA320000 (00465) *      SUB(RW3,MSS)      CLEANUP OF A(N) CALC*
A56 06F60 AC190020 (00466) *      ADDR(RW1,RW1)
A57 06F62 A8A94A7H (00467) *      ADDL(RW2,0,TF), JUMP(AA+1)
      (00468) *      SET(W1)
A58 06F64 A0300037 (00469) *      NOP(0)
A59 06F66 A2000020 (00470) *      SUBR(RW1,RW0), JUMPP(81)
A60 06F68 A3194F40 (00471) *      JUMPS(AA-2,R1),CLEAR
A61 06F6A A67059F1 (00472) *      (00473) *      WILD - APS OUTPUT
      (00474) *      (00475) *      REGISTER CONTENTS AT START
      (00476) *      RW0=2N, RW2=A(N)
      (00477) *      (00478) *      SET(R0)
A5C 06F6C A9300030 (00479) *      MOVW(RW3,RW2,TF)      RW3=A(N), OUTPUT
A5D 06F6E A810014 (00480) *      SUBR(RW2,RW0)      RW2=A(0)
A5E 06F70 A0210020 (00481) *      LOAD(RW1,DAYS(1),TF)      OTPT DUMP
A5F 06F72 AFD20794 (00482) *      ADDL(RW1,0,TF)      OTPT DUMP
A60 06F74 C091003H (00483) *      MOVW(RW1,RW0), JUMP(RW1,OE)      RW1=2N
A61 06F76 C2116450 (00484) *      (00485) *      OTPT A(N,J)
A62 06F78 C4A00002 (00486) *      ADD(RW2,2,TF)      OTPT A(N,N-1)
A63 06F7A C6200002 (00487) *      SUB(RW3,2,TF)
      (00488) *      SUML(RW1,4),JUMPP(84)
A64 06F7C C81162H4 (00489) *      LOAD(RW2,{21,L})
A65 06F7E CA602024 (00490) *      LOAD(RW1,MSS)
A66 06F80 CC500000 (00491) *      SUB(RW2,MSS)
A67 06F82 CF220000 (00492) *      ADD(RW1,1)
A68 06F84 D01A0001 (00493) *      ADDR(RW2,RW0,TF)
A69 06F86 D2A10028 (00494) *      ADDR(RW2,RW1,TF)
A6A 06F88 D4A1002A (00495) *      NOP(0)
      (00496) *      JUMPC(RW5,AF0)
A6R 06F8A D6000020 (00497) *      (00498) *      WAIT ON APU
A6C 06F8C D8006AH (00499) *      JUMP(AA+1,AF0),CLEAR
      (00500) *      JUMPC(RW1,OD,AF1)
A6D 06F8E DA206F6H (00501) *      (00502) *      JUMP TO DONE IF NO ABORT
A6F 06F90 DC007DA9 (00503) *

```


AFDTSING:	0004H (0001R) (00042)	
CSPUSNUS:	021FC (00014) (00045)	
DAYS:	00704 (00020) (00391) (00302) (00402)	
DWFS1:	00023 (00377)	
DWFSN:	00034 (00300)	
EXTRACTS:	00003 (00066)	
FITS:	00043 (00138) (00144)	
GLIOS:	00004 (00044) (00330) (00336) (00528)	
GLIOSN:	00024 (00337) (00370)	
HS:	00001 (00023) (00371)	
IPITS:	0001H (00364)	
IPITS1:	0001F (00371) (00372)	
INV:	0002H (00111) (00130) (00232)	
INVS1:	00016 (00361)	
INVS0:	0002C (00300)	
INVS2:	0003H (00097) (00090) (00125)	
INVS3:	0004F (00024) (00028)	
INVS4:	00000 (00025) (00343) (00344) (00366) (00367) (00381) (00411) (00412) (00416)	
INVS5:	00000 (00177) (00463) (00464) (00491) (00492) (00510) (00511)	
INVS6:	00004 (00043) (00060)	
INVS7:	0000C (00333) (00523)	
INVS8:	00014 (00328) (00527)	
INVS9:	00036 (00330) (00377) (00408)	
INVS10:	00094 (00059) (00372)	
INVS11:	00110 (00332) (00528)	
INVS12:	00052 (00454) (00460)	
INVS13:	0004C (00185) (00316)	
INVS14:	0007A (00276) (00320)	
INVS15:	00042 (00260) (00319)	
INVS16:	00004 (00301) (00315)	
INVS17:	00063 (00221) (00318)	
INVS18:	00090 (00200) (00317) (00317)	
INVS19:	0005C (00202) (00211)	
INVS20:	0007C (00512) (00518)	
INVS21:	00070 (00501) (00520)	
INVS22:	00064 (00484) (00489)	
INVS23:	0005C (00452) (00479)	
INVS24:	0004H (00428) (00436) (00438)	
INVS25:	00045 (00429) (00433)	
INVS26:	00053 (00462)	
INVS27:	00014 (00073) (00083)	
INVS28:	00010 (00078) (00088)	
INVS29:	00025 (00080) (00090) (00102)	
INVS30:	0002H (00076) (00085) (00105)	

MXS5:	00010 (00007)	(00092)
MXS6:	00023 (00094)	(00098)
NPS:	0000A (00026)	(00369)
OFFSFT:	04F50 (00052)	(00355)
OFFSFTS:	0000F (00027)	(00028)
OFFTS:	00020 (00301)	(00353)
OFFTS1:	00032 (00196)	(00307)
OP:	00010 (00021)	
PITCSSA:	00000 (00050)	(00062)
RANGS:	0004F (00028)	(00063)
SAS39:	00300 (00030)	(00354)
SAS61:	0030C (00031)	(00364)
SAS62:	0030F (00032)	(00362)
SAS64:	0042F (00033)	(00387)
SCLPS:	0002A (00186)	(00408)
START:	06C80 (00034)	(00050)
SVTS:	00342 (00029)	(00030)
WS:	00002 (00035)	(00032)
XIWS:	00800 (00038)	(00031)
XIWS:	00013 (00039)	(00350)
ZS:	00003 (00036)	(00393)


```
(((((C S P U))))))
((00037) * )
((00038) * )
((00039) * )
00006H70      #I=START
               EVEN
               FJCT
((00040)
((00041)
((00042)
```


A24	06NHA	081C081C	(00087)	MOV(ZERO,00)	Z4 OUT
A25	06NHC	081C081C	(00088)	MOV(ZERO,00)	Z5 OUT
A26	06NHF	081C081C	(00089)	MOV(ZERO,00)	Z6 OUT
A27	06HCO	081C081C	(00090)	MOV(ZERO,00)	Z7 OUT
A28	06RC2	40004000	(00091)	SUR(A0,A1)	M'-1 FOR LATER TEST M'-1
A29	06HC4	081C081C	(00092)		4
A2A	06HC6	081C081C	(00094)	MOV(ZERO,00)	Z8 OUT
A2B	06HC8	081C081C	(00095)	MOV(ZERO,00)	Z9 OUT
A2C	06RCA	08AC0000	(00096)	MOV(P,M4)\NOP	Z10 OUT
A2D	06HCC	081C081C	(00097)	MOV(ZERO,00)	G**P
A2E	06HCF	081C081C	(00098)	MOV(ZERO,00)	Z11 OUT
A2F	06HDO	081C081C	(00099)	MOV(ZERO,00)	Z12 OUT
A30	06RD2	08H80000	(00100)	MOV(P,A6)\NOP	Z13 OUT
A31	06HDA	081C081C	(00101)	MOV(ZERO,00)	G**P, TO ADD TO SUMC
A32	06RDB	49104A10	(00102)	MOV(A0),SUB(A0,A1)\MOV(A0),SUR(A0,A2)	Z14+ OUT
A33	06RDH	901F0031	(00103)	JUMPC(P2,T1)	M'-1-L; M'-1-L\N'-1-L-L;
A34	06HDA	02400890	(00104)	WHUST HF TIME FOR PULSE	N'-15-L
A35	06HDC	08H8081C	(00105)	P(A2)\MOV(R,A0)	LOOP TIL M'-1-L .LF. 0
A36	06HDF	48000000	(00106)		RESTORE M\COUNT PULSE A
A37	06HE0	901F001F	(00107)		S OUTPUT
A38	06HE2	089540A0	(00111)	MOV(P,00)\MOV(ZERO,00)	PUT OUT PULSE
A39	06HE4	081C081C	(00113)	MOV(A0),ADD(A5,A6)\NOP	M';ADD PULSE TO SUMC
A3A	06HE6	00004A10	(00114)	JUMPC(P1,T2)	LOOP IF WE NEED MORE PUL
A3B	06HE8	901F0019	(00115)	MOV(R,A5)\ADD(A3,A0)	SFS
A3C	06HEA	089C0000	(00116)	NOP\MOV(A0),SUR(A0,A2)	SUMC\N'+M-1 IS .LT. M, 0
A3D	06HEC	20322032	(00117)	JUMPC(P3,T2)	SAVE COUNT;DECREMENT
A3E	06HEE	00000000	(00119)	MOV(R,00)\NOP	LOOP UNTIL WE HAVE ENOUGH
A3F	06HE0	00000000	(00120)	CLPAR(PA)	H ZEROS
A40	06HE2	00000000	(00121)	NOP	SUMC OUT
A41	06HE4	00000000	(00122)	NOP	
A42	06HE6	10000042	(00123)	JUMP(N0GD)	
A43	06HE8	00000000	(00124)	NOP	
A44	06HEA	00000044	(00125)	V0C0SS2=8A-V0C0SSA	
A45	06HEC	00000000	(00126)	P40, V0C0SSZ	
A46	06HEE	00000000	(00127)	FJFECT	

PAGE 6: FCR 245... "MPFSTV(U)" CRATE PITCH & VOCAL TRACT RESPONSE

06C34 (00172) FND 9A-1
(00173) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
06C34 00000000 (00174) G107SI DATA IF'0.0'
00000034 (00175) G107S7=81.-G107S
06C36 (00176) FND

PAGE 7: PCN 245... "MPISTV(0)" CREATE PITCH & VOCAL TRACT RESPONSE

AFDTSORG:	00000	(00007)	(00032)
APSSMEM:	10000	(00000)	
ASTZFS:	00007	(00000)	
CIMM:	0002A	(00001)	(00094)
CSVUSMUS:	0210C	(00010)	(00035)
DWYS:	00794	(00011)	(00164)
G1078:	06002	(00034)	(00134)
G1078A:	0600C	(00137)	(00170)
G10781:	06014	(00132)	(00174)
G10780:	00000	(00141)	(00156)
G10787:	00014	(00136)	(00175)
HS:	00001	(00013)	
ITHMIS:	0000F	(00014)	
WS:	00000	(00015)	
WSS:	00000	(00016)	
NOCHI:	00042	(00070)	(00147)
SAS3R:	0030F	(00018)	(00123)
SAS73:	00414	(00023)	(00166)
SAS90:	00436	(00020)	(00145)
SAS91:	00439	(00021)	(00144)
SAS92:	0043A	(00019)	(00143)
SAS97:	00444	(00022)	
SLIKS:	00001	(00134)	(00142)
STZFS:	0010F	(00024)	(00159)
START:	00070	(00025)	(00040)
SVTS:	00392	(00017)	(00019)
VOCUS:	00072	(00033)	(00049)
VOCUSSA:	00000	(00047)	(00125)
VOCUSSZ:	00044	(00048)	(00126)
WS:	00002	(00026)	
WSS:	00004	(00027)	
X1S:	00000	(00028)	(00029)
X1RS:	00000	(00029)	(00157)

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) E- 0

ORIGINATED:13-DEC-79
UPDATED:30-MAY-80

(00001) : FCM 247... "MPSSRT(Y)" SORT DCT1(.) COEFFICIENTS
(00002) *
(00003) *
(00004) * DEFINE GLOBAL SYMBOLS
(00005) OPADD EXP, (1 .U.S. 14) + (12 .U.S. 4) + \$1K
(00006) OPADD INF, (3 .U.S. 10) + (12 .U.S. 5) + \$1K
(00007) OPADD WAFNE, (1 .U.S. 10) + (12 .U.S. 5) + \$1A
(00008) AFDT\$ORIG=\$RER
(00009) A\$SMEN=\$1FCO
(00010) CSPUSNUS=\$21FC
(00011) DCTS=\$2344
(00012) DCT1S=\$1517
(00013) DCT2S=\$11024
(00014) PMS=\$574
(00015) SM=3
(00016) HS=1
(00017) INCRFS=\$1256
(00018) INCRFS=\$1524
(00019) INCRFS=\$10
(00020) ISVTS=\$402
(00021) TSAS00=\$1SVTS+\$10
(00022) TSAS01=\$1SVTS+\$11
(00023) MSS=0
(00024) OVRSLAP=\$10
(00025) SZFS=\$1256-\$11
(00026) SVTS=\$0382
(00027) START=\$M300
(00028) TABLS0=\$13072
(00029) TABLS1=\$TABLS0*INCRFS
(00030) TABLS2=\$TABLS1*INCRFS
(00031) TABLS3=\$TABLS2*INCRFS
(00032) TMP2S=\$12560
(00033) TMP4S=\$13072
(00034) WS=2
(00035) ZS=3

00000RER

00011FCO

000021FC

0000092H

00000200

00000400

00000794

00000093

00000001

00000100

00001F24

00000000

00000502

00000502

00000503

00000000

0000000A

000000FF

00000382

000004300

00000C00

00000000

00000F00

00000A00

00000C00

00000002

00000003

00000003

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EXPAND ARRAY FUNCTION DISPATCH TABLE

BL=AFDT\$ORIG+1*2*(247-128)

ADDR SORTS(P7,1)

ADDR G10AS(P7,1)

ADDR CSPUSNUS(,1,0)

F.I.F.C.T

```

(00043) *
(00044) *
(00045) *
(00046) *
(00047) *
(00048) *
(00049) *
(00050) *
(00051) *
(00052) *
(00053) *
(00054) *
(00055) *
(00056) *
(00057) *
(00058) *
(00059) *
(00060) *
(00061) *
(00062) *
(00063) *
(00064) *
(00065) *

00001114
01F14 3C50
01F15 0000
01F16 3450
01F17 0000
01F18 2C50
01F19 0000
01F1A 2450
01F1B 0000
01F1C 662F
01F1D 0000
01F1E 6A2F
01F1F 0000
01F20 562F
01F21 0000
01F22 5A2F
01F24 0000

0000R300
08300 H477A2C0
08302 0A934F41
08304 17414FC1

HUS 2
#1=D'7700'
SVFN
INSTRS0 DATA INS0#D'512'+X'50',X'0000'
INSTRS1 DATA INS1#D'512'+X'50',X'0000'
INSTRS2 DATA INS2#D'512'+X'50',X'0000'
INSTRS3 DATA INS3#D'512'+X'50',X'0000'
INSTRS4 DATA INS4#D'512'+X'2F',X'0000'
INSTRS5 DATA INS5#D'512'+X'2F',X'0000'
INSTRS6 DATA INS6#D'512'+X'2F',X'0000'
INSTRS7 DATA INS7#D'512'+X'2F',X'0000'

HUS 1
#1=START
DATA 1F'0.713'
DATA 1F'2.5'
DATA 1F'1.5'
DATA 1F'-0.44802602'
DATA 1F'1.37192810'
DATA 1F'2.9068906'
EJECT

```


A24 04350 90160000 (00110)	JUMPC(INCST,FW1)	FOR I=0,1,2...LTH-1
A25 04352 1045001A (00111)	JUMPC(NTSOUT,G1),SET	GET LEVEL COUNTS
A26 04354 20490000 (00112) LVL\$1	SET(A1)\NOP	INFORM APS OF LVL1
A27 04356 20530000 (00113)	SET(UN)\NOP	FIXED POINT ADD
A28 04358 00004720 (00114)	NOP\ADD(A1,A7)	LVL1 CNT+1
A29 0435A 04F004F4 (00115)	MOV(10A,A0)\MOV(10A,A4)	A0=D=DC1(1+1):A4=D
A2A 0435C 04F004F4 (00116)	MOV(10A,00)\MOV(R,A1)	A0=1:LVL1=LVL1+1
A2B 0435E 20330000 (00117)	CLEAR(UN)\NOP	RESUME FLOATING POINT!
A2C 04360 40004000 (00118)	SUB(A0,A1)\SUB(A4,A5)	D-THRESH1:D-THRESH2
A2D 04362 91000020 (00119) #1	JUMPC(B1,AF1)	
A2E 04364 90160000 (00120)	JUMPC(INCST,FW1)	FOR I=0,1,2...LTH-1
A2F 04366 1045003A (00121)	JUMPC(NTSOUT,G1),SET	GET LEVEL COUNTS
A30 04368 20490000 (00122) LVL\$0	SET(AFO)\NOP	INFORM APS OF LVL0
A31 0436A 20530000 (00123)	SET(UN)\NOP	FIXED POINT ADD
A32 0436C 00004700 (00124)	NOP\ADD(A0,A7)	LVL0 CNT+1
A33 0436E 04F004F4 (00125)	MOV(10A,A0)\MOV(10A,A4)	A0=D=DC1(1+1):A4=D
A34 04370 04F00490 (00126)	MOV(10A,00)\MOV(R,A0)	A0=1:LVL0=LVL0+1
A35 04372 20330000 (00127)	CLEAR(UN)\NOP	RESUME FLOATING POINT!
A36 04374 49004000 (00128)	SUB(A0,A1)\SUB(A4,A5)	D-THRESH1:D-THRESH2
A37 04376 91000037 (00129) #1	JUMPC(B1,AF0)	
A38 04378 90160000 (00130)	JUMPC(INCST,FW1)	FOR I=0,1,2...LTH-1
A39 0437A 20450000 (00131)	SET(G1)\NOP	GET LEVEL COUNTS
A3A 0437C 00000200 (00132) CTSOUT	NOP\RC(A0)	R=LVL0 CNT
A3B 0437E 0000023C (00133)	NOP\MOV(00),R(A1)	R=LVL1 CNT
A3C 04380 0000025C (00134)	NOP\MOV(00),R(A2)	R=LVL2 CNT
A3D 04382 0000027C (00135)	NOP\MOV(00),R(A3)	R=LVL3 CNT
A3E 04384 0000049C (00136)	NOP\MOV(R,00)	00=LVL3 CNT(0) UPTO LTH)
A3F 04386 041C041C (00137)	MOV(ZF00,00)	00=0 FOR INSTR\$4+1... THRU INSTR\$7+1
A40 04388 041C041C (00138)	MOV(ZF00,00)	WAIT FOR...
A41 0438A 00000000 (00139) #1	NOP	"LEVELS" TO SETTLE
A42 0438C 901C0041 (00140)	JUMPC(B1,F0)	RELEASE APS INPUT
A43 0438E 20370000 (00141)	CLEAR(01)\NOP	RELEASE APS OUTPUT
A44 04390 20500000 (00142)	SET(00)\NOP	SCATTER-WRITE INTO INS0
A45 04392 04FC0000 (00143)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS1
A46 04394 04FC0000 (00144)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS2
A47 04396 04FC0000 (00145)	MOV(10A,00)\NOP	SCATTER-WRITE INTO INS3
A48 04398 04FC0000 (00146)	NOP	WAIT FOR...
A49 0439A 00000000 (00147) #2	NOP	"WRITES" TO SETTLE
A4A 0439C 901C0049 (00148)	JUMPC(B2,F0)	RELEASE APS INPUT
A4B 0439E 20370000 (00149)	CLEAR(01)\NOP	RELEASE APS OUTPUT
A4C 043A0 20500000 (00150)	SET(00)\NOP	00=100DR(1+1)
A4D 043A2 04FC0000 (00151) 000PS1	MOV(10A,00)\NOP	00=100DR(1+2)
A4E 043A4 04FC0000 (00152)	MOV(10A,00)\NOP	
A4F 043A6 04FC0000 (00153)	MOV(10A,00)\NOP	

```

A50 OR3A8 08FC0000 (00154)      MOV(TQA,00)\NOP
A51 OR3A8 90160040 (00155)      JUMPC(CORRST,FWT)
A52 OR3AC 20280000 (00156)      CLFAR "SKIP" FLAG
A53 OR3A8 081C081C (00157)      MOV(ZFR0,00)
A54 OR3A8 081C081C (00158)      MOV(ZFR0,00)
A55 OR3A8 081C081C (00159)      MOV(ZFR0,00)
A56 OR3A8 081C081C (00160)      MOV(ZFR0,00)
A57 OR3A8 081C081C (00161)      MOV(ZFR0,00)
A58 OR3A8 20370000 (00162)      SHUFLSV CLFAR(MI)\NOP
A59 OR3A8 20530000 (00163)      SET(UN)\NOP
A5A OR3A8 08FC0000 (00165)      MOV(TQA,A0)\NOP
A5B OR3A8 000008F0 (00166)      NOP\MOV(TQA,A0)
A5C OR3C0 40004000 (00167)      ADD(A0,A0)
A5D OR3C2 08F10000 (00168)      MOV(TQA,A1)\NOP
A5E OR3C4 000008F1 (00169)      NOP\MOV(TQA,A1)
A5F OR3C6 413C413C (00170)      MOV(00),ADD(A1,A1)
A60 OR3C8 08FC0000 (00171)      MOV(TQA,00)\NOP
A61 OR3CA 000008FC (00172)      NOP\MOV(TQA,00)
A62 OR3CC 08FC0000 (00174)      MOV(TQA,00)\NOP
A63 OR3CE 08FC0000 (00175)      MOV(TQA,00)\NOP
A64 OR3D0 00000000 (00176)      MOV(TQA,00)\NOP
A65 OR3D2 08FC0000 (00177)      MOV(TQA,00)\NOP
A66 OR3D4 08F20000 (00178)      MOV(TQA,A2)\NOP
A67 OR3D6 000008F2 (00179)      NOP\MOV(TQA,A2)
A68 OR3D8 425C425C (00180)      MOV(00),ADD(A2,A2)
A69 OR3DA 08FC0000 (00182)      MOV(TQA,00)\NOP
A6A OR3DC 000008FC (00183)      NOP\MOV(TQA,00)
A6B OR3DE 08FC0000 (00184)      MOV(TQA,00)\NOP
A6C OR3E0 08FC0000 (00185)      MOV(TQA,00)\NOP
A6D OR3E2 08FC0000 (00186)      MOV(TQA,00)\NOP
A6E OR3E4 91100846 (00187)      MOV(TQA,00)\NOP
A6F OR3E6 91100846 (00188)      JUMPS(DNESA,F1)
A70 OR3E8 901C0070 (00189)      JUMPC(STALS,F0)
A71 OR3EA 20480000 (00190)      SET(AF0)\NOP
A72 OR3EC 20500000 (00191)      SET(R0)\NOP
A73 OR3EE 08F10000 (00192)      MOV(TQA,A1)\NOP
A74 OR3F0 000008F1 (00193)      NOP\MOV(TQA,A1)
A75 OR3F2 413C413C (00194)      MOV(00),ADD(A1,A1)
A76 OR3F4 08FC0000 (00195)      MOV(TQA,00)\NOP
A77 OR3F6 000008FC (00197)      NOP\MOV(TQA,00)

NO=INDRK(1+3)
FOR I=0,1,2,...,I,TH=1
CLFAR "SKIP" FLAG
NO=RUFFFR ZONE=0
NO=RUFFFR ZONE=0
NO=RUFFFR ZONE=0
NO=RUFFFR ZONE=0
NO=RUFFFR ZONE=0

RELEASE APS INPUT
FIXED POINT ADD
A0=J
A0=K
2J/2K
A1=J
A1=K
2J->S6+1,2J';2K->S7+1,2K
,
INSTRS4 INTO INDS4
INSTRS5 INTO INDS5
NULL=TMP2(J*)
NULL=DCT1(J*)
NULL=TMP2(K*)
NULL=DCT1(K*)
A2=J
A2=K
2J->S4+1,2J';2K->S5+1,2K
,
INSTRS6 INTO INDS6
INSTRS7 INTO INDS7
NULL=TMP2(J**)
NULL=DCT1(J**)
NULL=TMP2(K**)
NULL=DCT1(K**)
I/O HAS SETTLED! DONE?
WAIT FOR OUTPUT TO CLFAR
SET OUTPUT "SKIP" FLAG
RELEASE APS OUTPUT
A1=J
A1=K
2J->S6+1,2J';2K->S7+1,2K
,
INSTRS4 INTO INDS4
INSTRS5 INTO INDS5

```

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A76 0R3F8 0R4C0000 (00100)      MOV(10A,00)\NOP
A79 0R3FA 0R4C0000 (00100)      MOV(10A,00)\NOP
A7A 0R3FC 00000R4C (00200)      NOP\MOV(10A,00)
A7H 0R3FF 00000R4C (00201)      NOP\MOV(10A,00)
A7C 0R400 0R420000 (00202)      MOV(10A,A2)\NOP
A7D 0R402 00000R42 (00203)      NOP\MOV(10A,A2)
A7E 0R404 425C425C (00204)      MOV(00),ADD(A2,A2)

A7F 0R406 0R4C0000 (00205) ;
A80 0R408 00000R4C (00206)      MOV(10A,00)\NOP
A81 0R40A 0R4C0000 (00207)      NOP\MOV(10A,00)
A82 0R40C 0R4C0000 (00208)      MOV(10A,00)\NOP
A83 0R40E 00000R4C (00210)      NOP\MOV(10A,00)
A84 0R410 00000R4C (00211)      NOP\MOV(10A,00)
A85 0R412 1000006F (00212)      JUMP(SHUFFLS)

A86 0R414 20300000 (00214) DNESA CLEAR(R0)\NOP
A87 0R416 20320000 (00216)      CLEAR(RA)\NOP
A88 0R418 00000000 (00218)      NOP
                                SORTSSZ=BA-SORTSSA
                                END SORTSSZ
                                EJECT

                                0R41A

```

```

DCT2(I)=TMP2(J*)
TMP4(I)=DCT1(J*)
DCT2(I+1)=TMP2(K*)
TMP4(I+1)=DCT1(K*)
A2=J**
A2=K**
2J1->S4+1,2J1**2K1->S5+1
,2K1**
INSTRS6 INTO INDS6
INSTRS7 INTO INDS7
DCT2(I+2)=TMP2(J*)
TMP4(I+2)=DCT1(J*)
DCT2(I+3)=TMP2(K*)
TMP4(I+3)=DCT1(K*)
"JUMP(...)" SETTLES 1/0
APS CANNOT TURN ITSELF 0
FF!
API DONE!

```


A1F	0R460	311421F1	(00265)	TSTSL0	SURL(RR1,1),JUMPN(STALS1)	PICKUP TILL NO MORE
A20	0R462	41091F79	(00266)		ADDL(RR0,HS,TF),JUMP(TSTSL0)	IO=TABLE0(1)
A21	0R464	42300037	(00267)	STALS1	SP1(W1)	STALL APS INPUT
A22	0R466	44500103	(00268)		LOAD(RW1,SIZE+4)	TORUR(.) SIZE-1+4
			(00269)	*		
A23	0R468	47400000	(00270)		LOAD(RR0,10),TF)	TORUR(.) RA,IO=TORUR=J
A24	0R46A	48600000	(00271)		LOAD(RR2,MSS)	DUMMY OP FOR EXEC
A25	0R46C	49800000	(00272)		ADD(RR0,MSS,TF)	IO=TORUR(1)=K
A26	0R46E	41040001	(00273)	INDS1	ADD(RR0,HS,TF)	IO=J=TORUR(1+2)
A27	0R470	4F0A0001	(00274)		ADD(RR0,HS,TF)	IO=K'=TORUR(1+3)
A28	0R472	50F41F1C	(00275)		LOAD(RR2,INSTSH4(2),TF)	OVERWRITE INDS4
A29	0R474	52F41F1F	(00276)		LOAD(RR2,INSTSH4(2),TF)	OVERWRITE INDS5
A2A	0R476	54600000	(00277)		LOAD(RR2,MSS)	OVERWRITE INDS6
A2B	0R478	562F0000	(00278)	INDS6	ADD(RR2,MSS,NA,C)	OVERWRITE INDS7
A2C	0R47A	58600000	(00279)		LOAD(RR2,MSS)	RESET RR2 TO 0!
A2D	0R47C	5A2F0000	(00280)	INDS7	ADD(RR2,MSS,NA,C)	MSS=2J,PC->PC1
A2E	0R47E	5F0A0001	(00281)		ADD(RR0,HS,TF)	RESET RR2 TO 0!
A2F	0R480	5FA00001	(00282)		ADD(RR0,HS,TF)	MSS=2J,PC->PC1
A30	0R482	60F41F20	(00283)		LOAD(RR2,INSTSH6(2),TF)	IO=J'
A31	0R484	62F41F22	(00284)		LOAD(RR2,INSTSH7(2),TF)	IO=K''
A32	0R486	64600000	(00285)		LOAD(RR2,MSS)	OVERWRITE INDS6
A33	0R488	662F0000	(00286)	INDS4	ADD(RR2,MSS,NA,C)	OVERWRITE INDS7
A34	0R48A	68600000	(00287)		LOAD(RR2,MSS)	RESET RR2 TO 0!
A35	0R48C	6A2F0000	(00288)	INDS5	ADD(RR2,MSS,NA,C)	MSS=2J,PC->PC1
A36	0R48E	6C192684	(00289)		SURL(RR1,4),JUMPP(INDSJ)	FOR I=0,1,2...LTH+1
			(00290)	*		
A37	0R490	6F200031	(00291)	DNFS1	CLEAR(R1)	INPUT DONE!
A38	0R492	70000020	(00292)		NOP	
A39	0R494	72204040	(00293)	G106S1	JSN(G106S0,P2)	SFT OUTPUT PC
A3A	0R496	74760A00	(00294)	TOPSP1	LOAD(RR1,TMP2S(3))	TMP2(.) RA
A3B	0R498	76890020	(00295)		ADD(RR3,RR2,TF)	IO=TMP2(J* OR K*)
A3C	0R49A	78760200	(00296)		LOAD(RR3,DCT1S(3))	DCT1(.) RA
A3D	0R49C	7A890020	(00297)		ADD(RR3,RR2,TF)	IO=DCT1(J* OR K*)
A3E	0R49E	7C2F0000	(00298)		ADD(RP2,MSS,NA,C)	PC->PC0
A3F	0R4A0	7E003A60	(00299)		JUMP(TOPSP1)	RESET PC AND SETTLE
			(00300)	*		
A40	0R4A2	80306D40	(00301)	G106S0	JSN(G106S3,P3)	SET OUTPUT PC(PC3)
A41	0R4A4	824600FF	(00302)		LOAD(RR0,TARLS0-1(3))	TARLF0(.) RA-1
A42	0R4A6	845600FF	(00303)		LOAD(RR1,TARLS1-1(3))	TARLE1(.) RA-1
A43	0R4A8	866600FF	(00304)		LOAD(RR2,TARLS2-1(3))	TARLE2(.) RA-1
A44	0R4AA	887600FF	(00305)		LOAD(RR3,TARLS3-1(3))	TARLE3(.) RA-1
A45	0R4AC	8A0053F5	(00306)	FLAGSTST	JUMPS(SETSCUT,G1)	G1=1,SET LEVEL, COUNTS
A46	0R4AE	8C004FF4	(00307)		JUMPS(INSTSL0,AF0)	AF0=1 THEN LEVEL, 0
A47	0R4A0	8F0040F4	(00308)		JUMPS(INSTSL1,AF1)	AF1=1 THEN LEVEL, 1

```

A48 0R4R2 90004FA (00309)      JUMPS(INSP1S2,AF2)
A49 0R4H4 920051PH (00310)      JUMPS(INSP1S3,AF3)
A4A 0R4H4 94004560 (00311)      JUMP(FLGSTST)
A4B 0R4H4 9620002H (00312)      INSP1S0 CLEAR(AF0)
A4C 0R4H4 9801457Q (00313)      ADDL(RW0,HS,TF),JUMP(FLGSTST)
A4D 0R4RC 9A20002Q (00314)      INSP1S1 CLEAR(AF1)
A4E 0R4H4 9C11457Q (00315)      ADDL(RW1,HS,TF),JUMP(FLGSTST)
A4F 0R4C0 9E20002A (00316)      INSP1S2 CLEAR(AF2)
A50 0R4C2 9F21457Q (00317)      ADDL(RW2,HS,TF),JUMP(FLGSTST)
A51 0R4C4 A220002H (00318)      INSP1S3 CLEAR(AF3)
A52 0R4C6 A51457Q (00319)      ADDL(RW3,HS,TF),JUMP(FLGSTST)
A53 0R4C8 A6500007 (00320)      SFTSCNT LOAD(RW1,7)
A54 0R4CA A441F13 (00321)      LOAD(RW0,INSTRS0-1(2))
A55 0R4CC A8A00002 (00322) #1    ADD(RW0,WS,TF)
A56 0R4CE AC115M1 (00323)      SUBL(RW1,1),JUMPP(#1)
A57 0R4D0 AF200030 (00324)      CLEAR(R0)
A58 0R4D2 A0500003 (00325)      LOAD(RW1,3)
A59 0R4D4 A2C3FEC0 (00326) #2    LOAD(RW0,APSSMFW(1),TF)
A5A 0R4D6 A4115M1 (00327)      SUBL(RW1,1),JUMPP(#2)
A5B 0R4D8 A6200030 (00328)      CLEAR(R0)
A5C 0R4DA A80000020 (00329)      NOP
A5D 0R4DC A0400000 (00330)      LOAD(RW0,TORDR(3),TF)
A5E 0R4DE AC50010R (00331)      LOAD(RW1,SIZE$+OVRSLAP-1)
A5F 0R4F0 A80A0001 (00332)      ADD(RW0,HS,TF)
A60 0R4F2 C0115F41 (00333)      SUBL(RW1,1),JUMPP(RDSOUT)
                                (00334) ;
                                (00335) *
A61 0R4F4 C24603FF (00336)      LOAD(RW0,DCT2S-2(3))
A62 0R4FA C47604FF (00337)      LOAD(RW3,TMP4S-2(1))
A63 0R4FR C6000020 (00338)      NOP
A64 0R4FA C9441F21 (00339)      LOAD(RW2,INSTRS6+1(2),TF)
A65 0R4FC CB641F23 (00340)      LOAD(RW2,INSTRS7+1(2),TF)
A66 0R4FE CCF3FEC0 (00341)      LOAD(RW2,APSSMFW(1),TF)
A67 0R4F0 CFAF0000 (00342)      ADD(RW2,MSS,TF,C)
A68 0R4F2 D1641F1D (00343)      LOAD(RW2,INSTRS4+1(2),TF)
A69 0R4FA D3641F1F (00344)      LOAD(RW2,INSTRS5+1(2),TF)
A6A 0R4FE D4F3FEC0 (00345)      LOAD(RW2,APSSMFW(1),TF)
A6B 0R4FH D6AF0000 (00346)      ADD(RW2,MSS,TF,C)
A6C 0R4FA D8206370 (00347)      JUMP(INDST,M0),CLEAR
A6D 0R4FC DA300032 (00348)      SET(RA)
A6E 0R4FE DC000020 (00349)      NOP
A6F 0R500 DF0076AR (00350)      JUMPPC(DMYSLIL,BF0)
A70 0R502 E0RA0002 (00351)      ADD(RW0,WS,TF)
A71 0R504 E2HA0002 (00352)      ADD(RW3,WS,TF)

AF2=1 THEN LEVEL 2
AF3=1 THEN LEVEL 3
RE-TEST ALL FLAGS
RELEASE APU
NQ=TABLE0(1),I=I+1
RELEASE APU
NQ=TABLE1(J),J=J+1
RELEASE APU
NQ=TABLE2(K),K=K+1
RELEASE APU
NQ=TABLE3(L),L=L+1
9 "INSTR" WORDS...
TO BE SET OR CLEARED
NQ=INSTRS+1
FOR I=0,1,2...7
STALL APS OUTPUT
INSTRS OVERWRITE
FOR I=0,1...3
STALL APS OUTPUT
RELEASED BY APU
TORDR(.) RA,OQ=IORDR(0)
IORDR(.) SIZE-2+OVLAP
OQ=IORDR(1)
FOR I=1,2...LTH-1;+10 0'
S

DCT2(.) RA-2
TMP4(.) RA-2
LET "JUMP(...)" SETTIF
SET MSS IN INSTRS6
SET MSS IN INSTRS7
OVERWRITE INDS4
OVERWRITE INDS5;PC3->PC
SET MSS IN INSTRS4
SET MSS IN INSTRS5
OVERWRITE INDS6
OVERWRITE INDS7;PC3->PC
RESET & STALL
ENABLE APU
LET "JUMP(...)" SETTIF
WAIT FOR "SKIP" FLAG
OQ=DCT2(J*)
NQ=TMP4(J*)=DCT1(J*)

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PAGE 10: FCN 247... "MPSSPT(Y)" SORT DCT1(.) COEFFICIENTS

A72 04506	144A0002	(00353)	ADD(HW0,MS,TF)	00=DCT2(K+)
A73 04508	144A0002	(00354)	ADD(HW3,MS,TF)	00=TMP4(K+)=DCT1T(K+)
A74 0450A	142F0000	(00355)	CHNGSPC	NO 101 CHANGE PC
A75 0450C	14006F60	(00356)	JUMP(TDPS3)	RESET PC AND SETTLE
A76 0450F	14CF20794	(00357)	DMYSPIL	00=?
A77 04510	144A0000	(00358)	ADD(HW2,DWYS(1),TF)	00=?
A78 04512	140A0000	(00359)	ADD(HW2,MS,TF)	00=?
A79 04514	142A17474	(00360)	ADDL(HW2,MS,TF),JUMP(CHNGSPC)	00=? THEN CHANGE PC
		(00361)		
	00008468	(00362)	G106SAE BC	ASSIGN VALUE TO CHAIN AN
		(00363)		CHOR
04516		(00364)	END #A-1	
04516	00000000	(00365)	STOPAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS	
	000000F6	(00366)	G10AST DATA 1F'0.0'	
04518		(00367)	G106S7=81-G106S	
		(00368)	END	

APDSURGE:	0006H (0000H) (0003H)	
APSSMFM:	1FEC0 (00009) (00326) (00341) (00345)	
CHNGSPC:	0007A (00355) (00360)	
CNTGOUT:	0003A (00101) (00111) (00121) (00132)	
CSPUSMUS:	021FC (00010) (00041)	
DCTS:	0092H (00011) (00296)	
DCT1S:	00200 (00012) (00316)	
DCT2S:	00400 (00013) (00357)	
DCTS:	00794 (00014) (00357)	
DMSFILL:	00076 (00350) (00357)	
DMSA:	00086 (00188) (00214)	
DMS1:	00037 (00291) (00311) (00315) (00317) (00319)	
FLGSTST:	00045 (00306) (00277) (00234) (00167)	
G10AS:	08427 (00040) (00293)	
G10AS1:	00039 (00234) (00348)	
G10AS3:	00060 (00301) (00362)	
G10ASA:	0846H (00230) (00366)	
G10AS1:	08516 (00225) (00301)	
G10AS0:	00030 (00243) (00367)	
G10AS2:	000F6 (00229) (00243) (00254) (00258) (00266) (00273) (00274) (00281) (00282)	
HS:	00001 (00016) (00313) (00315) (00317) (00319) (00332)	
INCS1:	00008 (00242) (00244)	
INS0:	00014 (00049) (00264)	
INS1:	0001A (00050) (00260)	
INS2:	00016 (00051) (00256)	
INS3:	00012 (00052) (00252)	
INCS1:	0000C (00086) (00100) (00110) (00120) (00130)	
INCRFS:	00100 (00017) (00029) (00030) (00031)	
INSA:	00033 (00051) (00286)	
INS5:	00035 (00054) (00288)	
INSA:	0002H (00055) (00278)	
INS7:	00020 (00056) (00280)	
INS1:	00063 (00338) (00347)	
INSJ:	00026 (00273) (00289)	
INSRTS0:	0004H (00307) (00312)	
INSRTS1:	00040 (00308) (00314)	
INSRTS2:	0004F (00309) (00316)	
INSRTS3:	00051 (00310) (00318)	
INSTRS0:	01F14 (00049) (00247) (00321)	
INSTRS1:	01F16 (00050)	
INSTRS2:	01F18 (00051)	
INSTRS3:	01F1A (00052)	
INSTRS4:	01F1C (00053) (00275) (00343)	

INSTR\$5:	01F1P (00054)	(00276)	(00344)
INSTR\$6:	01F20 (00055)	(00283)	(00339)
INSTR\$7:	01F22 (00056)	(00284)	(00340)
INTGMS:	01F24 (00018)	(00241)	
INWOPS:	00000 (00019)	(00330)	
ISAS00:	00502 (00021)		
ISAS01:	00503 (00022)	(00238)	
ISVTS:	00507 (00020)	(00021)	(00022)
I0\$SU:	00010 (00261)	(00263)	
I1\$SU:	00019 (00257)	(00259)	
I2\$SU:	00015 (00253)	(00255)	
I3\$SU:	00011 (00251)		
LVI\$0:	00030 (00087)	(00127)	
LVI\$1:	00026 (00088)	(00112)	
LVI\$2:	0001C (00091)	(00102)	
LVI\$3:	00012 (00092)		
MSS:	00000 (00023)	(00252)	(00277) (00278) (00279)
	(00280)	(00285)	(00286) (00287) (00288) (00289) (00290)
	(00359)	(00360)	
ORDR\$1:	00040 (00151)	(00155)	
OVPSLAP:	0000A (00024)	(00331)	
POSUIT:	0005F (00332)	(00333)	
SCIRS:	00001 (00227)	(00235)	
SFTSCNT:	00053 (00306)	(00320)	
SHUFLS:	0006F (00188)	(00212)	
SHUFLSV:	0005H (00163)		
S1ZFS:	000FF (00025)	(00240)	(00268) (00331)
SORTS:	0030R (00039)	(00072)	
SORT\$SA:	00000 (00070)	(00074)	(00218)
SORT\$SZ:	00089 (00071)	(00218)	(00219)
STALS:	00070 (00189)	(00189)	
STAL\$1:	00021 (00265)	(00267)	
START:	00400 (00027)	(00058)	
SVTS:	003M2 (00026)		
TIS:	00300 (00062)	(00235)	
T2S:	00402 (00063)	(00236)	
T3S:	00404 (00064)	(00237)	
TARLS0:	00C00 (00028)	(00029)	(00263) (00302)
TARLS1:	00000 (00029)	(00030)	(00259) (00303)
TARLS2:	00F00 (00030)	(00031)	(00255) (00304)
TARLS3:	00F00 (00031)	(00251)	(00305)
TMP2S:	00A00 (00032)	(00239)	(00294)
TMP4S:	00C00 (00033)	(00337)	
TNPSPI:	0003A (00294)	(00290)	

PAGE 13: 6CH 247... "MDSRT(V)" SONT DCTI(.) COEFFICIENTS

TOPSP3: 0006F (00349) (00356)
TSTSL0: 0001F (00265) (00266)
TSTSL1: 0001M (00261) (00262)
TSTSL2: 00017 (00257) (00258)
TSTSL3: 00013 (00253) (00254)
WS: 00002 (00034) (00242) (00248) (00322) (00351) (00352) (00353) (00354)
ZS: 00003 (00035)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) E- 0

MAKH001, IDCT CORRECTION
ORIGINATED:16-JAN-80
UPDATED:17-JUN-80

FCR 24R... "MPIDCM(Y,U,V)"

```
(00001) * FCR 24R... "MPIDCM(Y,U,V)"
(00002) *
(00003) *
(00004) * DEFINE GLOBAL SYMBOLS
(00005) *
(00006) * OPADD EXP, (1 .LS, 14) + (12 .LS, 4) + $1E
(00007) * OPADD OPF, (3 .LS, 10) + (12 .LS, 5) + $1H
(00008) * OPADD WAFNF, (1 .LS, 10) + (12 .LS, 5) + $1A
(00009) * OPADD FO, (3 .LS, 10) + (12 .LS, 5) + $1C
(00010) * AFDTUNG=$8F8
(00011) * COSZSD=1024
(00012) * CSPUSMUS=$21FC
(00013) * WYYS=$794
(00014) * IDPCTS=D'1024
(00015) * MS=1
(00016) * MS=1
(00017) * MS=0
(00018) * SIZES=D'256'-D'1
(00019) * STNZSD=1536
(00020) * SVTS=$03R2
(00021) * SAS27=SVTS+2*D'27
(00022) * SAS50=SVTS+2*D'50
(00023) * SAS9H=SVTS+2*D'9H
(00024) * SAS100=SVTS+2*D'100
(00025) * SAS101=SVTS+2*D'101
(00026) * START=$7150
(00027) * MS=2
(00028) * WWS=4
(00029) * WWS=5
(00030) * WWS=6
(00031) * ZS=3
(00032) * ZZMS=7
(00033) *
(00034) * EXPAND ARRAY FUNCTION DISPATCH TABLE
(00035) *
(00036) * $1=AFDTUNG+3*2*(24R-12R)
(00037) * ADDR IDCMS(7,1)
(00038) * ADDR G2015(47,1)
(00039) * ADDR CSPUSMUS(,1,0)
(00040) * EFFECT
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FCR 24R


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(00034) *
(00040) *
(00041) *
00007150 (00042) #1=START
(00043) EVFN
07150 20000034 (00044) TARI.FS DATA 2.3841450E-07
07152 40000044 (00045) DATA 3.2768000E+04
07154 08200030 (00046) DATA 1.5497208E-04
07156 30864150 (00047) DATA 0.249572748E+34
07158 12640007 (00048) DATA 0.712208897E+27
0715A 72551101 (00049) DATA 0.263632333E+21
0715C 1FR05ACC (00050) DATA 0.675972338E+14
0715E 88000038 (00051) TARI.FS2 DATA -5.9604646E-08
07160 58893046 (00052) DATA 0.116291788E+08
07162 78555540 (00053) DATA 0.9999999250
(00054) EJECT
2**(-22)
2**15
4*65*16**(-6)
C5
C4
C3
C2
-16**(-6)
C1
C0

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A10 071A0 ORFCORFC (00099)
A11 071A2 ORF00RFD (00100)
A12 071A4 ORF00RFD (00101)
A20 071A6 ORF00RFD (00102)
A21 071A8 ORF00RFD (00103)
A22 071A8 90160714 (00104)
A23 071AC 20172037 (00105)
A24 071AF 00000000 (00106)
A25 071A0 ORF00RFD (00108)
A26 071A2 ORF00RFD (00109)
A27 071A4 84200000 (00110)
A28 071A6 ORFC0000 (00111)
A29 071A8 20172037 (00112)
A2A 071A8 ORF00RFD (00113)
A2B 071AC ORFC00FC (00114)
A2C 071AF 854005H0 (00116)
A2D 071C0 ORF00RFD (00117)
A2E 071C2 ORF00RFD (00118)
A2F 071C4 ORF00RFD (00119)
A30 071C6 ORF00RFD (00120)
A31 071C8 ORF00RFD (00121)
A32 071CA ORF50RFD (00122)
A33 071CC ORFC00RAC (00123)
A34 071CE ORFC0000 (00124)
A35 071C0 R51C0000 (00125)
A36 071D2 1A6R3A6H (00126)
A37 071D4 1F731F73 (00127)
A38 071D6 R5D1H5D1 (00128)
A39 071D8 3A2H3A2H (00129)
A3A 071DA 32523252 (00130)
A3B 071DC ORR30HH3 (00131)
A3C 071DE 46704670 (00132)
A3D 071F0 022H022H (00133)
A3E 071F2 R5C0R5C0 (00135)
A3F 071F4 ORF20RFD (00136)
A40 071F6 5A005A00 (00137)
A41 071F8 ORR30HR3 (00138)
A42 071FA 45704570 (00139)
A43 071FC 4A0H4A0H (00140)
A44 071FE R5C0R5C0 (00141)
A45 071F0 ORF20RFD (00142)

MOV(T0A,M4)
MOV(T0A,M5)
MOV(T0A,M0)
MOV(T0A,M1)
NOP
JHMP(C10CLP,FW1)
CLEAR(M1)
NOP
MOV(T0A,M0)\NOP
MOV(T0A,M5)\NOP
MUL(M0,M5)\NOP
MOV(P,00)\NOP
CLEAR(M1)
MOV(T0A,M3)
MOV(T0A,M4)
MUL(M3,M4)
MOV(T0A,M5)
MOV(T0A,M4)
MOV(T0A,M1)
MOV(T0A,M7)
MOV(T0A,M6)
MOV(T0A,M5)
MOV(P,M4)
MOV(T0A,M2)\NOP
MOV(00),MUL(M2,M4)\NOP
MOV(M3),ALIGNCA31
MOVCA3),EXP(A3)
MOVCA1),MUL(M3,M6)
MOV(M0),ALIGNCA1)
MOV(A2),MURM(A2)
MOVIP,A3)
MOV(A0),ADD(A3,A6)
MOV(M3),M(A1)
MUL(M3,M6)
MOV(T0A,A2)
ADDIT(A0,A2)
MOV(P,A3)
MOV(A0),ADD(A3,A5)
MOV(M3),ADD(A0,A4)
MUL(M3,M6)
MOV(T0A,A2)

M0=VAR''
M5=1.0/20.0=0.05
0.05*VAR''
00=VAR''=0.05*VAR''
RELFAF APS INPUT
M3=SA TEMPORARILY
M4=2*(-22) TEMPORARILY
M5=2*(-15)
M4=4*65*16*(-6)
M1=C5
A7=C4
A6=C3
A5=C2
M4=SA*2*(-22)
M2=X(0,K)=VAR''
M0=X(19,K-2)=VAR,X(1,K)
A3=X(9,K-1),X(7,K-1)
A2=X(7,K-1),X(10,K-1)
A1=X(1,K),X(11,K-1)
M0=X(10,K-1),X(2,K)
A2=X(2,K),X(3A,K)
A3=X(11,K-1)
A0=X(3A,K),X(12,K-1)
M3=X(12,K-1),SET T HIT R
Y X(3A,K)
X(13,K-1)
A2=-16*(-6)
X(3R,K)
A3=X(13,K-1)
A0=X(93R,K),X(14,K-1)
M3=X(A4,K-1),X(4,K)
X(15,K-1)
A2=C1

```

```

A46 071F2 08H00000 (00143)
A47 071F4 85338533 (00144)
A48 071F6 4340340 (00145)
A49 071F8 48204820 (00146)
A4A 071FA 85038503 (00147)
A4B 071FB 08F208F2 (00148)
A4C 071FC 08H00000 (00149)
A4D 071FE 08H00000 (00150)
A4E 07200 08H00000 (00151)
A4F 07202 84C084C0 (00152)
A50 07204 42204220 (00153)
A51 07206 08H00000 (00154)
A52 07208 8318471 (00155)
A53 0720C 47204720 (00156)
A54 0720F 90160034 (00157)
A55 07210 20372037 (00158)
A56 07212 08H00000 (00159) *
A57 07214 08H00000 (00160) DCS
A58 07216 00000000 (00161)
A59 07218 84208420 (00162)
A5A 0721A 08H00000 (00163)
A5B 0721C 20320000 (00164) ?
A5C 0721E 00000000 (00165) *
A5D 07220 10000000 (00166) DNESA
A5E 07222 00000054 (00167)
A5F 07224 00000000 (00168)
A60 07226 00000000 (00169)
A61 07228 00000000 (00170)
A62 0722A 00000000 (00171)
A63 0722C 00000000 (00172)
A64 0722E 00000000 (00173)
A65 07230 00000000 (00174)
A66 07232 00000000 (00175)
A67 07234 00000000 (00176)
A68 07236 00000000 (00177)
A69 07238 00000000 (00178)
A6A 0723A 00000000 (00179)
A6B 0723C 00000000 (00180)
A6C 0723E 00000000 (00181)
A6D 07240 00000000 (00182)
A6E 07242 00000000 (00183)
A6F 07244 00000000 (00184)
A6G 07246 00000000 (00185)
A6H 07248 00000000 (00186)
A6I 0724A 00000000 (00187)
A6J 0724C 00000000 (00188)
A6K 0724E 00000000 (00189)
A6L 07250 00000000 (00190)
A6M 07252 00000000 (00191)
A6N 07254 00000000 (00192)
A6O 07256 00000000 (00193)
A6P 07258 00000000 (00194)
A6Q 0725A 00000000 (00195)
A6R 0725C 00000000 (00196)
A6S 0725E 00000000 (00197)
A6T 07260 00000000 (00198)
A6U 07262 00000000 (00199)
A6V 07264 00000000 (00200)
A6W 07266 00000000 (00201)
A6X 07268 00000000 (00202)
A6Y 0726A 00000000 (00203)
A6Z 0726C 00000000 (00204)
A70 0726E 00000000 (00205)
A71 07270 00000000 (00206)
A72 07272 00000000 (00207)
A73 07274 00000000 (00208)
A74 07276 00000000 (00209)
A75 07278 00000000 (00210)
A76 0727A 00000000 (00211)
A77 0727C 00000000 (00212)
A78 0727E 00000000 (00213)
A79 07280 00000000 (00214)
A7A 07282 00000000 (00215)
A7B 07284 00000000 (00216)
A7C 07286 00000000 (00217)
A7D 07288 00000000 (00218)
A7E 0728A 00000000 (00219)
A7F 0728C 00000000 (00220)
A7G 0728E 00000000 (00221)
A7H 07290 00000000 (00222)
A7I 07292 00000000 (00223)
A7J 07294 00000000 (00224)
A7K 07296 00000000 (00225)
A7L 07298 00000000 (00226)
A7M 0729A 00000000 (00227)
A7N 0729C 00000000 (00228)
A7O 0729E 00000000 (00229)
A7P 072A0 00000000 (00230)
A7Q 072A2 00000000 (00231)
A7R 072A4 00000000 (00232)
A7S 072A6 00000000 (00233)
A7T 072A8 00000000 (00234)
A7U 072AA 00000000 (00235)
A7V 072AC 00000000 (00236)
A7W 072AE 00000000 (00237)
A7X 072B0 00000000 (00238)
A7Y 072B2 00000000 (00239)
A7Z 072B4 00000000 (00240)
A80 072B6 00000000 (00241)
A81 072B8 00000000 (00242)
A82 072BA 00000000 (00243)
A83 072BC 00000000 (00244)
A84 072BE 00000000 (00245)
A85 072C0 00000000 (00246)
A86 072C2 00000000 (00247)
A87 072C4 00000000 (00248)
A88 072C6 00000000 (00249)
A89 072C8 00000000 (00250)
A8A 072CA 00000000 (00251)
A8B 072CC 00000000 (00252)
A8C 072CE 00000000 (00253)
A8D 072D0 00000000 (00254)
A8E 072D2 00000000 (00255)
A8F 072D4 00000000 (00256)
A8G 072D6 00000000 (00257)
A8H 072D8 00000000 (00258)
A8I 072DA 00000000 (00259)
A8J 072DC 00000000 (00260)
A8K 072DE 00000000 (00261)
A8L 072E0 00000000 (00262)
A8M 072E2 00000000 (00263)
A8N 072E4 00000000 (00264)
A8O 072E6 00000000 (00265)
A8P 072E8 00000000 (00266)
A8Q 072EA 00000000 (00267)
A8R 072EC 00000000 (00268)
A8S 072EE 00000000 (00269)
A8T 072F0 00000000 (00270)
A8U 072F2 00000000 (00271)
A8V 072F4 00000000 (00272)
A8W 072F6 00000000 (00273)
A8X 072F8 00000000 (00274)
A8Y 072FA 00000000 (00275)
A8Z 072FC 00000000 (00276)
A90 072FE 00000000 (00277)
A91 07300 00000000 (00278)
A92 07302 00000000 (00279)
A93 07304 00000000 (00280)
A94 07306 00000000 (00281)
A95 07308 00000000 (00282)
A96 0730A 00000000 (00283)
A97 0730C 00000000 (00284)
A98 0730E 00000000 (00285)
A99 07310 00000000 (00286)
A9A 07312 00000000 (00287)
A9B 07314 00000000 (00288)
A9C 07316 00000000 (00289)
A9D 07318 00000000 (00290)
A9E 0731A 00000000 (00291)
A9F 0731C 00000000 (00292)
A9G 0731E 00000000 (00293)
A9H 07320 00000000 (00294)
A9I 07322 00000000 (00295)
A9J 07324 00000000 (00296)
A9K 07326 00000000 (00297)
A9L 07328 00000000 (00298)
A9M 0732A 00000000 (00299)
A9N 0732C 00000000 (00300)
A9O 0732E 00000000 (00301)
A9P 07330 00000000 (00302)
A9Q 07332 00000000 (00303)
A9R 07334 00000000 (00304)
A9S 07336 00000000 (00305)
A9T 07338 00000000 (00306)
A9U 0733A 00000000 (00307)
A9V 0733C 00000000 (00308)
A9W 0733E 00000000 (00309)
A9X 07340 00000000 (00310)
A9Y 07342 00000000 (00311)
A9Z 07344 00000000 (00312)
AA0 07346 00000000 (00313)
AA1 07348 00000000 (00314)
AA2 0734A 00000000 (00315)
AA3 0734C 00000000 (00316)
AA4 0734E 00000000 (00317)
AA5 07350 00000000 (00318)
AA6 07352 00000000 (00319)
AA7 07354 00000000 (00320)
AA8 07356 00000000 (00321)
AA9 07358 00000000 (00322)
AAA 0735A 00000000 (00323)
AAB 0735C 00000000 (00324)
AAC 0735E 00000000 (00325)
AAD 07360 00000000 (00326)
AAE 07362 00000000 (00327)
AAF 07364 00000000 (00328)
AAG 07366 00000000 (00329)
AAH 07368 00000000 (00330)
AAI 0736A 00000000 (00331)
AAJ 0736C 00000000 (00332)
AAK 0736E 00000000 (00333)
AAL 07370 00000000 (00334)
AAO 07372 00000000 (00335)
AAP 07374 00000000 (00336)
AAQ 07376 00000000 (00337)
AAR 07378 00000000 (00338)
AAS 0737A 00000000 (00339)
AAU 0737C 00000000 (00340)
AAV 0737E 00000000 (00341)
AAW 07380 00000000 (00342)
AAZ 07382 00000000 (00343)
AAB 07384 00000000 (00344)
AAC 07386 00000000 (00345)
AAD 07388 00000000 (00346)
AAE 0738A 00000000 (00347)
AAH 0738C 00000000 (00348)
AAI 0738E 00000000 (00349)
AAJ 07390 00000000 (00350)
AAK 07392 00000000 (00351)
AAL 07394 00000000 (00352)
AAO 07396 00000000 (00353)
AAP 07398 00000000 (00354)
AAQ 0739A 00000000 (00355)
AAR 0739C 00000000 (00356)
AAS 0739E 00000000 (00357)
AAU 073A0 00000000 (00358)
AAV 073A2 00000000 (00359)
AAW 073A4 00000000 (00360)
AAZ 073A6 00000000 (00361)
AAB 073A8 00000000 (00362)
AAC 073AA 00000000 (00363)
AAD 073AC 00000000 (00364)
AAE 073AE 00000000 (00365)
AAH 073B0 00000000 (00366)
AAI 073B2 00000000 (00367)
AAJ 073B4 00000000 (00368)
AAK 073B6 00000000 (00369)
AAL 073B8 00000000 (00370)
AAO 073BA 00000000 (00371)
AAP 073BC 00000000 (00372)
AAQ 073BE 00000000 (00373)
AAR 073C0 00000000 (00374)
AAS 073C2 00000000 (00375)
AAU 073C4 00000000 (00376)
AAV 073C6 00000000 (00377)
AAW 073C8 00000000 (00378)
AAZ 073CA 00000000 (00379)
AAB 073CC 00000000 (00380)
AAC 073CE 00000000 (00381)
AAD 073D0 00000000 (00382)
AAE 073D2 00000000 (00383)
AAH 073D4 00000000 (00384)
AAI 073D6 00000000 (00385)
AAJ 073D8 00000000 (00386)
AAK 073DA 00000000 (00387)
AAL 073DC 00000000 (00388)
AAO 073DE 00000000 (00389)
AAP 073E0 00000000 (00390)
AAQ 073E2 00000000 (00391)
AAR 073E4 00000000 (00392)
AAS 073E6 00000000 (00393)
AAU 073E8 00000000 (00394)
AAV 073EA 00000000 (00395)
AAW 073EC 00000000 (00396)
AAZ 073EE 00000000 (00397)
AAB 073F0 00000000 (00398)
AAC 073F2 00000000 (00399)
AAD 073F4 00000000 (00400)
AAE 073F6 00000000 (00401)
AAH 073F8 00000000 (00402)
AAI 073FA 00000000 (00403)
AAJ 073FC 00000000 (00404)
AAK 073FE 00000000 (00405)
AAL 07400 00000000 (00406)
AAO 07402 00000000 (00407)
AAP 07404 00000000 (00408)
AAQ 07406 00000000 (00409)
AAR 07408 00000000 (00410)
AAS 0740A 00000000 (00411)
AAU 0740C 00000000 (00412)
AAV 0740E 00000000 (00413)
AAW 07410 00000000 (00414)
AAZ 07412 00000000 (00415)
AAB 07414 00000000 (00416)
AAC 07416 00000000 (00417)
AAD 07418 00000000 (00418)
AAE 0741A 00000000 (00419)
AAH 0741C 00000000 (00420)
AAI 0741E 00000000 (00421)
AAJ 07420 00000000 (00422)
AAK 07422 00000000 (00423)
AAL 07424 00000000 (00424)
AAO 07426 00000000 (00425)
AAP 07428 00000000 (00426)
AAQ 0742A 00000000 (00427)
AAR 0742C 00000000 (00428)
AAS 0742E 00000000 (00429)
AAU 07430 00000000 (00430)
AAV 07432 00000000 (00431)
AAW 07434 00000000 (00432)
AAZ 07436 00000000 (00433)
AAB 07438 00000000 (00434)
AAC 0743A 00000000 (00435)
AAD 0743C 00000000 (00436)
AAE 0743E 00000000 (00437)
AAH 07440 00000000 (00438)
AAI 07442 00000000 (00439)
AAJ 07444 00000000 (00440)
AAK 07446 00000000 (00441)
AAL 07448 00000000 (00442)
AAO 0744A 00000000 (00443)
AAP 0744C 00000000 (00444)
AAQ 0744E 00000000 (00445)
AAR 07450 00000000 (00446)
AAS 07452 00000000 (00447)
AAU 07454 00000000 (00448)
AAV 07456 00000000 (00449)
AAW 07458 00000000 (00450)
AAZ 0745A 00000000 (00451)
AAB 0745C 00000000 (00452)
AAC 0745E 00000000 (00453)
AAD 07460 00000000 (00454)
AAE 07462 00000000 (00455)
AAH 07464 00000000 (00456)
AAI 07466 00000000 (00457)
AAJ 07468 00000000 (00458)
AAK 0746A 00000000 (00459)
AAL 0746C 00000000 (00460)
AAO 0746E 00000000 (00461)
AAP 07470 00000000 (00462)
AAQ 07472 00000000 (00463)
AAR 07474 00000000 (00464)
AAS 07476 00000000 (00465)
AAU 07478 00000000 (00466)
AAV 0747A 00000000 (00467)
AAW 0747C 00000000 (00468)
AAZ 0747E 00000000 (00469)
AAB 07480 00000000 (00470)
AAC 07482 00000000 (00471)
AAD 07484 00000000 (00472)
AAE 07486 00000000 (00473)
AAH 07488 00000000 (00474)
AAI 0748A 00000000 (00475)
AAJ 0748C 00000000 (00476)
AAK 0748E 00000000 (00477)
AAL 07490 00000000 (00478)
AAO 07492 00000000 (00479)
AAP 07494 00000000 (00480)
AAQ 07496 00000000 (00481)
AAR 07498 00000000 (00482)
AAS 0749A 00000000 (00483)
AAU 0749C 00000000 (00484)
AAV 0749E 00000000 (00485)
AAW 07500 00000000 (00486)
AAZ 07502 00000000 (00487)
AAB 07504 00000000 (00488)
AAC 07506 00000000 (00489)
AAD 07508 00000000 (00490)
AAE 0750A 00000000 (00491)
AAH 0750C 00000000 (00492)
AAI 0750E 00000000 (00493)
AAJ 07510 00000000 (00494)
AAK 07512 00000000 (00495)
AAL 07514 00000000 (00496)
AAO 07516 00000000 (00497)
AAP 07518 00000000 (00498)
AAQ 0751A 00000000 (00499)
AAR 0751C 00000000 (00500)
AAS 0751E 00000000 (00501)
AAU 07520 00000000 (00502)
AAV 07522 00000000 (00503)
AAW 07524 00000000 (00504)
AAZ 07526 00000000 (00505)
AAB 07528 00000000 (00506)
AAC 0752A 00000000 (00507)
AAD 0752C 00000000 (00508)
AAE 0752E 00000000 (00509)
AAH 07530 00000000 (00510)
AAI 07532 00000000 (00511)
AAJ 07534 00000000 (00512)
AAK 07536 00000000 (00513)
AAL 07538 00000000 (00514)
AAO 0753A 00000000 (00515)
AAP 0753C 00000000 (00516)
AAQ 0753E 00000000 (00517)
AAR 07540 00000000 (00518)
AAS 07542 00000000 (00519)
AAU 07544 00000000 (00520)
AAV 07546 00000000 (00521)
AAW 07548 00000000 (00522)
AAZ 0754A 00000000 (00523)
AAB 0754C 00000000 (00524)
AAC 0754E 00000000 (00525)
AAD 07550 00000000 (00526)
AAE 07552 00000000 (00527)
AAH 07554 00000000 (00528)
AAI 07556 00000000 (00529)
AAJ 07558 00000000 (00530)
AAK 0755A 00000000 (00531)
AAL 0755C 00000000 (00532)
AAO 0755E 00000000 (00533)
AAP 07560 00000000 (00534)
AAQ 07562 00000000 (00535)
AAR 07564 00000000 (00536)
AAS 07566 00000000 (00537)
AAU 07568 00000000 (00538)
AAV 0756A 00000000 (00539)
AAW 0756C 00000000 (00540)
AAZ 0756E 00000000 (00541)
AAB 07570 00000000 (00542)
AAC 07572 00000000 (00543)
AAD 07574 00000000 (00544)
AAE 07576 00000000 (00545)
AAH 07578 00000000 (00546)
AAI 0757A 00000000 (00547)
AAJ 0757C 00000000 (00548)
AAK 0757E 00000000 (00549)
AAL 07580 00000000 (00550)
AAO 07582 00000000 (00551)
AAP 07584 00000000 (00552)
AAQ 07586 00000000 (00553)
AAR 07588 00000000 (00554)
AAS 0758A 00000000 (00555)
AAU 0758C 00000000 (00556)
AAV 0758E 00000000 (00557)
AAW 07590 00000000 (00558)
AAZ 07592 00000000 (00559)
AAB 07594 00000000 (00560)
AAC 07596 00000000 (00561)
AAD 07598 00000000 (00562)
AAE 0759A 00000000 (00563)
AAH 0759C 00000000 (00564)
AAI 0759E 00000000 (00565)
AAJ 07600 00000000 (00566)
AAK 07602 00000000 (00567)
AAL 07604 00000000 (00568)
AAO 07606 00000000 (00569)
AAP 07608 00000000 (00570)
AAQ 0760A 00000000 (00571)
AAR 0760C 00000000 (00572)
AAS 0760E 00000000 (00573)
AAU 07610 00000000 (00574)
AAV 07612 00000000 (00575)
AAW 07614 00000000 (00576)
AAZ 07616 00000000 (00577)
AAB 07618 00000000 (00578)
AAC 0761A 00000000 (00579)
AAD 0761C 00000000 (00580)
AAE 0761E 00000000 (00581)
AAH 07620 00000000 (00582)
AAI 07622 00000000 (00583)
AAJ 07624 00000000 (00584)
AAK 07626 00000000 (00585)
AAL 07628 00000000 (00586)
AAO 0762A 00000000 (00587)
AAP 0762C 00000000 (00588)
AAQ 0762E 00000000 (00589)
AAR 07630 00000000 (00590)
AAS 07632 00000000 (00591)
AAU 07634 00000000 (00592)
AAV 07636 00000000 (00593)
AAW 07638 00000000 (00594)
AAZ 0763A 00000000 (00595)
AAB 0763C 00000000 (00596)
AAC 0763E 00000000 (00597)
AAD 07640 00000000 (00598)
AAE 07642 00000000 (00599)
AAH 07644 00000000 (00600)
AAI 07646 00000000 (00601)
AAJ 07648 00000000 (00602)
AAK 0764A 00000000 (00603)
AAL 0764C 00000000 (00604)
AAO 0764E 00000000 (00605)
AAP 07650 00000000 (00606)
AAQ 07652 00000000 (00607)
AAR 07654 00000000 (00608)
AAS 07656 00000000 (00609)
AAU 07658 00000000 (00610)
AAV 0765A 00000000 (00611)
AAW 0765C 00000000 (00612)
AAZ 0765E 00000000 (00613)
AAB 07660 00000000 (00614)
AAC 07662 00000000 (00615)
AAD 07664 00000000 (00616)
AAE 07666 00000000 (00617)
AAH 07668 00000000 (00618)
AAI 0766A 00000000 (00619)
AAJ 0766C 00000000 (00620)
AAK 0766E 00000000 (00621)
AAL 07670 00000000 (00622)
AAO 07672 00000000 (00623)
AAP 07674 00000000 (00624)
AAQ 07676 00000000 (00625)
AAR 07678 00000000 (00626)
AAS 0767A 00000000 (00627)
AAU 0767C 00000000 (00628)
AAV 0767E 00000000 (00629)
AAW 07680 00000000 (00630)
AAZ 07682
```

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(00173) *
(00174) *
(00175) *
(00176) *
(00177) *
(00178) *
(00179) *
(00180) *
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(00182) *
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(00184) *
(00185) *
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(00202) *
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(00205) *
(00206) *
(00207) *
(00208) *
(00209) *
(00210) *
(00211) *
(00212) *
(00213) *
(00214) *
(00215) *
(00216) *

07222 00007206      EVEN
07222 00007206      ADDR G201S
07224 00007206      ADDR G201S+2*SCALRS
07226 00007206      DATA 0
07227 00007206      DATA G201S2
07228 00007206      ADDR G201S4
07229 00007206      EVEN
0722A 00203740      BEGIN APS(G201)
0722B 00203740      JSN(G201S0,02)
0722C 02300030      SET(C0)
0722D 04402000      LOAD(RR0,121)
0722E 06500000      LOAD(RR1,MSS)
0722F 08600000      LOAD(RR2,MSS)
07230 0A701006      LOAD(RR2,11),TF)
07231 0C700000      LOAD(RR3,MSS)
07232 0E700000      LOAD(RR3,MSS)
07233 10190039      ADDL(RR1,1)
07234 12A9002H      ADDH(RR2,RR1,TF)
07235 14B9002R      ADDR(RR0,RR1,TF)
07236 1619002R      ADDR(RR1,RR1)
07237 18090023      SUBH(RR0,RR1)
07238 1A390015      MOVH(RR3,RR2)
07239 1C09003A      ADDL(RR0,2)
07240 1E190035      SUML(RR1,5)
07241 20190039      ADDL(RR1,1)
07242 22A20002      SUBH(RR2,2,TF)
07243 241A0002      ADDH(RR3,2,TF)
07244 26A9002H      ADDH(RR0,RR1,TF)
07245 28090023      SUBH(RR0,RR1)
07246 2A0A0200      ADDH(RR0,2*256,TF)
07247 2C0201FF      SUML(RR1,5),JUMPP(R1)
07248 2E1910A5      LOAD(RR1,6)
07249 30500006      LOAD(RR0,DWYS(1),TF)
07250 32C20794      SUML(RR1,1),JUMPP(R2)
07251 341919H1      LOAD(RR0,DWYS(1),TF)
07252 36C20794      SET(CW1)
07253 38300037      NOP
07254 3A000020      NOP

```

POINTER TO CONSTRUCTED I
 NSTRUCTION BLOCK
 POINTER TO SCALAR BLOCK
 NUMBR OF SCALARS
 MODULE SIZE
 POINTER TO CHAIN ANCHOR
 END ON WORD BOUNDARY

SET OUTPUT PC(P2)
 ENABLE OUTPUT ADDRESSING
 COS(0)
 N-1
 DUMMY
 C(0)
 DUMMY
 DUMMY
 N
 C(N/2)
 COS(N/2) (=PI/4)
 2N
 COS(-N/2)
 C(N/2)
 COS(-N/2+1)
 4*(N-1)-1

FIX FROM LOOP TEST
 C(K)
 C(N-K)
 COS(K)
 SIN(K)=COS(N-K)
 C(-NEW K)
 NEED P DUMMIES
 STALL APS INPUT
 NOTE: 6 DUMMIES ARE NEEDED

```

(00217) ?
(00218) *
A1F 07266 4CC2041C IMULS
A1F 07268 3FC20446
A20 0726A 40300037
A21 0726C 42000020 (00222) *
A22 0726E 44C2043H (00224) *
A23 07270 46C27150 (00225)
A24 07272 48A00002 (00226)
A25 07274 4AA00002 (00227)
A26 07276 4C8A0002 (00228)
A27 07278 4E8A0002 (00229)
A28 0727A 508A0002 (00230)
A29 0727C 528A0002 (00231)
A2A 0727E 54700002 (00232)
A2H 07280 56F2044C (00233) #1
A2C 07282 58C2715F (00234)
A2D 07284 5AA00002 (00235)
A2E 07286 5CA00002 (00236)
A2F 07288 5F302841 (00237)
A30 0728A 60300037 (00238)
A31 0728C 62000020 (00239)
A32 0728F 64C2044A (00240) *
A33 07290 66C2044C (00241)
A34 07292 68F203F6 (00242)
A35 07294 6A200031 (00243) *
A36 07296 6C000020 (00244)
A37 07298 6E400032 (00245) *
A38 0729A 70C00066 (00246)
A39 0729C 72500000 (00247)
A3A 0729E 74600000 (00248)
A3B 072A0 768A0002 (00249)
A3C 072A2 7811002A (00250)
A3D 072A4 7A81002A (00251)
A3E 072A6 7C8A0002 (00252)
A3F 072A8 7E210010 (00253)
A40 072AA 80110013 (00254)
A41 072AC 82820006 (00255)
A42 072AE 848A0002 (00256)
A43 072H0 86AA0002 (00257)

(00217) ?
(00218) *
IO=VAR'
IO=1.0/20.0=0.05
STALL APS INPUT

IO=LOG16(10.0)
IO=2**(-22)
IO=2**15
IO=4**65+16**(-6)
IO=C5
IO=C4
IO=C3
IO=C2
IO=PS 4 IO'S=12 IO'S
IO=VAR'
IO=-16**(-6)
IO=C1
IO=C0
FOR I=0,1...3

IO=DC'
IO=VAR
SA=2**(-M LONG)
INPUT DONEI

ENABLE APU
VR(0)
N-1
DUMY
YI(0)
2N-2
YR(N/2)
YI(N/2)
YI(N/2)
4*(N/2-1)-1
YR(K)
YI(K)
YR(N-K)

```

PAGE 8: FCH 24H... "MPIDCM(Y,U,V)" MAKHUIL IDCT CORRECTION

A44 072H7	HAA0002	(00261)	ADD(RW2,2,TF)	VI(N-K)
A45 072H4	RA1141H4	(00262)	SUBT(RW1,4),JUMPP(#2)	
A46 072H6	HCC2044C	(00263) *	LOAD(HW0,SAS101(1),TF)	UU=VAP...
A47 072H7	HFC20794	(00264) *	LOAD(HW0,DHYS(1),TF)	UU=?
A48 072H8	90C20794	(00265)	LOAD(HW0,DHYS(1),TF)	UU=?
A49 072H9	92C2044C	(00266)	LOAD(HW0,SAS101(1),TF)	UU=VAP
A4A 072H7	94C2044A	(00267) *	LOAD(HW0,SAS100(1),TF)	UU=DC
A4B 072C0	96C2044C	(00270) DDCS	LOAD(HW0,SAS101(1),TF)	UU=VAR
A4C 072C2	98200030	(00271) *	CLFAR(P0)	OUTPUT DONE!
A4D 072C4	9A000020	(00272) DDFS0	MDP	
	0000729A	(00273) *	G201SA= 8C	ASSIGN VALUE TO CHAIN AN
		(00275) *		CHNR
072C6		(00277) ?	FND #A-1	
072C6	00000000	(00278)	STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS	
...		(00279) :	DATA 3F'0.0'	
072C6	000000A2	(00280) G201S1	G201S2=HL-G201S	
		(00281)	FND	
072C6		(00282)		

PAGE 9: PCH 24X... "MPIDCM(Y,U,V)" MARKHIL IDCT CORRECTION

AFDTSURG:	00000	(00000)	(00034)
COSZS:	00000	(00010)	
CSPHSMUS:	0210C	(00011)	(00037)
PCS:	00000	(00160)	
DMS:	00000	(00012)	(00212) (00266) (00267)
DNFSA:	00000	(00167)	
DNFSL:	00000	(00245)	
DNFSU:	00000	(00273)	
DRDCTS:	00000	(00013)	
FXPS:	00000	(00114)	
G201S:	0720A	(00016)	(00179) (00185) (00281)
G201SA:	0720A	(00182)	(00276)
G201S1:	0720C	(00177)	(00280)
G201S0:	00017	(00186)	(00248)
G201S7:	00007	(00181)	(00281)
HS:	00001	(00015)	
INDS:	00012	(00241)	
INDLP:	00014	(00087)	(00104)
INDMS:	07166	(00035)	(00061)
INDMSSA:	00000	(00059)	(00067) (00170)
INDMSSZ:	00050	(00060)	(00170) (00171)
IFPS:	00022	(00224)	
IMUS:	00010	(00219)	
MSS:	00000	(00016)	(00189) (00190) (00197) (00193) (00250) (00251)
NDCS:	00000	(00270)	
OFXPS:	00047	(00266)	
OMUS:	00046	(00264)	
SAS100:	00040	(00023)	(00241) (00270)
SAS101:	00040	(00024)	(00219) (00233) (00242) (00264) (00271)
SAS27:	00000	(00020)	(00274)
SAS50:	00000	(00021)	(00243)
SAS04:	00040	(00022)	(00270)
SCMS:	00001	(00179)	(00187)
SINZS:	00000	(00018)	
SIZE:	00000	(00017)	
SMUS:	00025	(00104)	(00042)
START:	07150	(00025)	
SVTS:	00302	(00019)	(00020) (00021) (00022) (00023) (00024)
TARIFS:	07150	(00044)	(00275)
TARIFS2:	07150	(00051)	(00234)
WS:	00002	(00026)	(00276)
WWS:	00004	(00027)	(00277) (00278) (00279) (00280) (00281) (00282) (00283) (00284) (00285) (00286) (00287) (00288) (00289) (00290) (00291) (00292) (00293) (00294) (00295) (00296) (00297) (00298) (00299) (00300)
WWS2:	00005	(00028)	
WWS3:	00006	(00029)	

PAGE 10: FCH 24R... "MPIDCM(Y,U,V)" WAKHUU, IDCT CORRECTION

ZS: 00003 (00030)
ZMS: 00007 (00031)

LINES WITH ERRORS: 0 (MAP VERSION H00101,10) F- 0

```
(00001) * FCR 249... "MPASRT(0)" SORT DCT1(.) COEFFICIENTS
(00002) * ORIGINATED:D:13-DEC-79
(00003) * UPDATED:D:27-MAY-80
(00004) * DEFINE GLOBAL SYMBOLS
(00005) *   UPADU FXV, (1 .I.S. 14) + (12 .I.S. 8) + $IF
(00006) *   OPADD ODF, (3 .I.S. 10) + (12 .I.S. 5) + $IH
(00007) *   UPADD WAFNF, (1 .I.S. 10) + (12 .I.S. 5) + $IA
(00008) * AFDT$UKG=$RFXH
(00009) * APSSMFM=$IFFCO
(00010) * CSPUSNMOS=$21FC
(00011) * DCTS=D'2344'
(00012) * DCTIS=D'512'
(00013) * DCT2S=D'1024'
(00014) * DCTMS=D'2344'
(00015) * JMYG=$74Q
(00016) * MS=3
(00017) * MS=1
(00018) * INCRFS=D'256'
(00019) * IORHS=D'0'
(00020) * ISVT$=$502
(00021) * TSAS00=ISVT$+D'0'
(00022) * TSAS01=ISVT$+D'1'
(00023) * MSS=0
(00024) * OVRSIAP=D'10'
(00025) * SIZE$=D'256'-D'1'
(00026) * SVTS=$0382
(00027) * START=$7300
(00028) * TABLS0=D'3072'
(00029) * TABLS1=TABLS0+INCRFS
(00030) * TABLS2=TABLS1+INCRFS
(00031) * TABLS3=TABLS2+INCRFS
(00032) * TMP2S=D'2560'
(00033) * TMP3S=D'3584'
(00034) * TMP4S=D'3072'
(00035) * WS=7
(00036) * WS=3
(00037) * EXPAND ARRAY FUNCTION DISPATCH TABLE
(00038) * #I=AFTDSUKG+3*2*(249-128)
(00039) * ADDR SORTS(P,1)
(00040) * ADDR G106$(P,1)
(00041) * ADDR CSPUSN05(.1,0)
(00042) * EFFECT
(00043)
```

[illegible]

01F3A 0016		
01F3B 0017		
01F3C 0018		
01F3D 0019	(00061)	DATA 0'24',0'25',0'26',0'27',0'28',0'29',0'30',0'31'
01F3E 001A		
01F3F 001B		
01F40 001C		
01F41 001D		
01F42 001E		
01F43 001F		
01F44 0020	(00062)	DATA 0'32',0'33',0'34',0'35',0'36',0'37',0'38',0'39'
01F45 0021		
01F46 0022		
01F47 0023		
01F48 0024		
01F49 0025		
01F4A 0026		
01F4B 0027		
01F4C 0028		
01F4D 0029	(00063)	DATA 0'40',0'41',0'42',0'43',0'44',0'45',0'46',0'47'
01F4E 002A		
01F4F 002B		
01F50 002C		
01F51 002D		
01F52 002E		
01F53 002F		
01F54 0030	(00064)	DATA 0'48',0'49',0'50',0'51',0'52',0'53',0'54',0'55'
01F55 0031		
01F56 0032		
01F57 0033		
01F58 0034		
01F59 0035		
01F5A 0036		
01F5B 0037		
01F5C 0038		
01F5D 0039	(00065)	DATA 0'56',0'57',0'58',0'59',0'60',0'61',0'62',0'63'
01F5E 003A		
01F5F 003B		
01F60 003C		
01F61 003D		
01F62 003E		
01F63 003F		
01F64 0040	(00066)	DATA 0'64',0'65',0'66',0'67',0'68',0'69',0'70',0'71'
01F65 0041		

01E66 0042		
01E67 0043		
01E68 0044		
01E69 0045		
01E6A 0046		
01E6B 0047		
01E6C 0048		
01E6D 0049		
01E6E 004A		
01E6F 004B		
01E70 004C		
01E71 004D		
01E72 004E		
01E73 004F		
01E74 0050		
01E75 0051		
01E76 0052		
01E77 0053		
01E78 0054		
01E79 0055		
01E7A 0056		
01E7B 0057		
01E7C 0058		
01E7D 0059		
01E7E 005A		
01E7F 005B		
01E80 005C		
01E81 005D		
01E82 005E		
01E83 005F		
01E84 0060		
01E85 0061		
01E86 0062		
01E87 0063		
01E88 0064		
01E89 0065		
01E8A 0066		
01E8B 0067		
01E8C 0068		
01E8D 0069		
01E8E 006A		
01E8F 006B		
01E90 006C		
01E91 006D		
(00067)	DATA D'172',D'173',D'174',D'175',D'176',D'177',D'178',D'179',	
(00068)	DATA D'180',D'181',D'182',D'183',D'184',D'185',D'186',D'187',	
(00069)	DATA D'188',D'189',D'190',D'191',D'192',D'193',D'194',D'195',	
(00070)	DATA D'196',D'197',D'198',D'199',D'100',D'101',D'102',D'103',	
(00071)	DATA D'104',D'105',D'106',D'107',D'108',D'109',D'110',D'111',	

01F92 006F		
01F94 006F		
01F96 0070		
01F98 0071		
01F9A 0072		
01F9C 0073		
01F9E 0074		
01FA0 0075		
01FA2 0076		
01FA4 0077		
01FA6 0078		
01FA8 0079		
01FAA 007A		
01FAC 007B		
01FAE 007C		
01FB0 007D		
01FB2 007E		
01FB4 007F		
01FB6 0080		
01FB8 0081		
01FBA 0082		
01FBC 0083		
01FBE 0084		
01FBF 0085		
01FCA 0086		
01FCB 0087		
01FCC 0088		
01FCE 0089		
01FCF 008A		
01FD0 008B		
01FD2 008C		
01FD4 008D		
01FD6 008E		
01FD8 008F		
01FDA 0090		
01FDC 0091		
01FDE 0092		
01FD7 0093		
01FD9 0094		
01FDA 0095		
01FDB 0096		
01FDD 0097		
01FDE 0098		
01FDF 0099		
(00072)	DATA D'112',D'113',D'114',D'115',D'116',D'117',D'118',D'119'	
(00073)	DATA D'120',D'121',D'122',D'123',D'124',D'125',D'126',D'127'	
(00074)	DATA D'128',D'129',D'130',D'131',D'132',D'133',D'134',D'135'	
(00075)	DATA D'136',D'137',D'138',D'139',D'140',D'141',D'142',D'143'	
(00076)	DATA D'144',D'145',D'146',D'147',D'148',D'149',D'150',D'151'	
(00077)	DATA D'152',D'153',D'154',D'155',D'156',D'157',D'158',D'159'	

01E94 009A	
01E9F 009B	
01EC0 009C	
01EC1 009D	
01EC2 009E	
01EC3 009F	
01EC4 00A0	DATA 0'160',0'161',0'162',0'163',0'164',0'165',0'166',0'167'
01EC5 00A1	
01EC6 00A2	
01EC7 00A3	
01EC8 00A4	
01EC9 00A5	
01ECA 00A6	
01ECB 00A7	
01ECC 00A8	DATA 0'168',0'169',0'170',0'171',0'172',0'173',0'174',0'175'
01ECD 00A9	
01ECF 00AA	
01ED0 00AB	
01ED1 00AC	
01ED2 00AD	
01ED3 00AE	
01ED4 00AF	
01ED5 00B0	DATA 0'176',0'177',0'178',0'179',0'180',0'181',0'182',0'183'
01ED6 00B1	
01ED7 00B2	
01ED8 00B3	
01ED9 00B4	
01EDA 00B5	
01EDB 00B6	
01EDC 00B7	
01EDD 00B8	DATA 0'184',0'185',0'186',0'187',0'188',0'189',0'190',0'191'
01EDE 00B9	
01EDF 00BA	
01EE0 00BB	
01EE1 00BC	
01EE2 00BD	
01EE3 00BE	
01EE4 00BF	
01EE5 00C0	DATA 0'192',0'193',0'194',0'195',0'196',0'197',0'198',0'199'
01EE6 00C1	
01EE7 00C2	
01EE8 00C3	
01EE9 00C4	
01EEA 00C5	

01F0A 0006	(000043)	DATA 0'200',0'201',0'202',0'203',0'204',0'205',0'206',0'207'
01F0B 0007		
01F0C 0008		
01F0D 0009		
01F0E 000A		
01F0F 000B		
01F10 000C		
01F11 000D		
01F12 000E		
01F13 000F		
01F14 0010		
01F15 0011		
01F16 0012		
01F17 0013		
01F18 0014		
01F19 0015		
01F1A 0016		
01F1B 0017		
01F1C 0018		
01F1D 0019		
01F1E 001A		
01F1F 001B		
01F20 001C		
01F21 001D		
01F22 001E		
01F23 001F		
01F24 0020		
01F25 0021		
01F26 0022		
01F27 0023		
01F28 0024		
01F29 0025		
01F2A 0026		
01F2B 0027		
01F2C 0028		
01F2D 0029		
01F2E 002A		
01F2F 002B		
01F30 002C		
01F31 002D		
01F32 002E		
01F33 002F		
01F34 0030		
01F35 0031		
01F36 0032		
01F37 0033		
01F38 0034		
01F39 0035		
01F3A 0036		
01F3B 0037		
01F3C 0038		
01F3D 0039		
01F3E 003A		
01F3F 003B		
01F40 003C		
01F41 003D		
01F42 003E		
01F43 003F		
01F44 0040		
01F45 0041		
01F46 0042		
01F47 0043		
01F48 0044		
01F49 0045		
01F4A 0046		
01F4B 0047		
01F4C 0048		
01F4D 0049		
01F4E 004A		
01F4F 004B		
01F50 004C		
01F51 004D		
01F52 004E		
01F53 004F		
01F54 0050		
01F55 0051		
01F56 0052		
01F57 0053		
01F58 0054		
01F59 0055		
01F5A 0056		
01F5B 0057		
01F5C 0058		
01F5D 0059		
01F5E 005A		
01F5F 005B		
01F60 005C		
01F61 005D		
01F62 005E		
01F63 005F		
01F64 0060		
01F65 0061		
01F66 0062		
01F67 0063		
01F68 0064		
01F69 0065		
01F6A 0066		
01F6B 0067		
01F6C 0068		
01F6D 0069		
01F6E 006A		
01F6F 006B		
01F70 006C		
01F71 006D		
01F72 006E		
01F73 006F		
01F74 0070		
01F75 0071		

PAGE 8: FCH 249... "MPASPT(11)" SORT DCT1(1) COEFFICIENTS

01F16 0042			
01F17 00F3			
01F18 00F4			
01F19 00F5			
01F1A 00F6			
01F1B 00F7			
01F1C 00F8	(000894)	DATA 0'248',0'249',0'250',0'251',0'252',0'253',0'254',0'255'	
01F1D 00F9			
01F1E 00FA			
01F1F 00FB			
01F20 00FC			
01F21 00FD			
01F22 00FE			
01F23 00FF			
00007300	(000900)	HUS 1	
	(000901)	#1=START	
	(000902)	DATA 1F'0.713'	
	(000903)	DATA 1F'7.5'	
	(000904)	DATA 1F'7.5'	
07300 0477A2C0	(000905)	DATA 1F'-0.48802602'	
07302 0A934F41	(000906)	DATA 1F'1.32192810'	
07304 17414FC1	(000907)	DATA 1F'2.90689006'	
	(000908)	F.PCT	

PAGE 4: FCH 249... "MPASHT(U)" SORT OCT() COEFFICIENTS

[illegible]

A24 07350 9016000C (00184)	JUMPC(JNCS1,F=1)	FOR I=0,1,2...LTH-1
A25 07352 1045003A (00184)	JUMPC(CNFSOUT,G1),SET	GET LEVEL COUNTS
A26 07354 20490000 (00185) LVL51	SET(CA1)NOP	INFORM APS OF LVL1
A27 07356 20530000 (00186)	SET(UN)NOP	FIXED POINT ADD
A28 07358 00004720 (00187)	NOPXADD(A1,A7)	LVL1 CNT+1
A29 0735A 08F000F4 (00188)	MOV(10A,A0)NOP(10A,A4)	A0=D=DC1(1+1);A4=D
A2A 0735C 08F00091 (00189)	MOV(10A,00)NOP(R,A1)	00=1;LVL1=LVL1+1
A2B 0735E 20330000 (00190)	CLEAR(UN)NOP	RESUME FLOATING POINT!
A2C 07360 49004080 (00191)	SUB(CA0,A1)NOP(RA4,A5)	D-THRESH1;D-THRESH2
A2D 07362 91090020 (00192) #1	JUMPS(R1,AF1)	
A2E 07364 9016000C (00193)	JUMPC(JNCS1,F=1)	FOR I=0,1,2...LTH-1
A2F 07366 1045003A (00194)	JUMPC(CNFSOUT,G1),SET	GET LEVEL COUNTS
A30 07368 20340000 (00195) LVL50	SET(CA0)NOP	INFORM APS OF LVL0
A31 0736A 20530000 (00196)	SET(UN)NOP	FIXED POINT ADD
A32 0736C 00004700 (00197)	NOPXADD(A0,A7)	LVL0 CNT+1
A33 0736E 08F000F4 (00198)	MOV(10A,A0)NOP(10A,A4)	A0=D=DC1(1+1);A4=D
A34 07370 08F00090 (00199)	MOV(10A,00)NOP(R,A0)	00=1;LVL0=LVL0+1
A35 07372 20330000 (00200)	CLEAR(UN)NOP	RESUME FLOATING POINT!
A36 07374 49004080 (00201)	SUB(CA0,A1)NOP(RA4,A5)	D-THRESH1;D-THRESH2
A37 07376 91080037 (00202) #1	JUMPS(R1,AF0)	
A38 07378 9016000C (00203)	JUMPC(JNCS1,F=1)	FOR I=0,1,2...LTH-1
A39 0737A 20450000 (00204)	SET(G1)NOP	GET LEVEL COUNTS
A3A 0737C 00000200 (00205)	NOPXAR(A0)	R=LVL0 CNT
A3B 0737E 0000024C (00206)	NOPXMOV(00),P(A1)	R=LVL1 CNT
A3C 07380 0000025C (00207)	NOPXMOV(00),R(A2)	R=LVL2 CNT
A3D 07382 0000027C (00208)	NOPXMOV(00),R(A3)	R=LVL3 CNT
A3E 07384 0000089C (00209)	NOPXMOV(R,00)	00=LVL3 CNT(0 UPTO LTH)
A3F 07386 081C081C (00210)	MOV(ZERO,00)	00=0 FOR INSTRS4+1...
A40 07388 081C081C (00211)	MOV(ZERO,00)	THRU INSTRS7+1
A41 0738A 00000000 (00212) #1	NOP	WAIT FOR...
A42 0738C 901C0041 (00213)	JUMPC(R1,F0)	"LVLFLS" TO SETTLE
A43 0738E 20370000 (00214)	CLEAR(W1)NOP	RELEASE APS INPUT
A44 07390 20500000 (00215)	SET(R0)NOP	RELEASE APS OUTPUT
A45 07392 08F00000 (00216)	MOV(10A,00)NOP	SCATTER-WRITE INTO INS0
A46 07394 08F00000 (00217)	MOV(10A,00)NOP	SCATTER-WRITE INTO INS1
A47 07396 08F00000 (00218)	MOV(10A,00)NOP	SCATTER-WRITE INTO INS2
A48 07398 08F00000 (00219)	MOV(10A,00)NOP	SCATTER-WRITE INTO INS3
A49 0739A 00000000 (00220) #2	NOP	WAIT FOR...
A4A 0739C 901C0049 (00221)	JUMPC(R2,F0)	"WRITES" TO SETTLE
A4B 0739E 20370000 (00222)	CLEAR(W1)NOP	RELEASE APS INPUT
A4C 073A0 20500000 (00223)	SET(R0)NOP	RELEASE APS OUTPUT
A4D 073A2 08F00000 (00224) DTHRESH1	MOV(10A,00)NOP	00=L0RDR(1)
A4E 073A4 08F00000 (00225)	MOV(10A,00)NOP	00=L0RDR(1+1)
A4F 073A6 08F00000 (00226)	MOV(10A,00)NOP	00=L0RDR(1+2)

[illegible]

A79 073FA 413C413C (00231)	MOV(D0),ADD(A1,A1)	2J->S6+1,2J';2K->S7+1,2K
A7A 073FC 08FC0000 (00232)	MOV(10A,00)\NOP	INSTRS4 INTO INDS4
A7B 073FE 000008FC (00233)	NOP\MOV(10A,00)	INSTRS5 INTO INDS5
A7C 07400 08FC0000 (00234)	MOV(10A,00)\NOP	DCT2(1)=TMP2(J*)
A7D 07402 08FC0000 (00235)	MOV(10A,00)\NOP	TMP4(1)=DCT1(J*)
A7E 07404 08FC0000 (00236)	MOV(10A,00)\NOP	TMP3(1)=DCTM(J*)
A7F 07406 000008FC (00237)	NOP\MOV(10A,00)	DCT2(1+1)=TMP2(K*)
A80 07408 000008FC (00238)	NOP\MOV(10A,00)	TMP4(1+1)=DCT1(K*)
A81 0740A 000008FC (00239)	NOP\MOV(10A,00)	TMP3(1+1)=DCTM(K*)
A82 0740C 08FC0000 (00240)	MOV(10A,A2)\NOP	A2=J'
A83 0740E 000008FC (00241)	NOP\MOV(10A,A2)	A2=K'
A84 07410 42A(425C (00242)	MOV(D0),ADD(A2,A2)	2J'->S4+1,2J';2K'->S5+1
A85 07412 08FC0000 (00243)	MOV(10A,00)\NOP	2K'
A86 07414 000008FC (00244)	NOP\MOV(10A,00)	INSTRS6 INTO INDS6
A87 07416 08FC0000 (00245)	MOV(10A,00)\NOP	INSTRS7 INTO INDS7
A88 07418 08FC0000 (00246)	MOV(10A,00)\NOP	DCT2(1+2)=TMP2(J*)
A89 0741A 08FC0000 (00247)	MOV(10A,00)\NOP	TMP4(1+2)=DCT1(J*)
A8A 0741C 000008FC (00248)	MOV(10A,00)\NOP	TMP3(1+2)=DCTM(J*)
A8B 0741E 000008FC (00249)	NOP\MOV(10A,00)	DCT2(1+3)=TMP2(K*)
A8C 07420 000008FC (00250)	NOP\MOV(10A,00)	TMP4(1+3)=DCT1(K*)
A8D 07422 10000073 (00251)	JUMP(SHIFT5)	TMP3(1+3)=DCTM(K*)
A8E 07424 20320000 (00252)	CLFAR(RA)\NOP	"JUMP(...)" SETTLES 1/0
A8F 07426 00000000 (00253)	NOP	API DONE!
07428 00000090 (00254)	SORTSSZ=8A-SORTSSA	
	END SORTSSZ	
	FJCT	


```

A1F 0746F 3F1021F1 (00304) TSTSL0 SUHL(HR1,1),JUMP(NSTALS1)
A20 07470 41001F74 (00305) ADD(HR0,HS,TF),JUMP(TSTSL0)
A21 07472 42300037 (00306) SET(M1)
A22 07474 44500103 (00307) LOAD(HR1,SIZE+4)
      (00308) *
A23 07476 47400000 (00309) LOAD(HR0,101,TF)
A24 07478 48500000 (00310) LOAD(HR2,MSS)
A25 0747A 490A0000 (00311) ADD(HR0,MSS,TF)
A26 0747C 4F0A0001 (00312) INDSJ ADD(HR0,HS,TF)
A27 0747E 4F0A0001 (00313) ADD(HR0,HS,TF)
A28 07480 50F4111C (00314) LOAD(HR2,INSTRS4(2),TF)
A29 07482 52F3111F (00315) LOAD(HR2,INSTRS(2),TF)
A2A 07484 54600000 (00316) LOAD(HR2,MSS)
A2B 07486 562F0000 (00317) INDS4 ADD(HR2,MSS,NA,C)
A2C 07488 58500000 (00318) LOAD(HR2,MSS)
A2D 0748A 5A2F0000 (00319) ADD(HR2,MSS,NA,C)
A2E 0748C 510A0001 (00320) ADD(HR0,HS,TF)
A2F 0748E 5F0A0001 (00321) ADD(HR0,HS,TF)
A30 07490 60F41120 (00322) LOAD(HR2,INSTRS6(2),TF)
A31 07492 62F41122 (00323) LOAD(HR2,INSTRS(2),TF)
A32 07494 64600000 (00324) LOAD(HR2,MSS)
A33 07496 662F0000 (00325) INDS4 ADD(HR2,MSS,NA,C)
A34 07498 68500000 (00326) LOAD(HR2,MSS)
A35 0749A 6A2F0000 (00327) INDS5 ADD(HR2,MSS,NA,C)
A36 0749C 6C1026H4 (00328) SUHL(HR1,4),JUMP(INDSJ)
      (00329) *
A37 0749F 6F200031 (00330) DNE51 CLEAR(M1)
A38 074A0 70000020 (00331) NOP
A39 074A2 7220A240 (00332) G10AS1 JSM(G10AS0,P2)
A3A 074A4 7476A000 (00333) 10PSP1 LOAD(HR3,IMP2S(1))
A3B 074A6 76H90020 (00334) ADDH(HR3,HR2,TF)
A3C 074A8 78740020 (00335) LOAD(HR3,DCTMS(2))
A3D 074AA 7AH90020 (00336) ADDH(HR3,HR2,TF)
A3E 074AC 7C760020 (00337) LOAD(HR3,DCTIS(1))
A3F 074AE 7EH90020 (00338) ADDH(HR3,HR2,TF)
A40 074B0 802F0000 (00339) ADD(HR2,MSS,NA,C)
A41 074B2 82003A60 (00340) JUMP(TOPSP1)
      (00341) *
A42 074B4 84307040 (00342) G10AS0 JSM(G10AS3,P3)
A43 074B6 864600FF (00343) LOAD(HR0,TARUS0-1(3))
A44 074B8 885600FF (00344) LOAD(HR1,TARUS1-1(3))
A45 074BA 8A6600FF (00345) LOAD(HR2,TARUS2-1(3))
A46 074BC 8C7600FF (00346) LOAD(HR3,TARUS3-1(3))
A47 074BE 8E0055F5 (00347) FIRSTST JUMPS(SETSCM1,G1)

```

```

PICKUP TILL NO MORE
IO=TAH0(1)
STALL APS INPUT
INDR(.) SIZE-1+4

TORR(.) A,IO=TORR=J
DUMMY OP FOR EXEC
IO=TORR(1)=K
IO=J=TORR(1+2)
IO=K=INDR(1+3)
OVERWRITE INDS4
OVERWRITE INDS5
RESET HR2 TO 0!
MSS=2J,PC->PC1
RESET HR2 TO 0!
MSS=2J,PC->PC1
IO=J
IO=K
OVERWRITE INDS6
OVERWRITE INDS7
RESET HR2 TO 0!
MSS=2J,PC->PC1
RESET HR2 TO 0!
MSS=2J,PC->PC1
FOR I=0,1,2...LTH+1

INPUT DONE!

SET OUTPUT PC
TMP2(.) RA
IO=TMP2(J+ OR K*)
DCTM(.) RA
IO=DCTM(J+ OR K*)
DCT1(.) RA
IO=DCT1(J+ OR K*)
PC->PC0
RESET PC AND SETTLE

SET OUTPUT PC(PC3)
TAH0(.) RA-1
TAH1(.) RA-1
TAH2(.) RA-1
TAH3(.) RA-1
G1=1,SET LEVFI, COUNTS

```

```

A48 074C0 Q00040D8 (00348)      JUMPS(INSTSD,AF0)
A49 074C2 Q2004FF9 (00149)      JUMPS(INSTSD,AF1)
A50 074C4 Q40051FA (00150)      JUMPS(INSTSD,AF2)
A51 074C6 Q60053FA (00151)      JUMPS(INSTSD,AF3)
A52 074C8 Q80055FA (00152)      JUMPS(INSTSD,AF4)
A53 074CA Q00047A0 (00153)      INSRTSD CLEAR(AF0)
A54 074CB Q20002H (00154)      ADDL(RW0,HS,TF),JUMP(FLAGSTST)
A55 074CD Q4001479 (00155)      CLEAR(AF1)
A56 074CE Q600029 (00156)      INSRTSD ADDL(RW1,HS,TF),JUMP(FLAGSTST)
A57 074CF Q8001479 (00157)      CLEAR(AF2)
A58 074D0 A1114779 (00158)      INSRTSD ADDL(RW2,HS,TF),JUMP(FLAGSTST)
A59 074D2 A220002A (00159)      CLEAR(AF3)
A60 074D4 A5214779 (00160)      INSRTSD ADDL(RW3,HS,TF),JUMP(FLAGSTST)
A61 074D6 A620002H (00161)      SETSCNT LOAD(RW1,7)
A62 074D8 A9314779 (00162)      ADDL(RW0,HS,TF)
A63 074DA A0000007 (00163)      ADDL(RW1,HS,TF)
A64 074DC AC441F13 (00164)      SUBL(RW1,1),JUMPP(41)
A65 074DE AF0A0002 (00165)      CLEAR(R0)
A66 074DF K01157H1 (00166)      LOAD(RW1,3)
A67 074E0 H2200030 (00167)      INSRTSD LOAD(RW0,APSSMEM(1),TF)
A68 074E2 H4500003 (00168)      SHRL(RW1,1),JUMPP(42)
A69 074E4 H6C3FFC0 (00169)      CLEAR(R0)
A70 074E6 H8115RH1 (00170)      NOP
A71 074E8 H0000020 (00171)      LOAD(RW0,INSTRS(3),TF)
A72 074EA H2400000 (00172)      INSRTSD LOAD(RW1,SIZES+OVRSLAP-1)
A73 074EC C0500108 (00173)      ADDL(RW0,HS,TF)
A74 074EE C30A0001 (00174)      SHRL(RW1,1),JUMPP(RDSOUT)
A75 074F0 C41161H1 (00175)      ?
A76 074F2 C64603FF (00176)      *
A77 074F4 C85600FF (00177)      LOAD(RW0,INSTRS-2(3))
A78 074F6 CA7600FF (00178)      LOAD(RW1,TMP3S-2(3))
A79 074F8 CC000020 (00179)      LOAD(RW1,TMP4S-2(3))
A80 074FA CF641F21 (00180)      INSRTSD NOP
A81 074FC D1641F23 (00181)      LOAD(RW2,INSTRS6+1(2),TF)
A82 074FE D2E3FFC0 (00182)      LOAD(RW2,INSTRS7+1(2),TF)
A83 07500 D4AF0000 (00183)      ADD(RW2,HS,TF,C)
A84 07502 D641F110 (00184)      LOAD(RW2,INSTRS4+1(2),TF)
A85 07504 D841F11F (00185)      LOAD(RW2,INSTRS5+1(2),TF)
A86 07506 DAF3FFC0 (00186)      LOAD(RW2,APSSMEM(1),TF)
A87 07508 DCF00000 (00187)      ADD(RW2,HS,TF,C)
A88 0750A E0206670 (00188)      JUMP(INSTR,R0),CLEAR
A89 0750C E0300032 (00189)      SET(RA)
A90 0750E E0300032 (00190)      G10AS3
A91 07510 F2007A8A (00191)      TOPSP3
A92 07512 F2007A8A (00192)      TOPSP3

AF0=1 THEN LEVEL 0
AF1=1 THEN LEVEL 1
AF2=1 THEN LEVEL 2
AF3=1 THEN LEVEL 3
RE-TFST ALL FLAGS
RELEASE APU
NO=TABLE0(11),11=11+1
RELEASE APU
NO=TABLE1(JJ),JJ=JJ+1
RELEASE APU
NO=TABLE2(KK),KK=KK+1
RELEASE APU
NO=TABLE3(LL),LL=LL+1
8 "INSTR" WORDS...
TO BE SET OR CLEARED
NO=INSTRS#+1
FOR I=0,1,2...7
STALL APS OUTPUT
3+1=4 SCATTER WRITES
INSTRS# OVERWRITE
FOR I=0,1...3
STALL APS OUTPUT
RELEASED BY APU
IORDR(.) RA,00=IORDR(0)
IORDR(.) SIZE-2+OVERLAP
00=IORDR(1)
FOR I=1,2...1,TH-1;+10 0*
S
DCT2(.) RA-2
TMP3(.) RA-2
TMP4(.) RA-2
LET "JUMP(...)" SETTLE
SET MSS IN INSTRS6
SET MSS IN INSTRS7
OVERWRITE INDS4
OVERWRITE INDS5;PC3->PC
SET MSS IN INSTRS4
SET MSS IN INSTRS5
OVERWRITE INDS6
OVERWRITE INDS7;PC3->PC
RESFT & STALL
ENABLE APU
WAIT FOR "SKIP" FLAG

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PAGE 16: FCW 249... "MPASRT(11)" SORT DCT1(.) COEFFICIENTS

A72 07514	FAAA0002	(00392)	ADD(HW0,MS,TF)	00=DCT2(J*)
A73 07516	FAAA0002	(00394)	ADD(HW1,MS,TF)	00=TMP3(J*)=DCTT(J*)
A74 07518	FAAA0002	(00394)	ADD(HW3,MS,TF)	00=TMP4(J*)=DCTIT(J*)
A75 0751A	FAAA0002	(00394)	ADD(HW0,MS,TF)	00=DCT2(K*)
A76 0751C	FAAA0002	(00396)	ADD(HW1,MS,TF)	00=TMP3(K*)=DCTT(K*)
A77 0751E	FAAA0002	(00397)	ADD(HW3,MS,TF)	00=TMP4(K*)=DCTIT(K*)
A78 07520	F02F0000	(00398)	CHNGSPC ADD(HW2,MSS,C)	NO I0! CHANGE PC
A79 07522	F200J160	(00399)	JUMP(TOPSP3)	RESFT PC AND SETTLE
A7A 07524	F4F20794	(00400)	DMYSFTL LOAD(HW2,DMYS(1),TF)	00=?
A7B 07526	FAAA0000	(00401)	ADD(HW2,MSS,TF)	00=?
A7C 07528	FAAA0000	(00402)	ADD(HW2,MSS,TF)	00=?
A7D 0752A	FAAA0000	(00403)	ADD(HW2,MSS,TF)	00=?
A7E 0752C	FAAA0000	(00404)	ADD(HW2,MSS,TF)	00=? THEN CHANGE PC
A7F 0752E	FAA17878	(00405)	ADD(HW2,MSS,TF),JUMP(CHNGSPC)	ASSIGN VALUE TO CHAIN AN
		(00406)		CHOR
	00007476	(00407)	G10KSAE BC	
		(00408)		
07530		(00409)	FND #A-1	
		(00410)	STORAGE BLUCK FOR CONSTRUCTED INSTRUCTIONS	
07530	00000000	(00411)	G10KSI DATA IF'0.0'	
	00000102	(00412)	G10KSIZE=81-G10Ks	
07537		(00413)	FND	

APDTSORG:	00000	(00000)	(00039)
APSSMEM:	1PFC0	(00009)	(00367)
CHNGSPC:	00078	(00398)	(00405)
CWTSNUT:	0003A	(00134)	(00144)
CSPUSMUS:	021FC	(00010)	(00042)
DCTS:	0002R	(00011)	(00337)
DCT16:	00200	(00012)	(00377)
DCT26:	00400	(00013)	(00377)
DCTMS:	0002R	(00014)	(00335)
DMS1:	0079A	(00015)	(00400)
DMSF1L:	0007A	(00391)	(00400)
DMSA:	0000F	(00225)	(00255)
DMS1:	00037	(00330)	(00354)
FLGSTST:	00047	(00347)	(00352)
G106S:	07430	(00041)	(00266)
G106S1:	00039	(00273)	(00332)
G106S2:	00070	(00342)	(00390)
G106S3:	07476	(00269)	(00407)
G106S4:	07530	(00264)	(00411)
G106S5:	00042	(00332)	(00342)
G106S6:	00102	(00266)	(00412)
HS:	00001	(00017)	(00282)
		(00354)	(00356)
		(00293)	(00297)
		(00301)	(00305)
		(00312)	(00313)
		(00320)	(00321)
		(00358)	(00360)
		(00373)	
IIMCS1:	0000H	(00281)	(00283)
INS0:	0001A	(00050)	(00303)
INS1:	0001A	(00051)	(00299)
INS2:	00016	(00052)	(00295)
INS3:	00012	(00053)	(00291)
INC1:	0000C	(00119)	(00133)
INCR1:	00100	(0001H)	(00029)
INDS4:	00033	(00054)	(00325)
INDS5:	00035	(00055)	(00327)
INDS6:	0002H	(00056)	(00317)
INDS7:	00020	(00057)	(00319)
INDS1:	00066	(00380)	(00389)
INDS1:	00026	(00312)	(00328)
INSMTS0:	00040	(00348)	(00353)
INSRTS1:	0004F	(00349)	(00355)
INSRTS2:	00051	(00350)	(00357)
INSRTS3:	00053	(00351)	(00359)
INSTPS0:	01F14	(00050)	(00286)
INSTS1:	01F16	(00051)	
INSTS2:	01F1H	(00052)	
INSTS3:	01F1A	(00054)	

INSTR4:	011C (00054) (00314) (00345)
INSTR5:	011P (00055) (00315) (00346)
INSTR6:	0120 (00056) (00322) (00341)
INSTR7:	0122 (00057) (00323) (00342)
INTR4:	0124 (00058) (00280)
INTR5:	0000 (00019) (00371)
ISAS00:	00502 (00021) (00277)
ISAS01:	00503 (00022) (00277) (00022)
ISVTS:	00502 (00020) (00021) (00022)
LOS00:	00010 (00300) (00302)
L1SS0:	00019 (00296) (00298)
L2SS0:	00015 (00292) (00294)
L3SS0:	00011 (00290)
LVI.S0:	00030 (00120) (00155)
LVI.S1:	00026 (00121) (00145)
LVI.S2:	00010 (00124) (00135)
LVI.S3:	00012 (00125)
MSS:	00000 (00023) (00291) (00295) (00299) (00303) (00310) (00311) (00316) (00317) (00318)
	(00319) (00324) (00325) (00326) (00327) (00339) (00344) (00348) (00398) (00401)
	(00402) (00403) (00404) (00405)
ORDRS1:	00040 (00184) (00188)
IVRSIAP:	0000A (00024) (00372)
RDSOUT:	00061 (00373) (00374)
SCI.RS:	00001 (00266) (00274)
SFTSCNT:	00055 (00347) (00361)
SHUFIS:	00073 (00225) (00253)
SHUFISV:	00054 (00196)
SIZES:	0008F (00025) (00279) (00307) (00372)
SORTS:	07308 (00040) (00105)
SORTSA:	00000 (00103) (00107)
SORTSS7:	00090 (00104) (00257) (00258)
STALS:	00074 (00226) (00226)
STALS1:	00021 (00304) (00306)
START:	07300 (00027) (00091)
SVTS:	00342 (00026)
T1S:	07300 (00095) (00274)
T2S:	07302 (00096) (00275)
T3S:	07304 (00097) (00276)
TARLS0:	00000 (00029) (00029)
TARLS1:	00000 (00029) (00030) (00302) (00343)
TARLS7:	00000 (00030) (00031) (00294) (00344)
TARLS3:	00000 (00031) (00290) (00346)
TMP2S:	00000 (00032) (00278) (00333)
TMP3S:	00000 (00033) (00378)

PAGE 14: FCW 240... "MPASRT(U)" SORT DCT1(.) COEFFICIENTS

TMPS: 0000 (00034) (00379)
TOPSP1: 0003A (00333) (00340)
TOPSP3: 00071 (00391) (00399)
TSTSL0: 0001F (00304) (00305)
TSTSL1: 0001A (00300) (00301)
TSTSL2: 00017 (00296) (00297)
TSTSL3: 00013 (00292) (00293)
WS: 00002 (00015) (00281) (00287) (00363) (00392) (00394) (00395) (00396) (00397)
ZS: 00003 (00036)

LINES WITH ERRORS: 0 (MAP VERSION 000101.10) 1- 0

QUANTIZE DCT PARAMETERS
 ORIGINATED:25-JUL-79
 UPDATED:29-MAY-80

FCR 250... "MPESTO(Y,U,V,W)"

```

(00001) * FCR 250... "MPESTO(Y,U,V,W)"
(00002) *
(00003) *
(00004) * DEFINE GLOBAL SYMBOLS
(00005) *
(00006) *
(00007) *
(00008) *
(00009) *
(00010) *
(00011) *
(00012) *
(00013) *
(00014) *
(00015) *
(00016) *
(00017) *
(00018) *
(00019) *
(00020) *
(00021) *
(00022) *
(00023) *
(00024) *
(00025) *
(00026) *
(00027) *
(00028) *
(00029) *
(00030) *
(00031) *
(00032) *
(00033) *
(00034) *
    
```

```

OPADD EXP, (1 .LS. 14) + (12 .LS. R) + $IF
OPADD EXP, (3 .LS. 10) + (12 .LS. 5) + $IF
OPADD WAFMT, (1 .LS. 10) + (12 .LS. 5) + $IA
OPADD FO, (3 .LS. 10) + (12 .LS. 5) + $IC
AFDTSORG=$RPH
APSSMEM=$IFFCO
CSPUSNOS=$2IFC
OMYS=$0794
MS=3
MS=1
MS=0
MS=0
TSVTS=$502
ISAS001=ISVTS+D'1'
SVTS=$042
SAS11=SVTS+2*D'11'
SAS38=SVTS+2*D'18'
SAS39=SVTS+2*D'39'
START=$7600
TMP3S=D'3584'
TMP4S=D'3072'
MS=2
MS=3
    
```

EXPAND ARRAY FUNCTION DISPATCH TABLE

```

$1=AFDTSORG+3*2*(250-128)
ADDR QUANS(R7,1)
ADDR G104S(R7,1)
ADDR CSPUSNOS(1,0)
EJECT
    
```

```

(00035) *
(00036) *
(00037) *
(00038) *
(00039) *
(00040) *
(00041) INSTRSO DATA INSURP1512'+X'150',X'0000'
(00042) *
(00043) NLS0 DATA 1F'0.0'
(00044) NLS1 DATA 1F'1.0'
(00045) NLS2 DATA 1F'2.0'
(00046) NLS3 DATA 1F'4.0'
(00047) NLS4 DATA 1F'8.0'
(00048) NLS5 DATA 1F'16.0'
(00049) * 0.1 1FVFL
(00050) DTHPS01 DATA 16F'1.0E10'
...
(00051) * 2 1FVFL
(00052) DTHPS2 DATA 1F'1.1260',15F'1.0E10'
...
(00053) * 3 1FVFL
(00054) DTHPS3 DATA 1F'0.5332',1F'1.2527',1F'2.3796',13F'1.0E10'
(00055) * 4 1FVFL
(00056) DTHPS4 DATA 1F'0.2644',1F'0.5667',1F'0.9198',1F'1.3444'
(00057) DATA 1F'1.8776',1F'2.5971',1F'3.724',9F'1.0E10'
(00058) * 5 1FVFL
(00059) DTHPS5 DATA 1F'0.1322',1F'0.2732',1F'0.4243',1F'0.5870'
(00060) DATA 1F'0.7632',1F'0.9555',1F'1.1669',1F'1.4019'

```

PAGE 3: FCH 250... "MPFSTO(V,U,V,M))" QUANTIZE DCT PARAMETERS

0101C 7A40D2C0		
0101F 0A55CFC1		
01020 0A371741		
01022 00549541 (00061)	DATA 1F'1.6663',1F'1.9687',1F'2.3217',1F'2.7463'	
01024 0F8E45C1		
01026 12920741		
01028 15F86C41		
0102A 1A3C6A41 (00062)	DATA 1F'3.2795',1F'3.9990',1F'5.1259',1.0F10	
0102C 1F0E3C1		
0102E 2001D7C1		
01030 12A05F49 (00063)	RUS 1	
00007600 (00064)	81=START	
(00065)	EVEN	
(00066)	FJCT	

START ADDRESS	MODULE SIZE	START ADDRESS	MODULE SIZE
07600 00	(000710)	07600 00	(000710)
07601 00	(000711)	07601 00	(000711)
	(000712)		(000712)
	(000713)		(000713)
	(000714)		(000714)
A00 07602 00	(000715)	07602 00	(000715)
A01 07604 0A200000	(000716)	07604 0A200000	(000716)
A02 07606 16A116A1	(000717)	07606 16A116A1	(000717)
A03 07608 08A20A92	(000718)	07608 08A20A92	(000718)
A04 07610 08A20A92	(000719)	07610 08A20A92	(000719)
A05 07612 08A20A92	(000720)	07612 08A20A92	(000720)
A06 07614 40200000	(000721)	07614 40200000	(000721)
A07 07616 08A20A92	(000722)	07616 08A20A92	(000722)
A08 07618 08A20A92	(000723)	07618 08A20A92	(000723)
A09 07620 08A20A92	(000724)	07620 08A20A92	(000724)
A10 07622 08A20A92	(000725)	07622 08A20A92	(000725)
A11 07624 16A116A1	(000726)	07624 16A116A1	(000726)
A12 07626 08A20A92	(000727)	07626 08A20A92	(000727)
A13 07628 08A20A92	(000728)	07628 08A20A92	(000728)
A14 07630 08A20A92	(000729)	07630 08A20A92	(000729)
A15 07632 08A20A92	(000730)	07632 08A20A92	(000730)
A16 07634 08A20A92	(000731)	07634 08A20A92	(000731)
A17 07636 08A20A92	(000732)	07636 08A20A92	(000732)
A18 07638 08A20A92	(000733)	07638 08A20A92	(000733)
A19 07640 08A20A92	(000734)	07640 08A20A92	(000734)
A20 07642 08A20A92	(000735)	07642 08A20A92	(000735)
A21 07644 08A20A92	(000736)	07644 08A20A92	(000736)
A22 07646 08A20A92	(000737)	07646 08A20A92	(000737)
A23 07648 08A20A92	(000738)	07648 08A20A92	(000738)
A24 07650 08A20A92	(000739)	07650 08A20A92	(000739)
A25 07652 08A20A92	(000740)	07652 08A20A92	(000740)
A26 07654 08A20A92	(000741)	07654 08A20A92	(000741)
A27 07656 08A20A92	(000742)	07656 08A20A92	(000742)
A28 07658 08A20A92	(000743)	07658 08A20A92	(000743)
A29 07660 08A20A92	(000744)	07660 08A20A92	(000744)
A30 07662 08A20A92	(000745)	07662 08A20A92	(000745)
A31 07664 08A20A92	(000746)	07664 08A20A92	(000746)
A32 07666 08A20A92	(000747)	07666 08A20A92	(000747)
A33 07668 08A20A92	(000748)	07668 08A20A92	(000748)
A34 07670 08A20A92	(000749)	07670 08A20A92	(000749)
A35 07672 08A20A92	(000750)	07672 08A20A92	(000750)
A36 07674 08A20A92	(000751)	07674 08A20A92	(000751)
A37 07676 08A20A92	(000752)	07676 08A20A92	(000752)
A38 07678 08A20A92	(000753)	07678 08A20A92	(000753)
A39 07680 08A20A92	(000754)	07680 08A20A92	(000754)
A40 07682 08A20A92	(000755)	07682 08A20A92	(000755)
A41 07684 08A20A92	(000756)	07684 08A20A92	(000756)
A42 07686 08A20A92	(000757)	07686 08A20A92	(000757)
A43 07688 08A20A92	(000758)	07688 08A20A92	(000758)
A44 07690 08A20A92	(000759)	07690 08A20A92	(000759)
A45 07692 08A20A92	(000760)	07692 08A20A92	(000760)
A46 07694 08A20A92	(000761)	07694 08A20A92	(000761)
A47 07696 08A20A92	(000762)	07696 08A20A92	(000762)
A48 07698 08A20A92	(000763)	07698 08A20A92	(000763)
A49 07700 08A20A92	(000764)	07700 08A20A92	(000764)
A50 07702 08A20A92	(000765)	07702 08A20A92	(000765)
A51 07704 08A20A92	(000766)	07704 08A20A92	(000766)
A52 07706 08A20A92	(000767)	07706 08A20A92	(000767)
A53 07708 08A20A92	(000768)	07708 08A20A92	(000768)
A54 07710 08A20A92	(000769)	07710 08A20A92	(000769)
A55 07712 08A20A92	(000770)	07712 08A	


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A20 07642 02600260 (00111)
A21 07644 10000023 (00112)
A22 07646 43003000 (00113) DCTSN
A23 07648 55005500 (00114) CLRSFLG
A24 0764A 08F008F0 (00115)
A25 0764C 08F008F0 (00116)
A26 0764E 000008F0 (00117)
A27 07650 84144414 (00118) LUMPS
A28 07652 4C134 13 (00120)
A29 07654 08F00000 (00121)
A2A 07656 000008F0 (00122)
A2B 07658 08F008F0 (00124)
A2C 0765A 911F0039 (00125)
A2D 0765C 911F003F (00126)
A2E 0765E 43A04340 (00127)
A2F 07660 20370000 (00128)
A30 07662 84358435 (00129)
A31 07664 40134013 (00131)
A32 07666 08F00000 (00132)
A33 07668 000008F0 (00133)
A34 0766A 08F008F0 (00135)
A35 0766C 911F0039 (00136)
A36 0766E 911F003F (00137)
A37 07670 43A04340 (00138)
A38 07672 10370027 (00139)
A39 07674 02600000 (00140) T3S
A3A 07676 20480000 (00141)
A3B 07678 08F00000 (00142)
A3C 0767A 91080043 (00143)
A3D 0767C 10370005 (00144)
A3E 0767E 00000260 (00146) T4S
A3F 07680 20480000 (00147)
A40 07682 000008F0 (00148)
A41 07684 91080043 (00149)
A42 07686 10370005 (00151)
A43 07688 20370000 (00153)
A44 0768A 08F008F0 (00154)

R=LE'';R=LO''
RFSFT "FLAG JAMS"
LE''+NL;LO''+NL
P<0 FORCING T-RIT->0 FOR
DCT1(J)
O(K)
O(K+1)
A4=O(K-2);O(K);A4=O(K-1)
;O(K+1)
LE''=LE''+2,T(K-2);LO''=LO''
;+2,T(K-1)
O(K+2)
O(K+3)
FORCE COMPLETION
IS T1=1 ON T(K-4)?
IS T2=1 ON T(K-3)?
LE''+2;LO''+2
RELEASE APS INPUT
A5=O(K);O(K+2);A5=O(K+1)
;O(K+3)
LE''=LE''+2,T(K);LO''=LO''
+2,T(K+1)
O(K+4)
O(K+5)
FORCE COMPLETION
IS T1=1 ON T(K-4)?
IS T2=1 ON T(K-3)?
LE''+2;LO''+2
K=K+4 FOR K<LTH
LE
INFORM APS
NO=LE
"FIX" ALL V VALUES @ ONC
E
FOR I=01,2...LTH-1
LO
INFORM APS
NO=LO
"FIX" ALL V VALUES @ ONC
E
FOR I=01,2...LTH-1
RELEASE APS INPUT
M7=SCALAR A

```


ADDRESS	DATA	INSTR	COMMENT
000172	00000000	00000000	POINT TO CONSTRUCTED I
000173	00000000	00000000	STRUCTION BLOCK
000174	00000000	00000000	POINT TO SCALAR BLOCK
000175	00000000	00000000	NUMBER OF SCALARS
000176	00000000	00000000	MODULE SIZE
000177	00000000	00000000	POINT TO CHAIN ANCHOR
000178	00000000	00000000	END OF WORD BOUNDARY
000179	00000000	00000000	
000180	00000000	00000000	
000181	00000000	00000000	
000182	00000000	00000000	
000183	00000000	00000000	
000184	00000000	00000000	
000185	00000000	00000000	
000186	00000000	00000000	
000187	00000000	00000000	
000188	00000000	00000000	
000189	00000000	00000000	
000190	00000000	00000000	
000191	00000000	00000000	
000192	00000000	00000000	
000193	00000000	00000000	
000194	00000000	00000000	
000195	00000000	00000000	
000196	00000000	00000000	
000197	00000000	00000000	
000198	00000000	00000000	
000199	00000000	00000000	
000200	00000000	00000000	
000201	00000000	00000000	
000202	00000000	00000000	
000203	00000000	00000000	
000204	00000000	00000000	
000205	00000000	00000000	
000206	00000000	00000000	
000207	00000000	00000000	
000208	00000000	00000000	
000209	00000000	00000000	
000210	00000000	00000000	
000211	00000000	00000000	
000212	00000000	00000000	
000213	00000000	00000000	
000214	00000000	00000000	
000215	00000000	00000000	

```

A1F 0766C 484A0002 (00216) ADD(HR1,MS,TF)
A20 0766A 40003860 (00217) JUMP(ADDS1)
A21 0766A 42AA0002 (00218) MS3 ADD(HR0,MS,TF)
A22 07662 34041C0C (00219) LOAD(HR1,MS,3(2),TF)
A23 0766A 46AA0002 (00220) ADD(HR2,MS,TF)
A24 0766C 48041C02 (00221) LOAD(HR1,THRS3(2),TF)
A25 0766C 48041C02 (00222) ADD(HR1,MS,TF)
A26 0766A 3C003860 (00223) JUMP(ADDS1)
A27 0766C 4F8A0002 (00224) MS2 ADD(HR0,MS,TF)
A28 0766A 50041C0A (00225) LOAD(HR1,MS,2(2),TF)
A29 07700 52AA0002 (00226) ADD(HR2,MS,TF)
A2A 07702 54041C02 (00227) LOAD(HR1,THRS2(2),TF)
A2B 07704 569A0002 (00228) ADD(HR1,MS,TF)
A2C 07706 58003860 (00229) JUMP(ADDS1)
A2D 07704 5A8A0002 (00230) MS1 ADD(HR0,MS,TF)
A2E 0770A 5C041C0A (00231) LOAD(HR1,MS,1(2),TF)
A2F 0770C 5FAA0002 (00232) ADD(HR2,MS,TF)
A30 0770E 60041C02 (00233) LOAD(HR1,THRS01(2),TF)
A31 07710 629A0002 (00234) ADD(HR1,MS,TF)
A32 07712 64003860 (00235) JUMP(ADDS1)
A33 0771A 66041C0A (00236) MS0 ADD(HR0,MS,TF)
A34 07716 68041C0A (00237) LOAD(HR1,MS,0(2),TF)
A35 07718 6AA00002 (00238) ADD(HR2,MS,TF)
A36 0771A 6C041C02 (00239) LOAD(HR1,THRS01(2),TF)
A37 0771C 6F9A0002 (00240) ADD(HR1,MS,TF)
A38 0771E 709A0002 (00241) ADDS1 ADD(HR1,MS,TF)
A39 07720 729A0002 (00242) SET(W3)
A3A 07722 74000437 (00243) NOP
A3B 07724 76000020 (00244) JUMP(C(ADDS1,AF0))
A3C 07726 78003860 (00245) JUMP(C(LINCS1,AF3))
A3D 07728 7A000437 (00246) *
A3E 0772A 7CC0043F (00247) SCLR5 LOAD(HR0,SAS3H(1),TF)
A3F 0772C 7EC00439 (00248) LOAD(HR0,SAS11(1),TF)
A40 0772E 8040207A (00249) LOAD(HR0,121)
A41 07730 82500000 (00250) LOAD(HR1,MS)
A42 07732 84020000 (00251) SUB(HR0,MS)
A43 07734 860A0002 (00252) IF1XSI ADD(HR0,MS,TF)
A44 07736 880A0002 (00253) ADD(HR0,MS,TF)
A45 07738 8A1943B2 (00254) SUB(HR1,2),JUMP(C(1F1XSI)
A46 0773A 8C200031 (00255) *
A47 0773C 8E000020 (00256) CLEAR(R1)
A48 0773E 90000020 (00257) NOP
A49 07740 92000020 (00258) *
A50 07742 94000020 (00259) *

```

```

IO=OTHR(OFFSET+1)
LFT APU CATCH-UP
IO=DCIT(J)
IO=NL=4
IO=DCIT(J)
IO=OTHR(1 LEVEL)
IO=OTHR(OFFSET+1)
LFT APU CATCH-UP
IO=DCIT(J)
IO=NL=2
IO=DCIT(J)
IO=OTHR(2 LEVEL)
IO=OTHR(OFFSET+1)
LFT APU CATCH-UP
IO=DCIT(J)
IO=NL=1
IO=DCIT(J)
IO=OTHR(1 LEVEL)
IO=OTHR(OFFSET+1)
LFT APU CATCH-UP
IO=DCIT(J)
IO=NL=0
IO=DCIT(J)
IO=OTHR(0 LEVEL)
IO=OTHR(OFFSET+1)
IO=OTHR(OFFSET+1+1)
IO=OTHR(OFFSET+1+2)
WAIT FOR THRESHOLD DEC.
APU DECISION ON APO
FOR I=0,1,2...LTH-1

```

```

IO=2**(-15)
IO=2**(-16)
TMP1(.) RA
TMP1(.) SIZE-1
TMP1(.) RA-2
IO=TMP1(1)
IO=TMP1(1+1)
FOR I=0,1,2...LTH-1
INPUT DONE!

```

```

A4R 0773R 00300032 (00260) G104S0 SET(RA)
A4R 0774R 02402012 (00261) LOAD(RW0,I21)
A4R 0775R 04500000 (00262) LOAD(RW1,MSS)
A4R 0776R 06020000 (00263) SUM(RW0,MSS)
A4R 0777R 08041000 (00264) OINCS,I
A4R 0778R 0A200030 (00265) LOAD(RW2,INSTRS0+1(2),TF)
A4R 0779R 0C000020 (00266) CLEAR(R0)
A4R 0780R 0E0056F9 (00267) NOP
A4R 0781R 10034FC0 (00268) JUMPS(CLNSTMP,AF1)
A4R 0782R 12000030 (00269) LOAD(RW2,APSSKPM(1),TF)
A4R 0783R 14000020 (00270) CLEAR(R0)
A4R 0784R 16000002 (00271) NOP
A4R 0785R 18000000 (00272) ADD(RW0,MS,TF)
A4R 0786R 1A000000 (00273) SUM(RW1,I),JUMPP(OINCS,I)
A4R 0787R 1C000000 (00274) JUMP(OFTXS,AF3),SET
A4R 0788R 1E000000 (00275) CLNSTMP
A4R 0789R 20000030 (00276) ADD(RW0,MS,TF)
A4R 0790R 22000000 (00277) SUM(RW1,I),JUMPP(CLNSTMP)
A4R 0791R 24000000 (00278) CLEAR(R0)
A4R 0792R 26000000 (00279) OFIXS
A4R 0793R 28000000 (00280) LOAD(RW0,I0)
A4R 0794R 2A000000 (00281) LOAD(RW1,MSS)
A4R 0795R 2C000000 (00282) SUM(RW0,MSS)
A4R 0796R 2E000000 (00283) LOAD(RW3,I)
A4R 0797R 30000000 (00284) LOAD(RW2,DWYS(1),TF)
A4R 0798R 32000000 (00285) SUM(RW3,I),JUMPP(B1)
A4R 0799R 34000000 (00286) ADD(RW0,MS,TF)
A4R 0800R 36000000 (00287) ADD(RW0,MS,TF)
A4R 0801R 38000000 (00288) SUM(RW1,2),JUMPP(OFTXS,I)
A4R 0802R 3A000000 (00289) CLEAR(R0)
A4R 0803R 3C000000 (00290) DMFS0
A4R 0804R 3E000000 (00291) NOP
A4R 0805R 40000000 (00292) G104SA= #C
A4R 0806R 42000000 (00293) END #A-1
A4R 0807R 44000000 (00294) STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
A4R 0808R 46000000 (00295) G104SI DATA 4E'0,0'
A4R 0809R 48000000 (00296) ...
A4R 0810R 4A000000 (00297) G104SZ=01-G104S
A4R 0811R 4C000000 (00298) END

```

```

FNAME APU
FMP1(.) NA
TMP1(.) SIZE-1
TMP1(.) NA-2
MSS=INITS IN INSO
STALL APS OUTPUT
RELEASED BY APU
IF AF1=1 THEN ZERO FILL
INSTRS0 INTO APS INPUT
STALL APS OUTPUT
00=TMP3(J)
FOR J=0,1...LTH-1
SIGNAL END TO APS INPUT
00=TMP3(J)
FOR J=2...LTH-1
STALL APS OUTPUT
ODCT(.) NA
ODCT(.) SIZE-1
ODCT(.) NA-1
3+1=4 DUMMIES
00=?
4 TIMES
00=ODCT(I)
00=ODCT(I+1)
FOR I=0,1,2...LTH-1
OUTPUT DONE!
ASSIGN VALUE TO CHAIN AN
CHOR

```

A00S1: 0003H (00211) (00217) (00223) (00229) (00235) (00241) (00245)
A0T0NG: 0006H (00009) (00030)
A0S0M0: 1F00 (00010) (00268)
M01: 00033 (00201) (00236)
M02: 00020 (00202) (00230)
M03: 00027 (00203) (00224)
M04: 00021 (00204) (00218)
M05: 00014 (00205) (00212)
M06: 00015 (00206)
M07: 00012 (00087) (00096)
M08: 00000 (00031) (00094)
M09: 00023 (00112) (00114)
M10: 00056 (00267) (00275) (00276)
M11: 0210C (00011) (00033)
M12: 00022 (00110) (00113)
M13: 00794 (00012) (00283)
M14: 00046 (00165)
M15: 00036 (00257)
M16: 00062 (00289)
M17: 01092 (00050) (00233) (00239)
M18: 01092 (00052) (00227)
M19: 01092 (00054) (00221)
M20: 01092 (00056) (00215)
M21: 01012 (00059) (00209)
M22: 00043 (00095) (00143) (00149) (00153)
M23: 076AF (00032) (00178) (00184) (00297)
M24: 07776 (00176) (00296)
M25: 00048 (00185) (00260)
M26: 00000 (00180) (00297)
M27: 00001 (00014) (00193) (00285) (00286)
M28: 00043 (00253) (00255)
M29: 00008 (00193) (00246)
M30: 00006 (00041) (00200)
M31: 00005 (00080) (00145) (00151)
M32: 01094 (00041) (00197) (00264)
M33: 00503 (00038) (00187)
M34: 00502 (00017) (00018)
M35: 00027 (00118) (00139)
M36: 00000 (00015)
M37: 00000 (00016) (00180) (00180) (00200) (00251) (00262) (00263) (00280) (00281)
M38: 01096 (00043) (00237)
M39: 01094 (00044) (00231)
M40: 01094 (00045) (00225)

PAGE 11: PCN 250... "MPFSTO(Y,U,V,W)" QUANTIZE OCT PARAMETERS

ML\$1:	01C8C (00046)	(00210)
ML\$4:	01C8F (00047)	(00213)
ML\$5:	01C90 (00048)	(00207)
OF1\$1:	00059 (00273)	(00270)
OF1\$3:	0005F (00285)	(00287)
OF1\$J:	0004C (00264)	(00272)
QUAN\$:	07602 (00031)	(00073)
QUAN\$SA:	00000 (00071)	(00075)
QUAN\$SZ:	00052 (00072)	(00169)
SAS11:	00398 (00020)	(00240)
SAS18:	003C1 (00021)	(00238)
SAS19:	00400 (00022)	
SCL\$8:	0003F (00178)	(00196)
START:	07600 (00023)	(00064)
SVTS:	00382 (00019)	(00021)
T\$:	00034 (00125)	(00136)
T4\$:	0003F (00126)	(00137)
TMP1\$:	00F00 (00024)	(00191)
TMP4\$:	00C00 (00025)	(00192)
WS:	00002 (00026)	(00206)
	(00224)	(00228)
	(00242)	(00253)
	(00242)	(00254)
	(00242)	(00251)
	(00242)	(00251)
Z\$:	00003 (00027)	(00210)
ZF0\$:	0000A (00088)	(00214)
		(00216)
		(00218)
		(00220)
		(00222)
		(00230)
		(00232)
		(00234)
		(00236)
		(00238)
		(00240)
		(00241)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) F- 0

PAGE 1: FCR 251... "MPSTO(Y,U,V,W)" DEQUANTIZE DCT PARAMETERS

DEQUANTIZE DCT PARAMETERS
ORIGINATED:14-JAN-80
UPDATED:07-APR-80

FCR 251... "MPSTO(Y,U,V,W)"

DEFINE GLOBAL SYMBOLS

OPADD EXP, (1 .LS, 14) + (12 .LS, 8) + SIF
OPADD ODP, (4 .LS, 10) + (12 .LS, 5) + SIF
OPADD WAPM, (1 .LS, 10) + (12 .LS, 5) + SIA
OPADD FO, (4 .LS, 10) + (12 .LS, 5) + SIC

AFDTSORG=SWFH

AFSSM=SIFFCO

CSPUSMOS=S2IFC

DECT=0'1024'

MS=3

MS=1

INIT=0'2088'

MINIT=>INIT

INIT=0'10392'

MS=0

MS=0

SVTS=S0382

SAS3=SVTS+2*0'38'

SAS3=SVTS+2*0'38'

START=S7FE0

TEMP4=0'3072'

UCT17=>TEMP4

TEMP4=0'9532'

MS=2

MS=3

EXPAND ARRAY FUNCTION DISPATCH TABLE

BI=AFDTSORG+1*2*(251-128)

ADDR QUANS(7,1)

ADDR G104S(7,1)

ADDR CSPUSMOS(1,0)

FJECT

FCR 251

01DNC 0040AC0		
01DNF 00433C1		
01DQ0 00455C1 (00059)	DATA 1F'-1.5770', 1F'-2.1773', 1F'-3.0169', 1F'-4.4311'	
01DQ2 01601C1		
01DQ4 00229C1		
01DQ6 03224C1		
01DQ8 00412C0	DATA 1F'-0.0640', 1F'-0.2004', 1F'-0.3461', 1F'-0.5025'	
01DQ9 00465C0		
01DQC 20400140		
01DQF 40516C0		
01DA0 55F36640 (00061)	DATA 1F'-0.6715', 1F'-0.8550', 1F'-1.0559', 1F'-1.2779'	
01DA2 0070A3C0		
01DA4 00727C1		
01DA6 0A3923C1		
01DAH 003504C1 (00062)	DATA 1F'-1.5259', 1F'-1.8068', 1F'-2.1306', 1F'-2.5129'	
01DAA 007453C1		
01DAC 110076C1		
01DAF 141A64C1		
01DB0 13066C1 (00063)	DATA 1F'-2.9797', 1F'-3.5793', 1F'-4.4180', 1F'-5.8330'	
01DB2 1CA268C1		
01DB4 235063C1		
01DB6 2FA9F0C1		
01DB8 003126C0 (00064)	DATA 1F'-0.0640', 1F'-0.2004', 1F'-0.3461', 1F'-0.5025'	
01DBA 99A685C0		
01DBC AC300140		
01DBF C0516C0		
01DC0 05F36640 (00065)	DATA 1F'-0.0715', 1F'-0.8550', 1F'-1.0559', 1F'-1.2779'	
01DC2 0070A3C0		
01DC4 00727C1		
01DC6 0A3923C1		
01DC8 003504C1 (00066)	DATA 1F'-1.5259', 1F'-1.8068', 1F'-2.1306', 1F'-2.5129'	
01DCA 007453C1		
01DCC 0070A3C1		
01DCE 004164C1		
01DD0 003504C1 (00067)	DATA 1F'-2.9797', 1F'-3.5793', 1F'-4.4180', 1F'-5.8330'	
01DD2 004268C1		
01DD4 A35063C1		
01DD6 AFA9F0C1		

RUS 1
 BL=START
 FVEN
 FJECT

(00068)
 (00069)
 (00070)
 (00071)

(00072) 0	START ADDRESS	FIXED PT. OP.
(00073) 0	MODULIF SIZE	INIT(1)->INSTRS1+1
(00074) 0		WAIT4 INSTRS1+1 REWRITE
(00075) 0		RELEASE APS INPUT
(00076) 0		RELEASE APS OUTPUT
(00077) 0		INSTRS1 INTO INS1
(00078) 0		M7=TMP4(1)=DCT1(J)
(00079) 0		A0=EM=QDCT(1)
(00080) 0		2M
(00081) 0		WAIT4 INS1 REWRITE
(00082) 0		RELEASE APS OUTPUT
(00083) 0		RELEASE APS INPUT
(00084) 0		2M->INSTRS2+1
(00085) 0		WAIT4 INSTRS2+1 REWRITE
(00086) 0		WAIT4 APS TO STALL
(00087) 0		RELEASE APS INPUT
(00088) 0		RELEASE APS OUTPUT
(00089) 0		INSTRS2 INTO INS2
(00090) 0		FLOATING PT. OP.
(00091) 0		WAIT4 INS2 REWRITE
(00092) 0		RELEASE APS INPUT
(00093) 0		RELEASE APS OUTPUT
(00094) 0		M0=DEC8(2M)
(00095) 0		DEC8(2M)+TMP4(1)
(00096) 0		INIT(I+1)->INSTRS1+1
(00097) 0		00=TMP1(1)=>DCT1(J)
(00098) 0		FIXED PT. OP.
(00099) 0		WAIT4 FOR 2 00'S TO SETT
(00100) 0		LF
(00101) 0		RELEASE APS INPUT
(00102) 0		FOR I=0,1,2,...LTH-1
(00103) 0		
(00104) 0		RELEASE APS OUTPUT
(00105) 0		INSTRS4+1=0; INSTRS5+1=0
(00106) 0		INSTRS6+1=0; INSTRS7+1=0
(00107) 0		RELEASE APS INPUT
(00108) 0		
(00109) 0		
(00110) 0		
(00111) 0		
(00112) 0		
(00113) 0		
(00114) 0		
(00115) 0		

ADDRESS	INSTR	OPERANDS	OPERATION	COMMENT
00159	0			
00160	0			
00161	0			
00162				
00163				
00164	?			
00165				
00166				
00167				
00168				
00169				
00170	?			
00171	G104S			
00172				
00173				
00174	SCURS			
00175				
00176				
00177				
00178				
00179				
00180				
00181				
00182	LOOPS1			
00183				
00184				
00185				
00186				
00187	INS1			
00188				
00189				
00190				
00191				
00192				
00193	HS5			
00194				
00195	HS4			
00196				
00197	HS3			
00198				
00199	HS2			
00200				
00201	HS1			
00202				

A1P 07M02 4F541054 (00203)	MS0	LOAD(HR1,DECS0(2))	DECS0(.) RA
A20 07M04 40300037 (00204)		SFT(M1)	STALL APS INPUT
A21 07M06 52000020 (00205)	STALS2	NOP	RELEASED BY APU
A22 07M08 54C41044 (00206)		LOAD(HR0,INSTRS2(2),TF)	REWRITE INS2
A23 07M0A 46300037 (00207)		SFT(M1)	STALL APS INPUT
A24 07M0C 44090016 (00208)		MOVH(HR0,MS)	RECALL TMP4(.) ADDRESS
A25 07M0E 4A9A0000 (00209)	IMS2	ADD(HR1,MS,TF)	APU SPTS MS, IQ=2M
A26 07M0G 413A0001 (00210)		SFT(M1)	IO=INIT(1+1)
A27 07M0I 4F300037 (00211)		NOP	STALL APS INPUT
A28 07M0K 50000020 (00212)		JUMPC(LIMPS1,AF3)	RELEASED BY APU
A29 07M0M 520000AM (00213)		SFT(M1)	FOR I=0,1,2...LTH-1
A2A 07M0N 54B00037 (00214)		NOP	STALL APS INPUT
A2B 07M0A 50000020 (00215)			RELEASED BY APU
A2C 07M0C 59403054 (00216)			
A2D 07M0E 54500000 (00217)		LOAD(HR0,131,TF)	IORDR(.) RA, IO=IORDR=J
A2E 07M0G 500A0000 (00218)		LOAD(HR1,MS)	IORDR(.) SIZE-1
A2F 07M0I 500A0000 (00219)		ADD(HR0,MS,TF)	IO=IORDR(1)=K
A30 07M0K 5F002006 (00220)		LOAD(HR2,121)	TMP1(.) RA
A31 07M0M 60700000 (00221)		LOAD(HR3,MS)	DUMMY UP FOR EXEC
A32 07M0N 62200000 (00222)		SUB(HR2,MS)	TMP1(.) RA-2
A33 07M0A 6120000M (00223)		SUB(HR2,0YR)	TMP1(.) RA-10
A34 07M0C 6619003C (00224)		ADD(HR1,4)	1 EXTRA LOOP IS NEEDED
A35 07M0E 690A0001 (00225)	INDSJ	ADD(HR0,MS,TF)	IO=J'=IORDR(1+2)
A36 07M0G 6C431050 (00226)		ADD(HR0,MS,TF)	IO=K'=IORDR(1+3)
A37 07M0I 6F431052 (00227)		LOAD(HR3,INSTRS4(2),TF)	REWRITE IND\$4
A38 07M0K 70A00002 (00228)		LOAD(HR3,INSTRS5(2),TF)	REWRITE IND\$5
A39 07M0M 72A00002 (00229)		ADD(HR2,MS,TF)	IO=TMP1(1)
A3A 07M0N 750A0001 (00230)		ADD(HR0,MS,TF)	IO=TMP1(1+1)
A3B 07M0A 770A0001 (00231)		ADD(HR0,MS,TF)	IO=J'
A3C 07M0C 78F41054 (00232)		LOAD(HR3,INSTRS6(2),TF)	IO=K''
A3D 07M0E 7AF41056 (00233)		LOAD(HR3,INSTRS7(2),TF)	REWRITE IND\$6
A3E 07M0G 7CA00002 (00234)		ADD(HR2,MS,TF)	REWRITE IND\$7
A3F 07M0I 7FA00002 (00235)		SUB(HR1,4),JUMPP(INDSJ)	IO=TMP1(1+2)
A40 07M0K 80193044 (00236)			IO=TMP1(1+3)
A41 07M0M 82200031 (00237)		CLEAR(H1)	FOR I=0,1,2...LTH+1
A42 07M0N 84000020 (00238)	DNFSI	NOP	INPUT DONE!
A43 07M0A 86300032 (00239)			
A44 07M0C 8830202A (00240)		SFT(RA)	ENABLE APU
A45 07M0E 8A500000 (00241)	G10450	LOAD(HR0,121)	TMP1(.) RA
A46 07M0G 8C020000 (00242)		LOAD(HR1,MS)	TMP1(.) SIZE-1
		SUB(HR0,MS)	TMP1(.) RA-2

A47 07902 44641010 (00241)	LOAD(HW2, INSTRS1+1(2),TF)	SFT MSS IN INSTRS1
A3 07904 90700010 (00248)	CLEAR(R0)	STALL APS OUTPUT
A44 07906 97000020 (00249)	NOOP	RELEASED BY APU
A44 07908 94434400 (00251)	LOAD(HW2, APSSMFM(1),TF)	RFWRITE INSI
A44 0790A 96200030 (00251)	CLEAR(R0)	STALL APS OUTPUT
A4C 0790C 90700020 (00252)	NOOP	RELEASED BY APU
A4D 0790E 90641014 (00253)	LOAD(HW2, INSTRS2+1(2),TF)	SFT MSS=2M IN INSTRS2
A4F 07910 90200030 (00254)	CLEAR(R0)	STALL APS OUTPUT
A4F 07912 90700020 (00255)	NOOP	RELEASED BY APU
A50 07914 A0434400 (00256)	LOAD(HW2, APSSMFM(1),TF)	RFWRITE INS2
A51 07916 A2200030 (00257)	CLEAR(R0)	STALL APS OUTPUT
A52 07918 A4000020 (00258)	NOOP	RELEASED BY APU
A53 0791A A7641040 (00259)	LOAD(HW2, INSTRS1+1(2),TF)	SFT MSS IN INSTRS1
A54 0791C A44A0002 (00260)	ADD(HW0,MSS,TF)	00=TMP1(I)>DPRODUCT(J)
A55 0791E A3114A61 (00261)	SUML(HW1,1),JUMPP(UMYSJ)	FOR I=0,1...LTH-1
A56 07920 A0400024 (00262)	SET(AP3)	SIGNAL END TO APS INPUT
A57 07922 A0200030 (00263)	CLEAR(R0)	STALL APS OUTPUT
A58 07924 B3441051 (00265)	LOAD(HW0, INSTRS4+1(2),TF)	00=0?
A59 07926 B441051 (00266)	LOAD(HW0, INSTRS5+1(2),TF)	00=0?
A5A 07928 B5141055 (00267)	LOAD(HW0, INSTRS6+1(2),TF)	00=0?
A5B 0792A B741057 (00268)	LOAD(HW0, INSTRS7+1(2),TF)	00=0?
A5C 0792C B4300030 (00269)	LOAD(HW0,I0)	DUMMY OP FOR EXEC
A5D 0792E B4500000 (00270)	LOAD(HW1,MSS)	DPRODUCT(,) SIZE-1
A5E 07930 B0200000 (00271)	SUML(HW0,MSS)	DUMMY OP FOR EXEC
A5F 07932 B5700002 (00272)	LOAD(HW1,2)	4 INTERMEDIARY DUMMIES..
A60 07934 C0110030 (00273)	ADDE(HW1,4)	6 1 EXTRA LOOP
A61 07936 C3641055 (00274)	LOAD(HW2, INSTRS6+1(2),TF)	SFT MSS IN INSTRS6
A62 07938 C5641057 (00275)	LOAD(HW2, INSTRS7+1(2),TF)	SFT MSS IN INSTRS7
A63 0793A C4434400 (00276)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS4
A64 0793C C4434400 (00277)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS5
A65 0793E C416A61 (00278)	SUML(HW1,1),JUMPP(UMYS1)	1 ST 4 00'S ARE ?
A66 07940 C4660300 (00279)	LOAD(HW2, DPRODUCTS(1))	DPRODUCT(,) HA
A67 07942 C4A40000 (00280)	ADD(HW2,MSS,TF)	00=DPRODUCT(J*)
A68 07944 D0660400 (00281)	LOAD(HW2,DPRODUCTS(1))	DPRODUCT(,) HA
A69 07946 D2A40000 (00282)	ADD(HW2,MSS,TF)	00=DPRODUCT(K*)
A6A 07948 D5641051 (00283)	LOAD(HW2, INSTRS4+1(2),TF)	SFT MSS IN INSTRS4
A6B 0794A D7641053 (00284)	LOAD(HW2, INSTRS5+1(2),TF)	SFT MSS IN INSTRS5
A6C 0794C D4434400 (00285)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS6
A6D 0794E D4434400 (00286)	LOAD(HW2, APSSMFM(1),TF)	OVERWRITE INDS7
A6E 07950 D311741 (00287)	SUML(HW1,1),JUMPP(UMYS2)	1 ST 4 00'S ARE ?
A6F 07952 D4660400 (00288)	LOAD(HW2, DPRODUCTS(1))	DPRODUCT(,) HA
A70 07954 D4A40000 (00289)	ADD(HW2,MSS,TF)	00=DPRODUCT(J*)
A71 07956 D2660400 (00290)	LOAD(HW2, DPRODUCTS(1))	DPRODUCT(,) HA

AD-A091 663

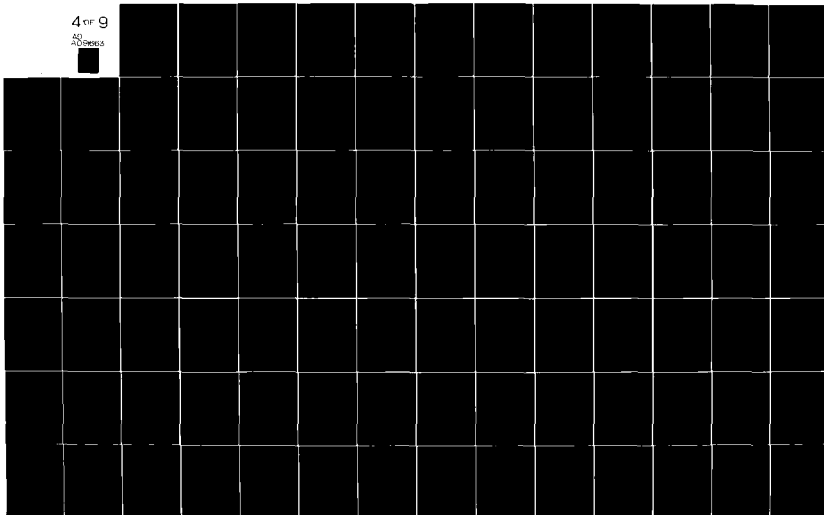
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME 50--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

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AD
ADDRESS



PAGE 9: PC 251... "MPEST(Y,U,V,W)" DEQUANTIZE OCT PARAMETERS

```
A72 0795R F4AA0000 (00291) INDS5 ADD(HW2,MSS,TF)
A73 0795A F41161R4 (00292) DMYS2 SHL(HW1,4),JUMP(INDS1)
      (00293) *
A74 0795C F4200040 (00294) DMFS0 CLEAR(RU)
A75 0795F F4000020 (00295) NOP
      (00296) *
      0000792C (00297) G104SA= RC
      (00298) ;
      07960 (00299) FND #A-1
      07960 00000000 (00300) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
      ... 00000000 (00301) G104S1 DATA 5F'0.0'
      0796A 000000F6 (00302) G104S2=HL-G104S
      (00303) FND
```

NO=DMDC(K#)
FOR I=-3,-2...LTH-1

OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN
CHOR

APDYSORG:	00000	(00000)	(00031)	
APSSMEM:	1F000	(00010)	(00250)	
RS0:	00010	(00100)	(00256)	(00276) (00277) (00285) (00286)
RS1:	00010	(00100)	(00201)	
RS2:	00010	(00100)	(00201)	
RS3:	00010	(00100)	(00190)	
RS4:	00010	(00100)	(00197)	
RS5:	00010	(00100)	(00195)	
CSPIUSNUS:	02100	(00011)	(00034)	
DFCS0:	01000	(00040)	(00203)	
DFCS1:	01000	(00050)	(00201)	
DFCS2:	01000	(00052)	(00190)	
DFCS3:	01000	(00054)	(00197)	
DFCS4:	01000	(00056)	(00195)	
DFCS5:	01000	(00060)	(00193)	
DMYS1:	00000	(00278)	(00281)	
DMYS2:	00073	(00287)	(00292)	
DMFSA:	00042	(00153)		
DMFS1:	00041	(00240)		
DMFS2:	00074	(00244)		
DMFCTS:	00400	(00012)	(00279)	(00281) (00288) (00290)
G1045:	07874	(00033)	(00165)	(00171) (00302)
G1045A:	07420	(00168)	(00297)	
G10451:	07460	(00163)	(00301)	
G10450:	00043	(00172)	(00243)	
G1045Z:	00066	(00167)	(00302)	
MS:	00001	(00014)	(00179)	(00184) (00210) (00226) (00227) (00232) (00233)
IAITS:	02A24	(00017)	(00177)	
INS1:	00000	(00042)	(00187)	
INS2:	00025	(00043)	(00209)	
IND6:	00070	(00044)	(00280)	
IND5:	00072	(00045)	(00291)	
IND6A:	00067	(00046)	(00280)	
IND7:	00069	(00047)	(00282)	
IND8:	00061	(00074)	(00297)	
IND9:	00034	(00226)	(00238)	
INST81:	01040	(00042)	(00182)	(00247) (00259)
INST82:	01040	(00043)	(00206)	(00253)
INST84:	01050	(00044)	(00228)	(00265) (00283)
INST85:	01052	(00045)	(00229)	(00266) (00284)
INST86:	01054	(00046)	(00234)	(00267) (00274)
INST87:	01056	(00047)	(00235)	(00268) (00275)
L00P9:	00004	(00004)	(00110)	
L00P81:	00000	(00182)	(00213)	

MS: 00000 (00014) (00175) (00176) (00187) (00209) (00219) (00220) (00222) (00223) (00245)
 MSS: 00000 (00014) (00270) (00271) (00280) (00282) (00289) (00291)
 NINC5J: 0004A (00250) (00261)
 OIAMS: 077F2 (00032) (0007M)
 OIAMSSA: 00000 (00076) (00080) (00156)
 OIAMSSZ: 00044 (00077) (00156) (00157)
 SAS38: 003CF (00021)
 SAS39: 00300 (00022)
 SCTRS: 00001 (00165) (00173)
 SHUFTS: 00033 (00135) (00151)
 SHUFTSV: 0001F (00112)
 STALS2: 00021 (00194) (00196) (00198) (00200) (00202) (00205)
 STAPT: 077E0 (00023) (00069)
 STARTS: 00001 (00081)
 SVTS: 003M2 (00020) (00021) (00022)
 TMP4S: 0252H (00026) (00178)
 MS: 00002 (00027) (00183) (00230) (00231) (00236) (00237) (00260)
 ZS: 00003 (00028)

PAGE 1:

FOR 252... "MPVDNM(Y,U,V)"

VAR,,DC,INTERPOLATE,FIX

VAR,,DC,INTERPOLATE,FIX
ORIGINATED:18-JAN-80
UPDATED:29-MAY-80

FOR 252... "MPVDNM(Y,U,V)"

(00001) *

(00002) *

(00003) *

(00004) *

(00005) *

(00006) *

(00007) *

(00008) *

(00009) *

(00010) *

(00011) *

(00012) *

(00013) *

(00014) *

(00015) *

(00016) *

(00017) *

(00018) *

(00019) *

(00020) *

(00021) *

(00022) *

(00023) *

(00024) *

(00025) *

(00026) *

(00027) *

(00028) *

(00029) *

(00030) *

(00031) *

(00032) *

(00033) *

(00034) *

(00035) *

(00036) *

(00037) *

(00038) *

DEFINE GLOBAL SYMBOLS

OPADD EXP, (1 .LS. 14) + (12 .LS. 4) + \$1F

OPADD ODF, (3 .LS. 10) + (12 .LS. 5) + \$1R

OPADD WAFMF, (1 .LS. 10) + (12 .LS. 5) + \$1A

OPADD FU, (3 .LS. 10) + (12 .LS. 5) + \$1C

APDTSORC=SRFH

CSPUSNUS=\$21FC

IMYS=\$0704

W=3

WS=1

WS=0

WS=0

NOUITS=0'1462'

NSIZE=\$0'246'-0'1'

SVTS=\$0142

SAS11=SVTS+240'11'

SAS14=SVTS+240'34'

SAS19=SVTS+240'14'

SAS100=SVTS+240'100'

SAS101=SVTS+240'101'

START=\$7980

WS=2

WS=4

X2=\$0'2048'

XSIZE=\$0'256'-0'1'

YMS=\$0'2048'

YSIZE=\$0'10'-0'1'

ZS=3

EXPAND ARRAY FUNCTION DISPATCH TABLE

BL=AFDTSORC+342*(252-128)

ADDR VINS(P7,1)

ADDR G100S(P7,1)

ADDR CSPUSNUS(1,0)

JECT

FOR 252


```

A1D 079D2 84808340 (00100)
A1E 079D4 3A943A94 (00101) ;
A1F 079D6 9016000F (00102)
A20 079D8 20372037 (00103)
A21 079DA 00000000 (00104)
A22 079DC 089C084C (00105)
A23 079DE 85F485F4 (00106)
A24 079E0 02840284 (00107)
A25 079E2 52805280 (00108)
A26 079E4 84018401 (00109)
A27 079E6 3A943A94 (00110)
A28 079E8 08F808F8 (00111)
A29 079EA 000008F8 (00112)
A2A 079EC 433C433C (00113)
A2B 079EE 90160023 (00114)
A2C 079F0 20372037 (00115)
A2D 079F2 00000000 (00116)
A2E 079F4 85F485F4 (00117)
A2F 079F6 08F808F8 (00118)
A30 079F8 433C433C (00119)
A31 079FA 08F808F8 (00120)
A32 079FC 000008F8 (00121)
A33 079FE 9016002F (00122)
A34 07A00 089C084C (00123)
A35 07A02 00000000 (00124)
A36 07A04 20260000 (00125)
A37 07A06 20372037 (00126)
A38 07A08 00000000 (00127)
07A0A 00000039 (00128)
07A0B 00000039 (00129)
07A0C 00000039 (00130)
07A0D 00000039 (00131)
07A0E 00000039 (00132)
07A0F 00000039 (00133)
07A10 00000039 (00134)
07A11 00000039 (00135)
07A12 00000039 (00136)
07A13 00000039 (00137)

```

```

MOV(A0),MUL(M1,M5)
MOV(A4),A1,ICN(A4)
JUMPC(P1,F+1)
CLEAR(M1)
NOP
MOV(R,00)
*LOOP FOR I=N+2, N+4, ..., N2+2
MOV(A4),MUL(M1,M7)
MOV(M1),R(A4)
ADJUST(A4,A2)
MOV(A1),MUL(M1,M6)
MOV(A4),A1,ICN(A4)
MOV(IQA,M3)\NOP
NOP\MOV(IQA,M3)
MOV(00),ADD(A1,A3)
JUMPC(P2,F+1)
CLEAR(MJ)
NOP
*LOOP FOR I=N2+2, N2+4, ..., N3-2
MOV(M3,M7)
MOV(P,A1)
MOV(00),ADD(A1,A3)
MOV(IQA,M3)\NOP
NOP\MOV(IQA,M3)
JUMPC(P3,F1)
MOV(R,00)
NOP
CLEAR(P2)\NOP
CLEAR(PA)
NOP
VDINSS2=8A-VDINSSA
END VDINSS2
PJFCT

```

```

MULD(I);NEW'(I)*MMPY(I)=
MNEW(I)
FIX(I-2)

OUTPUT FIX(N-2)\FIX(N-1)

NEW'(I-4)*SA;NEW'(I)*VAR
NEW'(I-2);NEW'(I-4)*SA
ROUND NEW'(I-4)*SA
NEW'(I)*VAR;NEW'(I-2)*SA
FIX(I-4)
NEW(I+2)
NEW(I+3)
OUTPUT FIX(I-4);NEW(I)*V
AR+DC=NEW'(I)

NEW(I)*VAR
NEW(I)*VAR
OUTPUT NEW'(I-2);NEW(I)*
VAR+DC=NEW'(I)
NEW(I+2)
NEW(I+3)
NEW'(N3-2)\NEW'(N3-1)

PLACE FOR TIMING PATCH
END SYNTHESIZER TIMING

```



```

A1D 07A4C 3A140034 (00142)      SUHL(RR1,4)
A1E 07A4E 3CA0004 (00143) #2     ADD(HR0,4,TF)
A1F 07A50 3FA00034 (00144)      SUHL(RR3,4,TF)
A20 07A52 4019142 (00145)      SUHL(RR1,2),JUMPP(#2)
A21 07A54 4202004 (00146)      SUH(HR0,4)
A22 07A56 4434003C (00147)      ADDL(RR3,4)
A23 07A58 46500002 (00148)      LOAD(RR1,2)
                                *JUMP FOR 1=N2-4,N2-2,...,N2 OR 6 DUMMIES
A24 07A5A 48A0004 (00149) #3     ADD(HR0,4,TF)
A25 07A5C 4AH00034 (00141)      SUHL(RR1,4,TF)
A26 07A5E 4C192401 (00142)      SUHL(RR1,1),JUMPP(#3)
A27 07A60 4E300037 (00143)      SET(W1)
A28 07A62 50640402 (00144)      LOAD(RR2,YOMS+2(2))
A29 07A64 52500009 (00145)      LOAD(RR1,YSIZES)
                                *JUMP FOR 1=0,2,...,N2+01,02-2=N3-2
A2A 07A66 54A0004 (00147) #4     ADD(HR0,4,TF)
A2B 07A68 56A00034 (00148)      SUHL(RR3,4,TF)
A2C 07A6A 58142402 (00149)      SUHL(RR1,2),JUMPP(#4)
                                *
A2D 07A6C 5A200031 (00201) DNEST  CLEAR(R1)
A2E 07A6E 5C000020 (00202)      NIP
                                *
A2F 07A70 5E300032 (00203)      SET(RA)
A30 07A72 60500003 (00205)      LOAD(RW1,3)
A31 07A74 63420794 (00206) DMYSN  LOAD(HW0,DMYS(1),TF)
A32 07A76 64113141 (00207)      SUHL(RW1,1),JUMPP(DMYSN)
A33 07A78 66440F15 (00208)      LOAD(RW0,NOUTS-1(2))
A34 07A7A 68500000 (00209)      LOAD(RW1,MSS)
A35 07A7C 6A700009 (00210)      LOAD(RW3,YSIZES)
                                * INTERPOLATED OUTPUTS
A36 07A7E 6D0A0001 (00211) #1     ADD(HW0,HS,TF)
A37 07A80 6F313641 (00213)      SUHL(RW3,1),JUMPP(#1)
A38 07A82 71720794 (00214)      LOAD(RW1,DMYS(1),TF)
A39 07A84 73720794 (00215)      LOAD(RW3,DMYS(1),TF)
A3A 07A86 75720794 (00216)      LOAD(RW3,DMYS(1),TF)
A3B 07A88 77720794 (00217)      LOAD(RW3,DMYS(1),TF)
                                * NON-INTERPOLATED OUTPUTS
A3C 07A8A 78500054 (00219)      LOAD(RW1,NSIZES-YSIZES-1)
A3D 07A8C 7A0A0001 (00220) #2     ADD(HW0,HS,TF)
A3E 07A8E 7C113041 (00221)      SUHL(RW1,1),JUMPP(#2)
                                * NORMALIZED MEMORY
A3F 07A90 7E440744 (00223)      LOAD(HW0,YOMS-2(2))
A40 07A92 80500004 (00224)      LOAD(RW1,YSIZES)
A41 07A94 828A0007 (00225) #3     ADD(HW0,WS,TF)

```

```

N2=N-3
NEW(N+1+2)

```

```

NEW(1)

```

```

STALL APS INPUT
YOM(.) RA+2
YOM(.) SIZE-1

```

```

INPUT DONE!

```

```

ENAHIF APU
3+1=4 DUMMIES
00=2
FOR I=0,1...3
  NOUT(.) HA-1
  NOUT(.) SIZE-1
  YOM(.) SIZE-1
  00=NOUT(1)
  FOR I=0,1,...,NSIZE-1
    00=2
    00=2
    00=2
    NSIZE-YSIZE-1
    00=NOUT(1)
    FOR I=YSIZE...NSIZE-1
      YOM(.) HA-2
      Y(.) SIZE-1
      00=YOM(1)

```

PAGE 7: 4CH 252... "MPVDM(V,U,V)" VAR.,DC,INTERPOLATE, FIX

```
A42 07A96 R41141N1 (00226) SUBI(RW1,1),JUMPP(B1)
      (00227) *
A43 07A98 R6200030 (00228) DMFSO CLEAR(R0)
A44 07A9A R8000020 (00229) NOP
A45 07A9C R8400000 (00230) LOAD(RW0,101)
A46 07A9E R0500000 (00231) LOAD(RW1,MSS)
A47 07AAD R4020000 (00232) SUBI(RW0,MSS)
      (00233) *
      00007A9C (00234) G100SA= #C
      (00235) ;
      07AA2 (00236) FND #A-1
      (00237) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
      07AA2 00000000 (00238) G100SI DATA 1F'0,0'
      00000002 (00239) G100S2=81-G100S
      07AA4 (00240) FND
```

FOR I=0,1...YSIZE-1

OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN
CHDR

AFPS(WG:	00000	(00004)	(00034)
CPUS(WG:	02100	(00010)	(00037)
DMYS:	00794	(00011)	(00206)
DMYS0:	00011	(00206)	(00207)
DMYS1:	00016	(00111)	
DMYS2:	00020	(00201)	
DMYS3:	00043	(00224)	
FS0:	07480	(00014)	
FS1:	07482	(00045)	(00153)
FS2:	07484	(00046)	
FS3:	07486	(00047)	
FS4:	07488	(00048)	
FS5:	0748A	(00049)	
FS6:	0748C	(00050)	
FS7:	0748E	(00051)	
FS8:	07490	(00052)	
FS9:	07492	(00053)	
FSA:	07494	(00054)	
G100S:	07A10	(00036)	(00144)
G100SA:	07A12	(00147)	(00230)
G100S1:	07A14	(00147)	(00234)
G100S2:	07A16	(00147)	(00238)
G100S3:	0002F	(00151)	(00204)
G100S4:	00027	(00146)	(00239)
HS:	00001	(00013)	(00212)
MS:	00000	(00014)	
MSS:	00000	(00015)	(00209)
MUTS:	00F16	(00016)	(00208)
MS1FS:	000F5	(00017)	(00219)
SAS100:	0044A	(00022)	(00155)
SAS101:	0044C	(00023)	(00154)
SAS11:	0039H	(00019)	(00157)
SAS3H:	0030F	(00020)	(00156)
SAS3Q:	00300	(00021)	
SC1PS:	00001	(00144)	(00152)
STAP:	07480	(00024)	(00042)
SVTS:	00342	(00018)	(00019)
VD1WS:	0748H	(00035)	(00062)
VD1WSA:	00000	(00060)	(00064)
VD1WSZ:	00039	(00061)	(00135)
WS:	00002	(00025)	(00225)
WWS:	00004	(00026)	
X2S:	00000	(00027)	(00158)
X512FS:	0000F	(00028)	(00159)
Y0MS:	00000	(00029)	(00160)
		(00194)	(00223)

PAGE 4: PCN 252... "MPVDNM(Y,U,V)" VAR.,DC,INTERPOLATE,PIX

YSIZES: 00009 (00010) (00161) (00195) (00210) (00219) (00224)
28: 00003 (00011)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) E= 0

22

011670
011670
07060
07060
07060
07060
07060

```

B1=$7A1D
DATA $79H6
B1=$7A1F
DATA $79H4
B1=$79H6
DATA $2000,$3D
DATA $1000,$3D
END

```

CHANGE: FROM ISA TO LOCAL.

222-14

204-15

END

PAGE 2: 0L=STAID

LINES WITH ERRORS: 0 (MAP VERSION M00101.10) K= 0

PAGE 2: FOR 253... "MPHASP(Y,II,V,W)" BASIS SPECTRUM CALCULATION

[illegible]

[illegible]

```

A17 07H04 85F0H5F0 (0009H)      MUL(M3,M7)
A18 07H06 0HAF08AF (0009H)      MOV(P,M7)
      (00100) *
A19 07H08 H41H84H0 (00102) * LOOP FOR N=0,1,2,...,2+2
      (00103) * WLP      MOV(EXD),MUL(M0,M4)\MOV(A0),MUL(M1,M5)
      (00104) *
A1A 07H0A 08C0H4A4 (00104)      MOV(I0,M1)\MOV(EX1,M2)
A1B 07H0C 0HFD089C (00105)      MOV(T0A,M5)\MOV(R,00)
A1C 07H0E 0H8A08C8 (00106)      MOV(R,M2)\MOV(I0,M0)
A1D 07H10 0000H4FC (00107)      NOP\MOV(T0A,M4)
A1E 07H12 84H08571 (00108)      MOV(A0),MUL(M1,M5)\MOV(A1),MUL(M2,M7)
      (00109) *
A1F 07H14 00004100 (00110)      NOP\ADD(A0,A1)
A20 07H16 000008C9 (00111)      NOP\MOV(I0,M1)
A21 07H18 000008FD (00112)      NOP\MOV(T0A,M5)
A22 07H1A 08C0H000 (00113)      MOV(I0,M0)\NOP
A23 07H1C 0HFC7F52 (00114)      MOV(T0A,M4)\MOV(A2),RCP(A2)
      (00115) *
A24 07H1E H5714413 (00116)      MOV(A1),MUL(M2,M7)\MOV(A3),MUL(M0,M4)
      (00117) *
A25 07H20 4100H6AF (00118)      ADD(A0,A1)\MOV(M7),LGS(A3)
A26 07H22 90160019 (00119)      JUMPC(M1,P,FMT)
A27 07H24 20327037 (00120)      CLEAR(M1)
A28 07H26 00000000 (00121)      NOP
      (00122) *
      TMP2(I)=0.5*LOG2(DCT1(I))*LOG2(SORT(DCT1(I)))
      *NOW DO LOG REFORMATTING
      (00123) *
      (00124) * PICK UP TWO FARE LOGS AT A TIME, MANTISSA AND EXPONENT
      (00125) * SEPARATELY.
      (00126) *
      K(1)
      (00127) *
A29 07H28 16H016H0 (00128)      MOV(T0A,M5)
A2A 07H2A 0HFD08FD (00128)      K(R)\MOV(A4),K(4)
A2B 07H2C 16F01604 (00129)      MOV(T0A,A0)\NOP
A2C 07H2E 08F00000 (00130)      NOP\MOV(T0A,A0)
A2D 07C00 000008FD (00131)      MOV(A5),INCR(A5)
A2E 07C02 22H522H5 (00132)      MOV(T0A,M4)\MOV(A5),ADD(A4,A5)
A2F 07C04 0HFC4595 (00133)      NOP\MOV(T0A,M4)
A30 07C06 000008FC (00134)      MOV(R,FX0)\MOV(R,FX0)
A31 07C08 0H8A0898 (00135)      MOV(R,M0)\MOV(R,A6)
A32 07C0A 0H8A0896 (00136)      MOV(EX1,A6)\MOV(EX1,M0)
A33 07C0C 0H5A08A8 (00137)      AHS(MUL(M0,M4))
A34 07C0E H400H400 (00138)      SUB(A0,A6)
A35 07C10 4F004F00 (00139)      MOV(R,A1)
A36 07C12 08410891 (00140)      MOV(T0A,A0)\MOV(ZERO,M3)
A37 07C14 08F00808 (00141)

```

ENG/FCR^2 (=A)

$V(N-2)^{-2} \cdot VR(N)^{-2} \cdot WR(N-1)^{-2}$
 $W(N-1)^{-2}$
 $VI(N) \cdot V(N-2)^{-2} \cdot A$
 $VI(N) \cdot L(N-3) \cdot OUT$
 $V(N-1)^{-2} \cdot WR(N)$
 $VR(N)$
 $VR(N)^{-2} \cdot VI(N)^{-2} \cdot W(N-1)^{-2}$
 $2 \cdot V(N-2)^{-2} \cdot A \cdot W(N-2)^{-2}$
 $W(N-1)^{-2}$
 $W(N)$
 $W(N)$
 $VR(N+1)$
 $VR(N+1) \cdot W(N-1)^{-2} \cdot W(N-1)^{-2}$
 $VI(N)^{-2} \cdot V(N-1)^{-2} \cdot A \cdot W(N-2)$
 $VR(N)^{-2}$
 $V(N)^{-2} \cdot W(N-1)^{-2} \cdot L(N-2)$

KD=2 (1/2 *LOG2(16))

STORE 1.0

M0

M1

GET 12R, \64.

FO\GET 65.

F1

GET 12R, 65 ON BOTH SIDES

12R, \65.

65, \12R.

12R, *E0\12R, *F1

M0-65\M1-65

M0-65\M1-65

M2

```

A38 07C16 000000F0 (00142)
A39 07C18 000000F2 (00143)
A3A 07C1A 000000F4 (00144)
A3B 07C1C 000000F6 (00145)
A3C 07C1E 000000F8 (00146)
A3D 07C20 000000FA (00147)
A3E 07C22 000000FC (00148)
A3F 07C24 000000FE (00149)
A40 07C26 00000100 (00150)
A41 07C28 00000102 (00151)
A42 07C2A 00000104 (00152)
A43 07C2C 00000106 (00153)
A44 07C2E 00000108 (00154)
A45 07C30 0000010A (00155)
A46 07C32 0000010C (00156)
A47 07C34 0000010E (00157)
A48 07C36 00000110 (00158)
A49 07C38 00000112 (00159)
A4A 07C3A 00000114 (00160)
A4B 07C3C 00000116 (00161)
A4C 07C3E 00000118 (00162)
A4D 07C40 0000011A (00163)
A4E 07C42 0000011C (00164)
A4F 07C44 0000011E (00165)
A50 07C46 00000120 (00166)
A51 07C48 00000122 (00167)
A52 07C4A 00000124 (00168)
A53 07C4C 00000126 (00169)
A54 07C4E 00000128 (00170)
A55 07C50 0000012A (00171)
A56 07C52 0000012C (00172)
A57 07C54 0000012E (00173)
A58 07C56 00000130 (00174)
A59 07C58 00000132 (00175)
A5A 07C5A 00000134 (00176)
A5B 07C5C 00000136 (00177)
A5C 07C5E 00000138 (00178)
A5D 07C60 0000013A (00179)
A5E 07C62 0000013C (00180)
A5F 07C64 0000013E (00181)
A60 07C66 00000140 (00182)
A61 07C68 00000142 (00183)
A62 07C6A 00000144 (00184)
A63 07C6C 00000146 (00185)
A64 07C6E 00000148 (00186)
A65 07C70 0000014A (00187)
A66 07C72 0000014C (00188)
A67 07C74 0000014E (00189)
A68 07C76 00000150 (00190)
A69 07C78 00000152 (00191)
A6A 07C7A 00000154 (00192)
A6B 07C7C 00000156 (00193)
A6C 07C7E 00000158 (00194)
A6D 07C80 0000015A (00195)
A6E 07C82 0000015C (00196)
A6F 07C84 0000015E (00197)
A70 07C86 00000160 (00198)
A71 07C88 00000162 (00199)
A72 07C8A 00000164 (00200)
A73 07C8C 00000166 (00201)
A74 07C8E 00000168 (00202)
A75 07C90 0000016A (00203)
A76 07C92 0000016C (00204)
A77 07C94 0000016E (00205)
A78 07C96 00000170 (00206)
A79 07C98 00000172 (00207)
A7A 07C9A 00000174 (00208)
A7B 07C9C 00000176 (00209)
A7C 07C9E 00000178 (00210)
A7D 07CA0 0000017A (00211)
A7E 07CA2 0000017C (00212)
A7F 07CA4 0000017E (00213)
A80 07CA6 00000180 (00214)
A81 07CA8 00000182 (00215)
A82 07CAA 00000184 (00216)
A83 07CAC 00000186 (00217)
A84 07CAE 00000188 (00218)
A85 07CB0 0000018A (00219)
A86 07CB2 0000018C (00220)
A87 07CB4 0000018E (00221)
A88 07CB6 00000190 (00222)
A89 07CB8 00000192 (00223)
A8A 07CBA 00000194 (00224)
A8B 07CBC 00000196 (00225)
A8C 07CCE 00000198 (00226)
A8D 07CB0 0000019A (00227)
A8E 07CB2 0000019C (00228)
A8F 07CB4 0000019E (00229)
A90 07CB6 000001A0 (00230)
A91 07CB8 000001A2 (00231)
A92 07CBA 000001A4 (00232)
A93 07CBC 000001A6 (00233)
A94 07CCE 000001A8 (00234)
A95 07CB0 000001AA (00235)
A96 07CB2 000001AC (00236)
A97 07CB4 000001AE (00237)
A98 07CB6 000001B0 (00238)
A99 07CB8 000001B2 (00239)
A9A 07CBA 000001B4 (00240)
A9B 07CBC 000001B6 (00241)
A9C 07CCE 000001B8 (00242)
A9D 07CB0 000001BA (00243)
A9E 07CB2 000001BC (00244)
A9F 07CB4 000001BE (00245)
AA0 07CB6 000001C0 (00246)
AA1 07CB8 000001C2 (00247)
AA2 07CBA 000001C4 (00248)
AA3 07CBC 000001C6 (00249)
AA4 07CCE 000001C8 (00250)
AA5 07CB0 000001CA (00251)
AA6 07CB2 000001CC (00252)
AA7 07CB4 000001CE (00253)
AA8 07CB6 000001D0 (00254)
AA9 07CB8 000001D2 (00255)
AAA 07CBA 000001D4 (00256)
AAB 07CBC 000001D6 (00257)
AAC 07CCE 000001D8 (00258)
AAD 07CB0 000001DA (00259)
AAE 07CB2 000001DC (00260)
AAF 07CB4 000001DE (00261)
AAG 07CB6 000001E0 (00262)
AAH 07CB8 000001E2 (00263)
AAI 07CBA 000001E4 (00264)
AAJ 07CBC 000001E6 (00265)
AAK 07CCE 000001E8 (00266)
AAL 07CB0 000001EA (00267)
AAO 07CB2 000001EC (00268)
AAP 07CB4 000001EE (00269)
AAQ 07CB6 000001F0 (00270)
AAR 07CB8 000001F2 (00271)
AAS 07CBA 000001F4 (00272)
AAT 07CBC 000001F6 (00273)
AAU 07CCE 000001F8 (00274)
AAV 07CB0 000001FA (00275)
AAW 07CB2 000001FC (00276)
AAX 07CB4 000001FE (00277)
AAY 07CB6 00000200 (00278)
AAZ 07CB8 00000202 (00279)

```

```

M3
12R*E0\12R*E1
F2
F3
GET 0 IN P
M0-65+12R*E0\12R*E1
F1
STORE A*E(N-2(N-1))
OUTPUT A*E(N-2(N-1))
+2)\F(N+3))
L(N(N+1))M(N+2(N-1))-65
M(N+4)\M(N+3))-65
M(N)-65;SUM+A*E(N-2)\M(N
+1)-65;SUM+A*E(N-1)
M(N+5)
12R*E(N+2(N+3))L(N(N+1)
)*SD
STORE SUM;M(N+2(N-1))-65+12R*
E(N+2)\(N+3)
F(N+4)
F(N+5)
RE SAFE?
ENOUGH?
ODD SUM
ATLTH
ATLTH-EVEN SUM\ODD SUM
ODDSUM
ATLTH-EVEN-ODD
1./LTH
ATLTH-SUM
(ATLTH-SUM)/LTH
OUTPUT IT (CTEMP)
M3=SA TEMPORARILY
M4=2*(-22) TEMPORARILY
M5=2*(15)
A4=4*65*16*(-6)
M1=C5

```

```

A5H 07C5C 0MF10MF7 (00186)
A5C 07C5F 0MF60MF7 (00187)
A5D 07C60 0MF50MF5 (00188)
A5E 07C62 0MAC0MAC (00189)
A5F 07C64 0MF00000 (00190) 81
A60 07C66 00000MF4 (00191)
A61 07C6H 451C451C (00192)
A62 07C6A 3A6H3A6H (00193)
A63 07C6C 1F731F73 (00194)
A64 07C6F 45014501 (00195)
A65 07C70 3A2H3A2H (00196)
A66 07C72 32523252 (00197)
A67 07C74 0H430H43 (00198)
A68 07C76 46704670 (00199)
A69 07C7H 027H027H (00200)
A6A 07C7A 45C045C0 (00201) 2
A6B 07C7C 0MF20MF2 (00202)
A6C 07C7F 5A005A00 (00204)
A6D 07C80 0H430H43 (00205)
A6E 07C82 45704570 (00206)
A6F 07C84 4A0H4A0H (00207)
A70 07C86 45C045C0 (00208)
A71 07C8H 0MF20MF2 (00209)
A72 07C8A 0H430H43 (00210)
A73 07C8C 453H453H (00211)
A74 07C8F 43A043A0 (00212)
A75 07C90 4A2H4A2H (00213)
A76 07C92 45014501 (00214)
A77 07C94 0MF20MF2 (00215)
A78 07C96 0H430H43 (00216)
A79 07C9H 0H430H43 (00217)
A7A 07C9A 0MF00MF0 (00218)
A7B 07C9C 4A0H4A0H (00219)
A7C 07C9F 47204720 (00220)
A7D 07CA0 0H430H43 (00221)
A7E 07CA2 4A714A71 (00222)
A7F 07CA4 47204720 (00223)
A80 07CA6 401C005F (00224)
A81 07CAM 20320000 (00225) 6
A82 07CAA 00000000 (00226) 0MFSA
A83 07CAC 10000000 (00227) NOP
      00000084 (00228) JMP(0)
      00000084 (00229) BASSS2=BA-HASSSA

MOV(T0A,A7)
MOV(T0A,A6)
MOV(T0A,A5)
MOV(P,M4)
MOV(T0A,M2)\NOP
NOP\MIV(T0A,M2)
MOV(M0),MUL(M2,M4)
MOV(M3),ALIG(A3)
MOV(A1),EXP(A3)
MOV(A1),MUL(M3,M6)
MOV(M0),ALIG(A1)
MOV(A2),NUM(A2)
MOV(P,A3)
MOV(A0),ADD(A3,A6)
MOV(M3),R(A1)

MUL(M3,M6)
MOV(T0A,A2)
ADD(TA0,A2)
MOV(P,A3)
MOV(A0),ADD(A3,A5)
MOV(M3),ADD(A0,A4)
MUL(M3,M6)
MOV(T0A,A2)
MOV(R,M2)
MOV(A3),MUL(M2,M5)
ADD(A2,A3)
MOV(M3),SUB(A1,A0)
MOV(A3),MUL(M3,M6)
MOV(T0A,A2)
MOV(R,MUL)
MOV(P,A1)
MOV(R,M6)
MUL(M1,M6)
ADD(A1,A2)
MOV(R,M7)
MOV(A1),MUL(M0,M7)
ADD(A1,A7)
JMP(CA1,F0)

CT,PAR(HA)\NOP
NOP
JMP(0)
BASSS2=BA-HASSSA

```

```

A7=C4
A6=C3
A5=C2
M4=SA*28*(-22)
M2=X(0,M)

00=X(19,K-2),X(1,K)
A3=X(9,K-1),X(7,K-1)
A3=X(7,K-1),X(10,K-1)
A1=X(1,K),X(11,K-1)
M0=X(10,K-1),X(2,K)
A2=X(2,K),X(3A,K)
A3=X(11,K-1)
A0=X(3A,K),X(12,K-1)
M3=X(12,K-1),SET T RIT H
Y X(3A,K)
X(13,K-1)
A2=-16*(-6)
X(3H,K)
A3=X(13,K-1)
A0=X(3H,K),X(14,K-1)
M3=X(A4,K-1),X(4,K)
X(15,K-1)
A2=C1
M2=X(4,K)
A3=X(15,K-1),X(5,K)
X(16,K-1)
M3=X(16,K-1),X(6,K)
A3=X(5,K),X(17,K-1)
A2=C0

A1=X(17,K-1)
M6=X(6,K)
X(R,K)
X(18,K-1)
M7=X(18,K-1)
A1=X(R,K),X(19,K-1)
X(9,K)
FOR I=0,1,2...LTH-1

```

APU NONE!

PAGE 7: PCB 253... "MPRASPE(U,V,W)" BASIS SPECTRUM CALCULATION

OTCAF

(00230)
(00231)

FWD MASS52
FJKT


```

(00276) *
(00277) *CHANGE FORMAT OF LOGS
A1F 07CF2 4CC20386 (00278) LOAD(HR0,SAS02(1),TF)
A1F 07CF4 3401036 (00279) LOAD(HR0,11)
A20 07CF6 40500000 (00280) LOAD(HR1,MSS)
A21 07CF8 42600000 (00281) LOAD(HR2,MSS)
A22 07CFA 44601006 (00282) LOAD(HR2,11,S)
A23 07CF0 46300000 (00283) LOAD(HR3,MSS)
A24 07CF2 47000000 (00284) LOAD(HR3,MSS)
A25 07D00 48000032 (00285) SURL(HR0,2)
A26 07D02 4C200031 (00286) SURL(HR2,1)
A27 07D04 4F140031 (00287) SURL(HR1,1)
A28 07D06 510A0002 (00288) * LOOP FOR N = 0,2,...,12-2
A29 07D08 530A0002 (00289) ADD(HR0,2,TF)
A2A 07D0A 550A0002 (00290) ADD(HR0,2,TF)
A2B 07D0C 570A0002 (00291) ADD(HR2,2,TF)
A2C 07D0E 590A0002 (00292) ADD(HR2,2,TF)
A2D 07D10 5B0A0002 (00293) SURL(HR1,2),JUMPP(83)
A2E 07D12 5D0A0002 (00294) LOAD(HR1,4)
A2F 07D14 5F0A0002 (00295) MOVH(HR0,HR0,TF)
A30 07D16 60142F43 (00296) MOVH(HR0,HR0,TF)
A31 07D18 63090011 (00297) SURL(HR1,1),JUMPP(84)
A32 07D1A 65090011 (00298) MOVH(HR0,HR0,TF)
A33 07D1C 66300037 (00299) SET(M1)
A34 07D1E 68000020 (00300) NOP(0)
A35 07D20 6AC20420 (00301) LOAD(HR0,SAS79(1),TF)
A36 07D22 6CC203FF (00302) LOAD(HR0,SAS54(1),TF)
A37 07D24 6E02038F (00303) *
A38 07D26 70027840 (00304) EXPST
A39 07D28 72000005 (00305) LOAD(HR1,SAS06(1),TF)
A3A 07D2A 740A0002 (00306) LOAD(HR1,TARLES(1),TF)
A3B 07D2C 762934M1 (00307) *
A3C 07D2E 78020334 (00308) EXPST
A3D 07D30 7A700000 (00309) LOAD(HR2,1),JUMPP(81)
A3E 07D32 7C020000 (00310) SURL(HR0,121)
A3F 07D34 7E0A0002 (00311) LOAD(HR3,MSS)
A40 07D36 800A0002 (00312) SUR(HR0,MSS)
A41 07D38 820A0002 (00313) ADD(HR0,MSS,TF)
A42 07D3A 840A0002 (00314) ADD(HR1,MSS,TF)
A43 07D3C 860A0002 (00315) ADD(HR1,MSS,TF)

1/2 * LOG2(16) = 2
PWEIT Z=1
DUMMY
NEED TO ADDRESS IT AS SH
(ORT, T00
DUMMY
DUMMY
UHASE -2
U-1
U2-2
M(N)
M(N+1)
K(N)
K(N+1)
SIX PAIRS OF DUMMIES (10
+2)
"MM" AND "F"

WAIT FOR APU
ATLTH
1/1,TH
10=0.25
10=2**(-22)
10=2**15,4*65*16**(-6)
C5,C4,C3,C2
6 CONSTANTS TOTAL!
TMP2(,) RA
TMP2(,) SIZE-1
TMP2(,) RA-2
10=TMP2(1)
10=TMP2(1+1)
10=-16**(-6)
10=C1
10=C0

```

```

A44 0703F HRI00036 (00320)      SUML(RW1,6)
A45 07040 HRI00037 (00321)      SUML(RW1,7),JUMPP(R2)
A46 07042 MCC20794 (00322)      LOAD(RW0,DWYS(1),TF)
A47 07044 RRC20794 (00323)      LOAD(RW0,DWYS(1),TF)
A48 07046 QQA00002 (00324)      ADD(RW1,W,S,TF)
A49 07048 QQA00002 (00325)      ADD(RW1,W,S,TF)
A4A 0704A QQA00002 (00326)      ADD(RW1,W,S,TF)
A4B 0704C QQA00002 (00327) *
A4C 0704F QQA00002 (00328) DNEF1
A4D 07050 QQA00002 (00329) NOP
A4E 07052 QQA00002 (00330) *
A4F 07054 QQA00002 (00331) G204SD
A50 07056 QQA00002 (00332)      SPT(RA)
A51 07058 QQA00002 (00333)      LOAD(RW3,SAS73(1),TF)
A52 0705A QQA00002 (00334)      LOAD(RW0,DWYS(1),TF)
A53 0705C QQA00002 (00335)      MOVH(RW0,RW0,TF)
A54 0705E QQA00002 (00336)      MOVH(RW0,RW0,TF)
A55 0705F QQA00002 (00337)      LOAD(RW0,11,TF)
A56 07060 QQA00002 (00338)      LOAD(RW1,MSS)
A57 07062 QQA00002 (00339)      LOAD(RW2,MSS)
A58 07064 QQA00002 (00340)      ADD(RW0,2,TF)
A59 07066 QQA00002 (00341)      SUML(RW1,2)
A5A 07068 QQA00002 (00342) * LOOP FOR N = 2,4,...,UZ-2
A5B 0706A QQA00002 (00343)      ADD(RW0,2,TF)
A5C 0706C QQA00002 (00344)      SUML(RW1,2),JUMPP(R5)
A5D 0706E QQA00002 (00345) *
A5E 0706F QQA00002 (00346) * OUTPUT FLT PT LOGS
A5F 07070 QQA00002 (00347)      LOAD(RW0,DWYS(1),TF)
A60 07072 QQA00002 (00348)      MOVH(RW0,RW0,TF)
A61 07074 QQA00002 (00349)      LOAD(RW0,12,TF)
A62 07076 QQA00002 (00350)      LOAD(RW1,MSS)
A63 07078 QQA00002 (00351)      LOAD(RW2,MSS)
A64 0707A QQA00002 (00352)      ADD(RW0,2,TF)
A65 0707C QQA00002 (00353)      SUML(RW1,2)
A66 0707E QQA00002 (00354) * LOOP FOR N=2,3,...,YZ-2
A67 07080 QQA00002 (00355)      ADD(RW0,2,TF)
A68 07082 QQA00002 (00356)      ADD(RW0,2,TF)
A69 07084 QQA00002 (00357)      SUML(RW1,2),JUMPP(R6)
A70 07086 QQA00002 (00358)      LOAD(RW0,SASH0(1),TF)
A71 07088 QQA00002 (00359) *
A72 0708A QQA00002 (00360) EXPD0
A73 0708C QQA00002 (00361)      LOAD(RW0,DWYS(1),TF)
A74 0708E QQA00002 (00362)      LOAD(RW0,DWYS(1),T,1)
A75 07090 QQA00002 (00363)      LOAD(RW0,DWYS(1),TF)
A76 07092 QQA00002 (00364)      LOAD(RW0,101,TF)
A77 07094 QQA00002 (00365) *
A78 07096 QQA00002 (00366) *

```

```

-16**(-6) RA
FOR I=0,1,2,...,LTH-1
  IQ=?
  IQ=?
  IQ=-16**(-6)
  IQ=C1
  IQ=C0
INPUT DONE1

KNABIE APU
EGR1 = 1/EGR
NEED 3 DUMMIES OUT

UC(0)
UZ-1
DUMMY, SPACING MUST BE 2
UC(1)

UC(N)
UC(N+1)

2 DUMMIES
TMP2(0)
YZ-1
DUMMY
Y(1)

YCN)
YCN+1)

NORMALIZED SUM OF LOGS

IQ=?
IQ=?
IQ=?
DCTM1(.,) RA,00=DCTM1(0)

```


DCFM1(.,) SIZE-1
 DCTM1(.,) RA-2
 HQ=DCTM1(1)
 HQ=DCTM1(1+1)
 HQ=DCTM1(1+2)
 HQ=DCTM1(1+3)
 FOR I=1,2,...,UTH-1

OUTPUT DONE!

ASSIGN VALUE TO CHAIN AN
 CHOR

A6Q 07000 12700000 (00365) LOAD(HW1,MSS)
 A6A 07000 13020000 (00365) SUB(HW0,MSS)
 A6M 07000 13340000 (00365) 01 ADD(HW0,MS,TF)
 A6C 07000 13660000 (00365) ADD(HW0,MS,TF)
 A6D 07000 13980000 (00365) ADD(HW0,MS,TF)
 A6E 07000 14300000 (00365) ADD(HW0,MS,TF)
 A6F 07000 14620000 (00370) SUB(HW1,4),JUMP(B1)
 A70 07000 14940000 (00371) 0 CLEAR(P0)
 A71 07000 15260000 (00373) DNEF0 NOP
 00007000 (00374) 0 G204SA=BC
 0700A (00375) ; END RA=1
 0700A (00376) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
 0700A 00000000 (00379) G204SI DATA 7F10,0
 ...
 0700A 00000002 (00380) G204SZ=81-G204S
 0700A (00381) END

AFUTSORG:	00000	(00000)	(00032)	
MASS:	07000	(00033)	(00060)	
MASSA:	00000	(00050)	(00075)	(00229)
MASSZ:	00004	(00059)	(00229)	(00230)
CSPUSNOIS:	02100	(00000)	(00035)	
IMYS:	00790	(00009)	(00270)	(00322) (00323) (00333) (00347) (00360) (00361) (00362)
DNFSA:	00001	(00226)		
DNFS1:	00000	(00328)		
DNFS0:	00070	(00372)		
FXPS:	00055	(00100)		
FXPS1:	00037	(00307)		
FXPS0:	00065	(00360)		
G204S:	07000	(00033)	(00230)	(00244) (00300)
G204SA:	07000	(00241)	(00375)	
G204S1:	07000	(00236)	(00370)	
G204S0:	00040	(00245)	(00331)	
G204SZ:	00002	(00240)	(00380)	
HS:	00001	(00011)		
IP:	00030	(00150)	(00165)	
MSS:	00000	(00012)	(00250)	(00251) (00280) (00281) (00284) (00285) (00313) (00314) (00337)
		(00338)	(00350)	(00351) (00364) (00365)
MLP:	00019	(00102)	(00119)	
SAS00:	00302	(00014)	(00250)	(00260)
SAS02:	00306	(00015)	(00270)	
SAS06:	00300	(00016)	(00307)	
SAS12:	00300	(00017)	(00250)	
SAS50:	00300	(00018)	(00305)	
SAS60:	00300	(00019)	(00240)	
SAS73:	00410	(00020)	(00247)	(00332)
SAS76:	00410	(00021)		
SAS77:	00410	(00022)		
SAS79:	00420	(00023)	(00304)	
SAS80:	00420	(00024)	(00350)	
START:	07000	(00025)	(00040)	
SVTS:	00302	(00013)	(00014)	(00015) (00016) (00017) (00018) (00019) (00020) (00021) (00022)
		(00023)	(00024)	
TARIFS:	07000	(00043)	(00300)	
TARIFS2:	07000	(00050)		
WS:	00002	(00026)	(00310)	(00315) (00316) (00317) (00318) (00319) (00324) (00325) (00326)
		(00366)	(00367)	(00368) (00369)
WWS:	00004	(00027)		
ZS:	00003	(00020)		
ZZHS:	00007	(00029)		

SCAN DCT BASIS SPECTRUM
 ORIGINATED:02-NOV-79
 UPDATED:25-MAY-80

FCR 254... "MPSCAN(Y,U,V)"

DEFINE GLOBAL SYMBOLS

OPADD EXP, (1 .LS. 14) + (12 .LS. 8) + SIF
 OPADD DDF, (3 .LS. 10) + (12 .LS. 5) + SIF

AFDTSORG=SREF

APSSMFM=SIFFCO

CSPUSNOS=S21FC

DMS=\$794

M=3

HS=1

MSS=0

SVTS=S03R2

SAS12=SVTS+2*D'12'

SAS13=SVTS+2*D'13'

SAS79=SVTS+2*D'79'

SASR0=SVTS+2*D'R0'

SASR2=SVTS+2*D'R2'

SASR3=SVTS+2*D'R3'

SASR4=SVTS+2*D'R4'

START=S7F10

S7FS=D'256'-D'1'

TMPI5=D'204H'

TMP25=D'2560'

WS=2

WMS=4

ZS=3

ZZHS=7

EXPAND ARRAY FUNCTION DISPATCH TABLE

EL=AFDTSORC+3*2*(254-128)

ADDR SCAS(R7,1)

ADDR G205S(R7,1)

ADDR CSPUSNOS(1,0)

FJFCT

FCR 254

(00001)	*
(00002)	*
(00003)	*
(00004)	*
(00005)	*
(00006)	*
(00007)	*
(00008)	*
(00009)	*
(00010)	*
(00011)	*
(00012)	*
(00013)	*
(00014)	*
(00015)	*
(00016)	*
(00017)	*
(00018)	*
(00019)	*
(00020)	*
(00021)	*
(00022)	*
(00023)	*
(00024)	*
(00025)	*
(00026)	*
(00027)	*
(00028)	*
(00029)	*
(00030)	*
(00031)	*
(00032)	*
(00033)	*
(00034)	*
(00035)	*
(00036)	*

00000000C
 00117F17
 00000000C
 00117F24
 00000000C
 00117F1C
 00000000C
 00117F16


```

A21 07F5A 02A00000 (00088) VALDSN R(A5)\NOP
A22 07F5A 08C00000 (00089) MOV(R,M0)\NOP
A23 07F5A 02A00000 (00090) P(A3)\NOP
A24 07F5A 08C00000 (00091) MOV(R,M0)\NOP
A25 07F5C 20372037 (00092) CLEAR(WI)
(00093) *
A26 07F5A 08A00000 (00094) * ITOT'=1.0/ITOT
A27 07F60 08A00000 (00095) SINVS
A28 07F62 16A016A0 (00096) MOV(R,M0)\NOP
A29 07F64 08A00000 (00097) K(2)
A30 07F66 21000000 (00098) MOV(R,A6)\NOP
A31 07F68 08A00000 (00099) P(P(A0)\NOP
A32 07F6A 84A00000 (00100) MOV(R,M0)\NOP
A33 07F6C 08A00000 (00101) MUL(M0,M6)\NOP
A34 07F6E 49C00000 (00102) MOV(P,A1)\NOP
A35 07F70 08A00000 (00103) SUB(A6,A1)\NOP
A36 07F72 08A00000 (00104) MOV(R,M4)\NOP
A37 07F74 08A00000 (00105) MUL(M0,M4)\NOP
A38 07F76 08A00000 (00106) MOV(P,M0)\NOP
A39 07F78 08A00000 (00107) MUL(M0,M6)\NOP
A40 07F7A 08A00000 (00108) MOV(P,A1)\NOP
A41 07F7C 08A00000 (00109) SUB(A6,A1)\NOP
A42 07F7E 08A00000 (00110) MOV(R,M4)\NOP
A43 07F80 08A00000 (00111) MUL(M0,M4)\NOP
A44 07F82 08A00000 (00112) MOV(P,M7)\NOP
(00113) *
A45 07F84 08A00000 (00114) * CTMPD=(RTLTH-S(LG))/ITOT
A46 07F86 08A00000 (00115) SSUMS
A47 07F88 02000200 (00116) MOV(ZFRO,A0)
A48 07F8A 08A00000 (00117) P(A0)
A49 07F8C 08A00000 (00118) #1
A50 07F8E 08A00000 (00119) MOV(10A,A0)\MOV(A7),ADD(A0,A7)
A51 07F90 08A00000 (00120) NOP
A52 07F92 91160048 (00121) JUMPS(SS2,FW1)
A53 07F94 471708F1 (00122) MOV(A7),ADD(A0,A7)\MOV(10A,A1)
A54 07F96 08A00000 (00123) NOP
A55 07F98 91160048 (00124) JUMPS(SS3,FW1)
A56 07F9A 08A00000 (00125) MOV(10A,A1)\MOV(A7),ADD(A1,A7)
A57 07F9C 08A00000 (00126) NOP
A58 07F9E 91160048 (00127) JUMPS(SS4,FW1)
A59 07FA0 471708F0 (00128) MOV(A7),ADD(A1,A7)\MOV(10A,A0)
A60 07FA2 471708F0 (00129)
A61 07FA4 471708F0 (00130)
S
A5=UE(?) * CTMP
00=UE(?) * CTMP
A3=ITOT
00=ITOT
RELEASE APS INPUT
M6=SC=ITOT
A0=ITOT
P=2.0;R=2.0
A6=2
R1=1/(C+DEL(=FO))
M0=FO
P1=FO*C
A1=FO*C
R2=2-FO*C
M4=R2
P2=FO*R2(=F1)
M0=F1
P3=F1*C
A1=P3
R3=2-F1*C
M4=R3
P4=F1*(2-F1*C)(=F2)
M7=1.0/ITOT
A6=RTLTH
A0=0.0
R=0.0
A0=UE;A7=SUM0(-2),A7+U0(-1)
?
A7=SUME(-1),A7+UE;A1=U0
?
A1=UE(+1);A7=SUM0(-1),A7+SUM0
?
A7=SUME,A7+UE(+1);A0=U0(+1)

```

```

A45 07F9C 00000000 (00131) NOP
A46 07F9F 00160030 (00132) JUMPC(R1,F*1)
A47 07FA0 00104717 (00133) MOV(ZERO,A0)\MOV(A7),ADD(A0,A7)
A48 07FA2 47170011 (00134) SS2 MOV(A7),ADD(A0,A7)\MOV(ZERO,A1)
A49 07FA4 00114737 (00135) SS3 MOV(ZERO,A1)\MOV(A7),ADD(A1,A7)
A4A 07FA6 47370000 (00136) SS4 MOV(A7),ADD(A1,A7)\NOP
A4B 07FA8 00400048 (00137) MOV(R,A0)\MOV(R,F*0)
A4C 07FAA 00410000 (00138) MOV(F*1,A1)\NOP
A4D 07FAC 43004100 (00139) ADD(A0,A1)
A4E 07FAE 40130000 (00140) MOV(A3),SUR(A6,A3)\NOP
A4F 07F80 00400000 (00141) MOV(R,M0)
A50 07F82 00400000 (00142) MUL(M0,M7)\NOP
A51 07F84 00400000 (00143) ? MOV(P,M0)\NOP
A52 07F86 20372037 (00144) ? CUPAR(H1)
A53 07F88 16601660 (00145) * TMP2(1)=TMP2(1)+CTEMP+0.5 I=0,1,2,...,LTH-1
A54 07F8A 00400000 (00146) K(0.5)
A55 07F8C 00400000 (00147) MOV(P,F*0)\NOP
A56 07F8E 41110000 (00148) MOV(P,A0)\MOV(F*1,A0)
A57 07F90 47570000 (00149) MOV(A1),ADD(A0,A1)\NOP
A58 07F92 00154000 (00150) MOV(A7),ADD(A2,A7)\NOP
A59 07F94 00155000 (00151) MOV(ZERO,A4)\NOP
A5A 07F96 4F600000 (00152) MOV(10A,A3)\MOV(10A,A6)
A5B 07F98 00154000 (00153) SUR(A3,A7)\NOP
A5C 07F9A 00159000 (00154) ? MOV(R,A2)\NOP
A5D 07F9C 00160000 (00155) MOV(10A,A3)\NOP
A5E 07F9E 4A600000 (00156) SUR(A3,A2)\NOP
A5F 07FA0 476002C0 (00157) MOV(10A,F*0)\NOP
A60 07FA2 00164000 (00158) VADD$2 ADD(A3,A7)\R(A6)
A61 07FA4 00165000 (00159) JUMPC(M0K,T1)
A62 07FA6 00000054 (00160) MOV(A5),MAX(A5,A4)\NOP
A63 07FA8 00167000 (00161) NOP\MOV(F*1,F*0)
A64 07FAA 00168000 (00162) MOV(F*1,A2)\NOP
A65 07FAC 011F000A (00163) MOV(R,F*0)\NOP
A66 07FAE 00170000 (00164) JUMPS(ZR0,T1)
A67 07FB0 00171000 (00165) ? MOV(R,M0)\MOV(F*1,A6)
A68 07FB2 10000070 (00166) JUMPC(VADD$2,F*1)
A69 07FB4 00F00000 (00167) ? JUMP(VADD$2F)
A6A 07FB6 00F00000 (00168) MOV(10A,A0)\NOP

```

```

A0=SUMF,FX0=SUM0
A1=SUM0
A3=(SUMF+SUM0),RTLTH-A3
M0=RTLTH-(SUMF+SUM0)
ITOT'*(RTLTH-(SUMF+SUM0)
)
SA=CTEMP=(RTLTH-SING)/IT
NT
RELEASE APS INPUT

R=.5
FX0=CTEMP
A0=CTEMP
A1=0.5,CTEMP+0.5
A7=CTEMP+0.5, ADD FIRST
AND CONST.
MIN
MAX\OLD SUM=MAX
SUM CONST, SINCE WE ADD
IT LATER
THIS IS NOW OLD VALUE.
A3=NEW
NEW=OLD
SAVE NEW
NEW SUM\OLD SUM
JUMPIF NEW>OLD
NEWSUM,ZERO
NEW INPUT
NEW INPUT
OLD SUM <= NEW SUM
JUMPIF 0.GF,NEWSUM
OUTPUT NEW SUM,OLD SUM <=
- NEWSUM
LOOP
DUMMY TO USE UP INPUT

```

```

A6A 07FE6 081C0000 (00175) ZND      MOV(ZERO,00)\NOP
A6H 07FE6 90160069 (00176)          JUMPC(ZR0LP,FWI)
      (00177) ;
A6C 07FE6 20372037 (00178)          CLEAR(MI)
A6D 07FE6 10000071 (00179)          JUMPI(VINPTS)
A6E 07FE6 0000009C (00180) NDK      NOP\MOV(R,00)
A6F 07FE6 9016005C (00181)          JUMPC(VAIDS2,FWI)
A70 07FE6 20372037 (00182) VAIDS2F  CLEAR(MI)
      (00183) *
A71 07FE6 08FE00FF (00184) * TMP1(T)=INTEGER PART(TMP2(I)) I=0,1,2,...,LTH-1
A72 07FE6 08FE00FF (00185) VINPTS  MOV(10A,M7)
A73 07FE6 08170017 (00186)          MOV(10A,M6)
A74 07FE6 08170017 (00187)          MOV(ZERO,A7)
A75 07FE6 08FE0000 (00188)          MOV(10A,M0)\NOP
A76 07FE6 08FE0000 (00189)          MOV(10A,A6)\NOP
A77 07FE6 000000FF (00190)          NOP\MOV(10A,M0)
A78 07FE6 000000FF (00191)          NOP\MOV(10A,A6)
A79 07FE6 02C902C9 (00192) 81      MOV(A1),MUL(M0,M6)
      (00193)
A7A 07FE6 5F205F20 (00194) ;      MOV(M1),R(A6)
      (00195)
A7B 07FE6 84F084F0 (00196)          MOV(NULL),ADDL3(A1,A7)
A7C 07FE6 3A1C3A1C (00197)          MOV(A0),MUL(M1,M7)
A7D 07FE6 08FE0000 (00198)          MOV(100),ALIGN(A0)
A7E 07FE6 08FE0000 (00199)          MOV(10A,M0)\NOP
A7F 07FE6 08FE0000 (00200)          MOV(10A,A6)\NOP
A80 07FE6 000000FF (00201)          NOP\MOV(10A,M0)
A81 07FE6 32103210 (00202)          NOP\MOV(10A,A6)
A82 07FE6 901C0078 (00203)          MOV(A0),NORM(A0)
      (00204) *
A83 07FE6 20320000 (00205) DNPSA   JUMPC(81,F0)
A84 07FE6 00000000 (00206)          CLEAR(MA)\NOP
      (00207)
      07F1C 00000005 SCASSZ=8A-SCASSA
      (00208)
      (00209)
      END SCASSZ
      EJECT

```

```

KEEP GOING TIL INPUT DONE
F
LET APS GO
GO ON TO NEXT RTN
OLD SUM OUT
LOOP
RELEASE APS INPUT

```

```

M7=16**6)
M6=16**(-6)
A7=SA=0.0
M0=X(0,1)
A6=X(0,1-4)
M0=X(0,1+1)
A6=X(0,1-3)
A1=X(4,1-2),P=X(1,1)
M1=X(3,1-1),T1=SIGN(X(0,
I-1))
R=X(5,1-2)
A0=X(1,1),P=X(4,1-1)
OQ=X(5,1-2),R=X(2,1)
M0=X(0,1+2)
A6=X(0,1-2)
X=(0,1+3)
X=(0,1-1)
A0=X(2,1),R=X(3,1)
FOR I=0,1,2,...,LTH-1
APU DONE!

```



```

A1C 07F5C 3AA0002 (00254)      ADD(RW2,WS,TF)
A1D 07F5F 3A391H1 (00255)      SUBL(RW3,1),JUMPP(#2)
                                CLFAR(R1)
A1F 07F60 3C20003 (00257) DNEF1
                                NOP
A1F 07F62 3A00002 (00258)      JSN(G205S0,P2)
A20 07F64 4020274 (00259)      G205S1
A21 07F66 4219002 (00260)      TOPSP1
A22 07F68 44A0002 (00261)      ADD(RW0,WS,TF)
A23 07F6A 4619224 (00262)      SUBL(RW1,1),JUMPP(#1)
A24 07F6C 4830003 (00263)      SET(M1)
A25 07F6E 4A2F000 (00265)      ADD(RW2,WS,NA,C)
A26 07F70 4C00216 (00266)      JUMP(TOPSP1)
A27 07F72 4F30394 (00268)      JSN(G205S3,P3)
A28 07F74 50C60A0 (00269)      LOAD(RW0,TMP2S(3),TF)
A29 07F76 525000F (00270)      LOAD(RW1,SIZES-2)
A2A 07F78 54AE002 (00271)      AND(RW0,WS,TF,C)
A2B 07F7A 56F20426 (00272)      LOAD(RW2,SASH2(1),TF)
A2C 07F7C 58F2042H (00273)      LOAD(RW2,SASH3(1),TF)
A2D 07F7E 5AF20422 (00275) DSUMS
A2E 07F80 5C60A00 (00277)      LOAD(RW0,TMP2S(3),TF)
A2F 07F82 5F5000F (00278)      LOAD(RW1,SIZES-2)
A30 07F84 60HF0002 (00279)      AND(RW0,WS,TF,C)
A31 07F86 62700003 (00281)      LOAD(RW3,3)
A32 07F88 64F20744 (00282)      LOAD(RW2,DWYS(1),TF)
A33 07F8A 66313241 (00283)      SUBL(RW3,1),JUMPP(#2)
A34 07F8C 68C60A0 (00284)      LOAD(RW0,TMP1S(3),TF)
A35 07F8E 6A5000F (00285)      LOAD(RW1,SIZES-2)
A36 07F90 6C4F0002 (00286)      AND(RW0,WS,TF,C)
A37 07F92 6F200030 (00288)      CLFAR(R0)
A38 07F94 70000020 (00289)      NOP
A39 07F96 72300032 (00290)      SET(PA)
A3A 07F98 74110031 (00291)      SUBL(RW1,1)
A3B 07F9A 76A0002 (00292)      AND(RW0,WS,TF)
A3C 07F9C 78113H1 (00293)      SUBL(RW1,1),JUMPP(#1)
A3D 07F9E 7A8F0002 (00294)      AND(RW0,WS,TF,C)
A3E 07FA0 7C004A60 (00295)      JUMP(TOPSP3)
                                G205SA= BC
                                00007F2A (00297)

```

```

IQ=TMP2(LTH-4)
FOR I=0,1,...5
INPUT DONE!

SET OUTPUT PC(PC2)
SIZE-1-2
IQ=DCTM2(1) OR TMP2(1)
FOR I=2,3,...LTH-2
STALL BEFORE JUMP! CRITI
CAL!!
PC->PC0
LET "CHANGE" SETTLE

SET OUTPUT PC(PC3)
IQ=TMP2(0)
TMP2(,) SIZE-3
IQ=TMP2(1),CHANGE PC
IQ=U(7)+CTEMP
IQ=ITOT

IQ=CTEMP

IQ=TMP2(0)
TMP2(,) SIZE-3
IQ=TMP2(1),CHANGE PC

3+1=4 DUMMIES
IQ=?
FOR I=0,1,...3
IQ=TMP1(0)
TMP1(,) SIZE-3
IQ=TMP1(1),CHANGE PC

OUTPUT DONE!

ENABLE APU
SIZE-1-1
IQ=TMP2(1) OR TMP1(1)
FOR I=2,3,...LTH-2
IQ=... (LTH-1),CHANGE PC
PC->PC2

ASSIGN VALUE TO CHAIN AN

```

PAGE 9: YCR 254... "MPCAN(Y,U,V)" SCAN DCT MASSS SPECTRUM

CHOR

07FA2 (00298) ;
(00299) ;
(00300) ; STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS
07FA2 00000000 (00301) G20551 DATA IF'0,0'
00000000 (00302) G20552=01-G2055
07FA4 (00303) ;
END

AFDTSUMG:	000000	(00007)	(00032)
APSSMEM:	100000	(00000)	
CSPUSMOS:	02100	(00004)	(00035)
DMS:	00700	(00010)	(00202)
DMPGA:	00000	(00205)	
DNFS1:	00010	(00257)	
DNFS0:	00000	(00200)	
G2056:	07020	(00014)	(00216) (00222) (00302)
G20551:	00020	(00223)	(00250)
G20563:	00030	(00260)	(00290)
G2056A:	07020	(00210)	(00207)
G20561:	07020	(00210)	(00301)
G20560:	00027	(00250)	(00260)
G20567:	00000	(00210)	(00302)
HS:	00001	(00012)	
IADUS:	00000	(00235)	
IMPTS:	00000	(00240)	
INTPS1:	00017	(00240)	(00251)
ISUMS:	00006	(00230)	
MSS:	00000	(00013)	(00227) (00246) (00253) (00265)
MOK:	00000	(00160)	(00100)
OADUS:	00020	(00277)	
OMPTS:	00031	(00281)	
OK:	00001	(00165)	
OSUMS:	00020	(00275)	
SS2:	00000	(00121)	(00134)
SS3:	00000	(00124)	(00135)
SS4:	00000	(00120)	(00136)
SS12:	00000	(0015)	(00240)
SS13:	00000	(0016)	(00241)
SS19:	00020	(00017)	(00230)
SSR0:	00022	(00010)	(00225) (00275)
SSR2:	00020	(00010)	(00212)
SSR3:	00020	(00020)	(00231)
SSR4:	00020	(00021)	(00235)
SCAS:	07012	(00033)	(00040)
SCASSA:	00000	(00047)	(00053) (00207)
SCASSZ:	00000	(00040)	(00207) (00200)
SCIMS:	00002	(00216)	(00225)
SINVS:	00020	(00095)	
SIPFS:	00000	(00023)	(00232) (00237) (00240) (00270) (00278) (00285)
SSUMS:	00030	(00115)	
START:	07010	(00022)	(00041)
SVTS:	00000	(00014)	(00016) (00017) (00018) (00019) (00020) (00021)

PAGE 11: FCM 254... "MPSCAN(Y,U,V)" SCAN DET BASIS SPECTRUM

TMP1S:	00000 (00024) (00284)	
TMP2S:	00000 (00025) (00231)	(00236) (00242) (00243) (00269) (00277)
TMP3P1:	00021 (00260) (00266)	
TMP3P3:	0001A (00291) (00295)	
VADUS:	00006 (00059) (00071)	
VADUS2:	0005C (00160) (00172) (00181)	
VADUS2A:	00070 (00173) (00182)	
VADUS2M:	00021 (00076) (00079) (00083) (0008H)	
VDS1:	0001C (00063) (00082) (00086)	
VDS2:	00014 (00066) (00073)	
VIMPTS:	00071 (00179) (00185)	
WS:	00002 (00026) (00228) (00233) (0023H) (00245) (00249) (00250) (00254) (00261) (00271)	
	(00279) (00286) (00292) (00294)	
WWS:	00004 (00027)	
ZS:	00003 (00028)	
ZM:	0006A (00169) (00175)	
ZRULP:	00069 (00174) (00176)	
ZZMS:	00007 (00029)	

UNITS WITH ERRORS: 0 (MAP VERSION R00101.10) F- 0

(00001) : FCR 255... "MPCDHA(Y,U,V)" COMPLETE DCT HIT ASSIGNMENT
(00002) * ORIGINATED:25-JUN-79
(00003) * UPDATED:22-MAY-80
(00004) * DEFINE GLOBAL SYMBOLS
(00005) OPADD EXP, (1 .LS. 14) + (12 .LS. 8) + \$IF
(00006) OPADD UDF, (1 .LS. 10) + (12 .LS. 5) + \$IH
(00007) AFDTSURGE=\$RPH
(00008) APSSMFM=\$IFFCO
(00009) CSPUSNOS=\$21FC
(00010) DMS=\$794
(00011) TRITS=0'208H'
(00012) #=3
(00013) HS=1
(00014) MSS=0
(00015) SVTS=\$04H2
(00016) SAS3H=\$VTS+2*0'3H'
(00017) SAS79=\$VTS+2*0'79'
(00018) SASM4=\$VTS+2*0'84'
(00019) STZFS=D'256'-0'1'
(00020) STANT=\$H100
(00021) TMPIS=D'204H'
(00022) WS=2
(00023) WMS=4
(00024) *
(00025) * EXPAND ARRAY FUNCTION DISPATCH TABLE
(00026) RI=\$AFDTSURG+3*2*(255-128)
(00027) ADDR CDRA\$(R7,1)
(00028) ADDR G102\$(R7,1)
(00029) ADDR CSPUSNOS(1,0)
(00030) EJECT

PAGE 4: FOR 255... "MPCDRA(Y,U,V)" COMPLETE DCT HIT ASSIGNMENT

```

A1F 0813E 00000012 (00001) NOP\MOV(ZF00,A2)
A1F 08140 000004200 (00002) NOP\ADD(A4,A2)
A20 08142 00000000C (00003) NOP\MOV(R,00)
A21 08144 10000027 (00004) JUMP(MTSOUT)
A22 08146 000000200 (00005) MTSA NOP\R(A4)
A23 08148 00000000C (00006) NOP\MOV(R,00)
A24 0814A 10400026 (00007) JUMP(MTSTST,AF1),SFT
A25 0814C 000000000 (00008) MOV(10A,00)\NOP
A26 0814E 90160025 (00009) MTSTST JUMPC(MTSIN,F01)
A27 08150 90100027 (00010) MTSHUT JUMPC(MTSHUT,00E)
A28 08152 20372037 (00011) CLEAR(M1)
A29 08154 00000000F (00012) * MOV(10A,M7)
A2A 08156 000000000 (00013) * MOV(10A,M0)\NOP
A2B 08158 00000000F (00014) * NOP\MOV(10A,M0)
A2C 0815A 04710471 (00015) MOV(A1),MUL(M0,M7)
A2D 0815C 3A3C3A3C (00016) MOV(00),ALIGN(A1)
A2E 0815E 9010002A (00017) * JUMPC(M1,F1)
A2F 08160 20320000 (00018) DNESA CLEAR(CR)\NOP
A30 08162 000000000 (00019) * NOP
08164 000000031 (00104) * CDRASSZ=BA-CDHASSA
08165 000000005 (00105) * END CDRASSZ
08166 000000006 (00106) * EJECT

INCRMENT=0
IRIT(LTH)+?
IRIT(LTH)=IRIT(LTH)+?
INPUT CLFAP,CHECK OUTPUT
R=IRIT(?)
IRIT(?)=IRIT(?)
ENTER "MT OUFUF" a TEST
IRIT(1)=IRIT(1)
WAIT FOR INPUT TO COMPLE
TE
WAIT FOR OUTPUT TO CLEAR
RELEASE APS INPUT

M7=SA
M0=00F
M0=000
A0=P',P=SA*U
00=R',M'="IFIX"(P)
FOR ALI I=0,1,2...LTH-1

API DONE

```


PAGE 7: FCH 255... "MPCDRA(V,U,V)" COMPLETE DCT HIT ASSIGNMENT

0000007C (00195)
00196) END

0000007C (00195)
00196) END

COMPLETE DCT HIT ASSIGNMENT

PAGE 42 FCH 255... "MPCDRA(V,U,V)"

ADIS:	00009 (00051)	(00057)
APDSURC:	00008 (00007)	(00026)
APSSMWM:	10000 (00008)	
CHAS:	00002 (00027)	(00043)
CHASSA:	00000 (00041)	(00045)
CHASSV:	00031 (00042)	(00104) (00105)
CLMPS:	00010 (00055)	(00066)
CLMPS1:	00000 (00133)	
CLMPS0:	00029 (00167)	
CLPS:	00000 (00060)	(00061)
CSPUSMUS:	02100 (00009)	(00029)
DAYS:	00794 (00100)	(00157) (00167) (00170)
DNFSA:	00020 (00101)	
DNFS1:	00010 (00153)	
DNFS0:	00010 (00144)	
FINST:	00014 (00138)	
FINSD:	00031 (00172)	(00176)
FIXS:	00029 (00063)	(00094)
FWISC:	00000 (00059)	(00061)
G102S:	00100 (00028)	(00113) (00119) (00195)
G102SA:	00106 (00116)	(00191)
G102S1:	00106 (00111)	(00194)
G102S0:	00010 (00120)	(00156)
G102S2:	00070 (00115)	(00195)
HS:	00001 (00013)	(00184) (00185)
IRIS:	00020 (00011)	
IFIRST:	00010 (00149)	(00151)
IFIXS:	00017 (00131)	(00146)
INSS:	00002 (00113)	(00122)
INST:	00005 (00125)	(00127)
INCS1:	00011 (00139)	(00140)
INCS0:	00020 (00173)	(00174)
LIMS1:	00010 (00040)	(00042)
LIMITS:	00013 (00069)	(00076)
LSTISA:	00010 (00074)	(00079)
MSS:	00000 (00014)	(00182) (00183)
MTSA:	00027 (00075)	(00079) (00085)
MTSIN:	00025 (00048)	(00089)
MTSOUT:	00027 (00044)	(00091) (00091)
MTYST:	00026 (00047)	(00089)
OFIXS:	00032 (00155)	(00178)
OFIXS1:	00030 (00184)	(00186)
OUTST:	00023 (00160)	(00162)
PASS61:	00000 (00136)	(00141)

PAGE 9: FOR 255... "MPCHRA(V,U,V)" (COMPLETE DCT HIT ASSIGNMENT)

PASS0:	0002H (00170) (00175)	
SAS3H:	003CF (00016) (00146)	
SAST9:	00420 (00017) (00122)	
SASR4:	0042A (00018) (00133)	
SI7FS:	0006F (00019) (00124)	(00134) (00137) (00144) (00159) (00168) (00171)
START:	00100 (00020) (00034)	
SVTS:	00342 (00015) (00036)	(00017) (00018)
TRP1S:	00400 (00021) (00123)	(00136) (00147) (00158) (00170)
TST6S:	00001 (00046) (00052)	
TST8S:	00014 (00070) (00078)	
WS:	00002 (00022) (00125)	(00139) (00149) (00150) (00160) (00161) (00173)
WWS:	00004 (00023)	

LINE WITH ERRORS: 0 (MAP VERSION 000101.10) F= 0

LINE 1, EXT ORIGINATOR: 18-APR-79

T A B L E O F C O N T E N T S

COMPILED: 03-JUN-80

PAGE 2

PAGE 1: L1NF1.TXT ORIGINATED:18-APR-79

(00001) * L1NF1.TXT ORIGINATED:18-APR-79

A31	00062	32300200	(00065) ?	xx	
A32	00064	33300700	(00066)	SP2	SET "PI HUMPED" FLAG
A33	00066	34300799	(00067)	JED(P1, P2, P3, P4)	WASTE FIRST INPUT
A35	00068	35300001	(00069)	ADD(P1, INC5, T4)	GEN ADDRESS & RESET P1
A36	0006C	00300400	(00090)	JUMP(CUR50)	TOP OF LOOP
A37	0006E	36300001	(00091)	ADD(P1, INC5, T4)	GEN ADDRESS & RESET P1
A38	00070	01300400	(00092)	JUMP(CUR50)	TOP OF LOOP
A39	00072	33300600	(00093)	STOP	CSPU SHUTS IT OFF!
	00074		(00094)	END	

T.

PAGE 52 LIST.FAT OBJECT.FAT:18-APR-79
 0001F1F204-JUN-80

ALPH:	00006 (00021)		
CURS0:	00001 (00053)	000043 (00000)	000002
CURS01:	00014 (00054)	000060	
CURS1:	0001A (00053)	00050	000066
CURS11:	00022 (00057)	00073	
CURS2:	00028 (00066)	00072	00079
CURS01:	00026 (00079)	00084	
CWTF:	00003 (00012)	00011	
DWYS:	00079 (00013)	00014	000046
DWYSM1:	00094 (00013)	00044	000084
DWYS:	00034 (00043)		
FLSTS:	00037 (00084)	00091	
HR:	00008 (00023)	00031	
IRCS:	00001 (00015)	00082	000089
ISPR:	00171 (00020)	00022	
TSR-1:	00170 (00022)	00023	00040 00042 00035 00036
RCV1:	011F4 (00024)	00033	00035
RCV1M1:	011F7 (00034)	00050	00076
RCV1PRC:	011F8 (00035)	00067	
RCV2:	01369 (00025)	00034	00036
RCV2M1:	01364 (00031)	00070	
RCV2PRC:	01409 (00036)	00073	
RSPTS0:	00012 (00064)		
RSPTS1:	00026 (00076)		
SPM1:	0140C (00026)	00028	00040 00031
SPM1M1:	0140E (00026)	00064	
SPM1PRC:	01E4C (00030)	00053	
SPM1PRM:	01E54 (00031)	00049	
SPM2:	01E4F (00027)	00029	00032
SPM2M1:	01E4D (00029)	00057	
SPM2PRC:	01704 (00042)	00060	
SPTS0:	00009 (00049)		
SPTS1:	0000A (00050)		
SPTS2:	00008 (00044)	00048	
TDS1:	00000 (00041)		
ZRUS:	00000 (00016)	00043	00047

FILES WITH ERRORS: 0 (MAP VERSION 000101.10) 1 - 0

3.6.2 Executive Programs

The modules contained in this section are:

GTE 300

IOS

FF2R

APDONE

CSPUIS

T A B L E O F C O N T E N T S

OFFLINE SYMBOLS FOR ARRAY FUNCTION ASSEMBLIES	PAGE 1
ARITHMETIC FUNCTIONS DISPATCH TABLE	PAGE 4
DISPATCH TABLE ENTRIES FOR FFTN, FFTMR, FFTNI, FFTNH	PAGE 14
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APU3-VCOS VECTOR COSINE	PAGE 20
APU3-VFLT	PAGE 25
APS3-V1124	PAGE 26
APS3-V1124A	PAGE 29
APS3-V2134R	PAGE 32
MAP-300 EXTENDED ARRAY FUNCTIONS - PROG. #R30102.03 - MAY 7, 1980	PAGE 35
MUT-IN-PLACE FFT FOR ONE AND TWO DIMENSIONS	PAGE 36
FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS	PAGE 36
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SPECIAL HANDLING FOR WHASE, YHASE, AND UNASE	PAGE 48
SET PENDING SLOTS TO PROPER VALUES	PAGE 50
FFT - AP PROGRAMS	PAGE 53
APU PROGRAMS	PAGE 53
SCRAMBLE AND FIRST RADIX-2 STAGE, FORWARD	PAGE 53
SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD	PAGE 55
SUCCESSIVE RADIX-4 STAGES, FORWARD	PAGE 57
SCRAMBLE AND FIRST RADIX-2 STAGE, REVERSE	PAGE 61
SCRAMBLE AND FIRST RADIX-4 STAGE, REVERSE	PAGE 63
SUCCESSIVE RADIX-4 STAGES, REVERSE	PAGE 65
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CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)	PAGE 72
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SUCCESSIVE RADIX-4 STAGES (INPUT - P0)	PAGE 78
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VM0V	PAGE 81
DEFINE TOP OF MODULE	PAGE 82

(00001) * SNAP-II MAP-100 ARITH. MODULES - PROG. #H30101.03 MAY 7, 1980
 (00002) *
 (00003) * MODIFIED FOR GTF SYLVANIA BY S. TERRACE
 (00004) *
 (00005) *
 (00006) *
 (00007) *
 (00008) *
 (00009) *
 (00010) *
 (00011) *
 (00012) *
 (00013) *
 (00014) *
 (00015) *
 (00016) *
 (00017) *
 (00018) *
 (00019) *
 (00020) *
 (00021) *
 (00022) *
 (00023) *
 (00024) *
 (00025) *
 (00026) *

MODULES INCLUDED:

NAME	PCB #
VFUT	136
VMOV	143
VSMAL	144
VCS	146
FTN	204
FTNA	205
FTIN	206
FTINH	207

! DEFINE SYMBOLS FOR ARRAY FUNCTION ASSEMBLIES

FROM THE SNAP-II EXECUTIVE --- REL. 3.05 --- 5/22/79

000008FH (00027) *
 00000245 (00028) *
 000001FA (00029) *
 00000663 (00030) *
 00000F20 (00031) *
 00000240 (00032) *
 0000024R (00033) *
 000005W2 (00034) *
 0000000C (00035) *
 00000000 (00036) *
 00000010 (00037) *
 0000000A (00038) *
 0000000F (00039) *
 00000009 (00040) *
 00000004 (00041) *
 0001FEC0 (00042) *
 (00043) *
 00000604 (00044) *

MODIFIED FOR NEW START ADDRESS

00000000	(00045)	ACTSAT = SARA
00000001	(00046)	ACTSHA = SARA
00000002	(00047)	ACTSGO = S0
00000003	(00048)	
00000004	(00049)	CLMSGO = S792
00000005	(00050)	CLMSGO = S21FC
00000006	(00051)	
00000007	(00052)	DAYS = S794
00000008	(00053)	
00000009	(00054)	ERRORS = S1AFA
00000010	(00055)	
00000011	(00056)	FTSCHSZ = S79A
00000012	(00057)	FTSCHSZ = S0
00000013	(00058)	FTSCHSZ = S4
00000014	(00059)	FTSCHSZ = S5
00000015	(00060)	FTSCHSZ = S11
00000016	(00061)	FTSCHSZ = S20
00000017	(00062)	
00000018	(00063)	HS = 1
00000019	(00064)	
00000020	(00065)	LOADSAP = S1A7F
00000021	(00066)	LOADSAP = S1A7F
00000022	(00067)	
00000023	(00068)	MSKSLRYT = SFF00
00000024	(00069)	MSKSLRYT = S00FF
00000025	(00070)	MSKSLRYT = S00FF
00000026	(00071)	MSKSLRYT = S00FF
00000027	(00072)	
00000028	(00073)	SHFTLSM5 = S7AF
00000029	(00074)	SVTS = S3H2
00000030	(00075)	SVTSUN1 = S3CE
00000031	(00076)	SVTSUN1 = S3CE
00000032	(00077)	SVTSUN1 = S3CE
00000033	(00078)	SVTSUN1 = S3CE
00000034	(00079)	SVTSUN1 = S3CE
00000035	(00080)	SVTSUN1 = S3CE
00000036	(00081)	SVTSUN1 = S3CE
00000037	(00082)	SVTSUN1 = S3CE
00000038	(00083)	SVTSUN1 = S3CE
00000039	(00084)	SVTSUN1 = S3CE
00000040	(00085)	SVTSUN1 = S3CE
00000041	(00086)	SVTSUN1 = S3CE

FINCT

PAGE 3: SNAP-11 MAP-100 ARITH. MODULES - PROG. #30101.01 MAY 7, 1980

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(00087) *
00000028H SI = TOPSPTR
(00088) *
(00089) *
00288 0010467H ADDR TOPSPTR(,1)
(00090) *
(00091) *
(00092) *
(00093) *
(00094) *
(00095) *
00000003 DEFINE SYMBOLS FOR THIS MODULE
(00096) *
00004000 #M = 3
(00097) *
(00098) *
(00099) *
(00099) *
FJFCT
(00099) *

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(00100) * SPECIAL NOTE: THESE MODULES ARE THE SAME AS RELEASE 2.1 EXCEPT
 (00101) * THE SPECIAL BINDING IN THE PFT'S AND SPECIAL FUNCTIONS HAVE BEEN
 (00102) * MODIFIED FOR RELEASE 3.0
 (00103) *

(00104) *
 (00105) ! ARITHMETIC FUNCTIONS DISPATCH TABLE
 (00106) *

(00107) *
 (00108) * EVERY ARRAY FUNCTION OCCUPIES ONE NODE IN THE TABLE.
 (00109) * THE NODE FOR ARRAY FCR #N WILL BE FOUND AT LOCATION
 (00110) * APTS = 3 * WS * (8N-124)
 (00111) *

(00112) * EACH NODE CONSISTS OF 3 POINTERS AS FOLLOWS:
 (00113) * WORD 1 = ADDR APUSMODULE(R7,1)
 (00114) * WORD 2 = ADDR APSSMODULE(R7,1)
 (00115) * WORD 3 = ADDR CSPUSMODULE(.1,SSSV)

(00116) * IF HIT 0 OF THE FCR CONTROL BITS = 0
 (00117) * NORMAL HITTING OCCURS AND CSPUSMODULE
 (00118) * IS EXECUTED FOR SPECIAL AP-DONE SUPPORT
 (00119) * ELSE CSPUSMODULE IS FOR SPECIAL BINDING
 (00120) * SSSV = SPECIAL SUPPORT SEMAPHORE VALUE
 (00121) *

(00122) *
 (00123) *
 (00124) *

JECT

STANDARD FORMAT FOR ALL ARRAY FCN'S

FCR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	PTR TO NEXT FCR AND FUNCTION LIST FLAG(LSR)	
1	FCR NUMBER	
2	Y (0) AND (4)	SA
3	U (1) AND (9)	SR
4	V (2) AND (10)	SC
5	W (3) AND (11)	SD

OBJECT

(00125) *
 (00126) *
 (00127) *
 (00128) *
 (00129) *
 (00130) *
 (00131) *
 (00132) *
 (00133) *
 (00134) *
 (00135) *
 (00136) *
 (00137) *
 (00138) *
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 (00150) *
 (00151) *
 (00152) *
 (00153) *
 (00154) *
 (00155) *
 (00156) *
 (00157) *
 (00158) *
 (00159) *
 (00160) *
 (00161) *
 (00162) *
 (00163) *
 (00164) *

		BL = ADDRESS		SET PC TO DISPATCH TABLE	
		00000000 (00165)			
		(00166) *			
		(00167) *			
		00000000 (00168)			
		00000000 (00169)			
		(00170) *			
		(00171) *			
U	00000000	00100000	F-000000	ADDR	SADDR(R7,1)
U	00000000	00100000	F-00172	ADDR	S0021S(R7,1)
	00000000	001021FC	(00174)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00175) *		
U	00000000	00100000	F-00173	ADDR	SSURS(R7,1)
U	00000000	00100000	F-00176	ADDR	S0021S(R7,1)
	00000000	001021FC	(00178)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00179) *		
U	00000000	00100000	F-00177	ADDR	SMURS(R7,1)
U	00000000	00100000	F-00180	ADDR	S0021S(R7,1)
	00000000	001021FC	(00182)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00183) *		
U	00000000	00100000	F-00181	ADDR	SOIRS(R7,1)
U	00000000	00100000	F-00184	ADDR	S0021S(R7,1)
	00000000	001021FC	(00186)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00187) *		
	00000000	00100000	(00188)	ADDR	APUSNULL(R7,1)
	00000000	00100000	(00189)	ADDR	APSSNULL(R7,1)
	00000000	001021FC	(00190)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00191) *		
	00000000	00100000	(00192)	ADDR	APUSNULL(R7,1)
	00000000	00100000	(00193)	ADDR	APSSNULL(R7,1)
	00000000	001021FC	(00194)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00195) *		
	00000000	00100000	(00196)	ADDR	APUSNULL(R7,1)
	00000000	00100000	(00197)	ADDR	APSSNULL(R7,1)
	00000000	001021FC	(00198)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00199) *		
	00000000	00100000	(00200)	ADDR	APUSNULL(R7,1)
	00000000	00100000	(00201)	ADDR	APSSNULL(R7,1)
	00000000	001021FC	(00202)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00203) *		
	00000000	00100000	(00204)	ADDR	VFLTS(R7,1)
	00000000	001041FC	(00205)	ADDR	V1124AS(R7,1)
	00000000	001021FC	(00206)	ADDR	CSPUSNOS(,1,0)
	00000000	001021FC	(00207) *		
U	00000000	00100000	F-00185	ADDR	VFIXS(R7,1)

NULL VALUES FOR UNIMPLEMENTED FOR

128 = SCALAR ADD

129 = SCALAR SUBTRACT

130 = SCALAR MULTIPLY

131 = SCALAR DIVIDE

132 = UNASSIGNED

133 = UNASSIGNED

134 = UNASSIGNED

135 = UNASSIGNED

136 = VECTOR FLOAT

137 = VECTOR FIX

U	00920 001F0000 F-0020R	ADDR	V1124RS(R7,1)	
	00922 001021FC (00210)	ADDR	CSPUSNDS(,1,0)	
	00924 001F0000 (00211) *	ADDR	APUSNULL(R7,1)	138 = VECTOR FLOAT (R-RIT)
U	00926 001F0000 F-0020Q	ADDR	V1124CS(R7,1)	
	00928 001021FC (00214)	ADDR	CSPUSNDS(,1,0)	
	0092A 001F0000 F-00213	ADDR	VFIXS(R7,1)	139 = VECTOR FIX (H-HIT)
U	0092C 001F0000 F-00216	ADDR	V1124DS(R7,1)	
	0092F 001021FC (00218)	ADDR	CSPUSNDS(,1,0)	
	00930 001F0000 (00220)	ADDR	APUSNULL(R7,1)	140 = UNASSIGNED
	00932 001F0000 (00221)	ADDR	APSSNULL(R7,1)	
	00934 001021FC (00222)	ADDR	CSPUSNDS(,1,0)	
	00936 001F0000 (00223)	ADDR	APUSNULL(R7,1)	141 = UNASSIGNED
	00938 001F0000 (00224)	ADDR	APSSNULL(R7,1)	
	0093A 001021FC (00225)	ADDR	CSPUSNDS(,1,0)	
	0093C 001F0000 (00226)	ADDR	APUSNULL(R7,1)	142 = UNASSIGNED
	0093E 001F0000 (00227)	ADDR	APSSNULL(R7,1)	
	00940 001021FC (00230)	ADDR	CSPUSNDS(,1,0)	
	00942 001F0000 (00231)	ADDR	APUSNULL(R7,1)	143 = UNASSIGNED
	00944 001F0000 (00232)	ADDR	APSSNULL(R7,1)	
	00946 001021FC (00233)	ADDR	CSPUSNDS(,1,0)	
	00948 001F0000 (00234)	ADDR	APUSNULL(R7,1)	144 = VECTOR SCALAR MULTI-ADD(1 VECTOR)
	0094A 001F4122 (00235)	ADDR	V54A1S(R7,1)	
	0094C 001021FC (00236)	ADDR	V1124S(R7,1)	
	0094E 001F0000 F-00217	ADDR	CSPUSNDS(,1,0)	
U	00950 001F0000 F-00240	ADDR	V54A2S(R7,1)	145 = VECTOR SCALAR MULTI-ADD(2 VECTOR)
	00952 001021FC (00242)	ADDR	V2134AS(R7,1)	
	00954 001F0000 F-00241	ADDR	CSPUSNDS(,1,0)	
U	00956 001F0000 F-00244	ADDR	VMULS(R7,1)	146 = VECTOR MULTIPLY
	00958 001021FC (00246)	ADDR	V2134AS(R7,1)	
	0095A 001F0000 F-00245	ADDR	CSPUSNDS(,1,0)	
U	0095C 001F4122 (00249)	ADDR	VRCPS(R7,1)	147 = VECTOR RECIPROCAL
	0095E 001021FC (00250)	ADDR	V1124S(R7,1)	
	00960 001F0000 (00251) *	ADDR	CSPUSNDS(,1,0)	
		ADDR	APUSNULL(R7,1)	148 = VECTOR DIVIDE

00962	001F0000	(00253)	ADDR	APSSNULL(R7,1)	CURRENTLY NOT IMPLEMENTED
00964	001021FC	(00254)	ADDR	CSPUSNDS(1,0)	
00966	001F0000	(00255)	ADDR	APUSNULL(R7,1)	149 = UNASSIGNED
00968	001F0000	(00256)	ADDR	APSSNULL(R7,1)	
0096A	001021FC	(00257)	ADDR	CSPUSNDS(1,0)	
0096C	001F0000	(00258)	ADDR	APUSNULL(R7,1)	150 = UNASSIGNED
0096E	001F0000	(00259)	ADDR	APSSNULL(R7,1)	
00970	001021FC	(00260)	ADDR	CSPUSNDS(1,0)	
00972	001F0000	(00261)	ADDR	APUSNULL(R7,1)	151 = UNASSIGNED
00974	001F0000	(00262)	ADDR	APSSNULL(R7,1)	
00976	001021FC	(00263)	ADDR	CSPUSNDS(1,0)	
00978	001F0000	(00264)	ADDR	APUSNULL(R7,1)	152 = VECTOR ABSOLUTE
0097A	001F4122	(00265)	ADDR	VSAHS(R7,1)	
0097C	001021FC	(00266)	ADDR	V1124S(R7,1)	
0097E	001F0000	(00267)	ADDR	CSPUSNDS(1,0)	
00980	001F4122	(00268)	ADDR	VSAHS(R7,1)	153 = VECTOR ABSOLUTE SQUARED
00982	001021FC	(00269)	ADDR	V1124S(R7,1)	
00984	001F0000	(00270)	ADDR	CSPUSNDS(1,0)	
00986	001F4122	(00271)	ADDR	VSAHS(R7,1)	154 = VECTOR SQUARE
00988	001021FC	(00272)	ADDR	V1124S(R7,1)	
0098A	001F0000	(00273)	ADDR	CSPUSNDS(1,0)	
0098C	001F0000	(00274)	ADDR	VSAHS(R7,1)	155 = VECTOR MAGNITUDE SQUARED
0098E	001021FC	(00275)	ADDR	V1124S(R7,1)	
00990	001F0000	(00276)	ADDR	CSPUSNDS(1,0)	
00992	001F0000	(00277)	ADDR	VSAHS(R7,1)	156 = VECTOR MAGNITUDE
00994	001021FC	(00278)	ADDR	V1124S(R7,1)	
00996	001F0000	(00279)	ADDR	CSPUSNDS(1,0)	
00998	001F0000	(00280)	ADDR	VSAHS(R7,1)	157 = VECTOR LOG
0099A	001021FC	(00281)	ADDR	V1124S(R7,1)	
0099C	001F0000	(00282)	ADDR	CSPUSNDS(1,0)	
0099E	001F0000	(00283)	ADDR	VSAHS(R7,1)	158 = UNASSIGNED
009A0	001021FC	(00284)	ADDR	V1124S(R7,1)	
009A2	001F0000	(00285)	ADDR	CSPUSNDS(1,0)	
009A4	001F0000	(00286)	ADDR	VSAHS(R7,1)	159 = UNASSIGNED
009A6	001021FC	(00287)	ADDR	V1124S(R7,1)	
009A8	001F0000	(00288)	ADDR	CSPUSNDS(1,0)	
009AA	001F0000	(00289)	ADDR	VSAHS(R7,1)	
009AC	001021FC	(00290)	ADDR	V1124S(R7,1)	
009AE	001F0000	(00291)	ADDR	CSPUSNDS(1,0)	
009B0	001F0000	(00292)	ADDR	VSAHS(R7,1)	
009B2	001021FC	(00293)	ADDR	V1124S(R7,1)	
009B4	001F0000	(00294)	ADDR	CSPUSNDS(1,0)	
009B6	001021FC	(00295)	ADDR	VSAHS(R7,1)	
009B8	001F0000	(00296)	ADDR	V1124S(R7,1)	

009A4	001F0000	(00297)	ADDR	APSSMULT(R7,1)	
009A6	001021FC	(00298)	ADDR	CSPUSNUS(,1,0)	
U	009AB	001F0000 F-00299	ADDR	VCLIPS(R7,1)	160 = VECTOR CLIP
	009AA	001F4122 (00301)	ADDR	V1124S(R7,1)	
	009AC	001021FC (00302)	ADDR	CSPUSNUS(,1,0)	
U	009AE	001F0000 F-00303	ADDR	VTURNS(R7,1)	161 = VECTOR THRESHOLD
	009B0	001F4122 (00305)	ADDR	V1124S(R7,1)	
	009B2	001021FC (00306)	ADDR	CSPUSNUS(,1,0)	
U	009B4	001F0000 F-00307	ADDR	VLIMITS(R7,1)	162 = VECTOR LIMIT
	009B6	001F4122 (00309)	ADDR	V1124S(R7,1)	
	009B8	001021FC (00310)	ADDR	CSPUSNUS(,1,0)	
U	009BA	001F0000 F-00311	ADDR	VCOMPS(R7,1)	163 = VECTOR COMPARE
U	009BC	001F0000 F-00312	ADDR	V2134S(R7,1)	
	009BE	001021FC (00314)	ADDR	CSPUSNUS(,1,0)	
U	009C0	001F0000 F-00315	ADDR	VTAGS(R7,1)	164 = VECTOR TAG
	009C2	001F4122 (00317)	ADDR	V1124S(R7,1)	
	009C4	001021FC (00318)	ADDR	CSPUSNUS(,1,0)	
U	009C6	001F0000 (00319)	ADDR	APUSMULT(R7,1)	165 = UNASSIGNED
	009C8	001F0000 (00320)	ADDR	APSSMULT(R7,1)	
	009CA	001021FC (00321)	ADDR	CSPUSNUS(,1,0)	
U	009CC	001F0000 (00322)	ADDR	APUSMULT(R7,1)	166 = UNASSIGNED
	009CE	001F0000 (00323)	ADDR	APSSMULT(R7,1)	
	009D0	001021FC (00324)	ADDR	CSPUSNUS(,1,0)	
U	009D2	001F0000 (00325)	ADDR	APUSMULT(R7,1)	167 = UNASSIGNED
	009D4	001F0000 (00326)	ADDR	APSSMULT(R7,1)	
	009D6	001021FC (00327)	ADDR	CSPUSNUS(,1,0)	
U	009D8	001F0000 (00328)	ADDR	SSUMS(R7,1)	168 = SCALAR SUM
U	009DA	001F0000 F-00329	ADDR	S1011S(R7,1)	
	009DC	001021FC (00330)	ADDR	CSPUSNUS(,1,0)	
U	009DE	001F0000 (00331)	ADDR	SSUMS(R7,1)	169 = SCALAR SUM OF ABSOLUTES
	009DF	001F0000 F-00332	ADDR	S1011S(R7,1)	
U	009F0	001F0000 F-00333	ADDR	CSPUSNUS(,1,0)	
	009F2	001021FC (00334)	ADDR	SSUMS(R7,1)	170 = SCALAR SUM OF SQUARES
U	009F4	001F0000 F-00335	ADDR	S1011S(R7,1)	

U	009F6 001F0000 F-00340	ADDR	S1011S(R7,1)	
	009F8 001021FC (00342)	ADDR	CSPUSNDS(,1,0)	
U	009FA 001F0000 F-00341	ADDR	SMAXS(R7,1)	171 = SCALAR MAXIMUM
U	009FC 001F0000 F-00344	ADDR	S1002S(R7,1)	
	009FE 001021FC (00346)	ADDR	CSPUSNDS(,1,0)	
U	009F0 001F0000 F-00345	ADDR	SMAYSHS(R7,1)	172 = SCALAR MAXIMUM ABSOLUTE
U	009F2 001F0000 F-00348	ADDR	S1007S(R7,1)	
	009F4 001021FC (00350)	ADDR	CSPUSNDS(,1,0)	
U	009F6 001F0000 F-00349	ADDR	S00TS(R7,1)	173 = SCALAR DOT PRODUCT
U	009F8 001F0000 F-00352	ADDR	S2011S(R7,1)	
	009FA 001021FC (00354)	ADDR	CSPUSNDS(,1,0)	
	009FC 001F0000 (00355)	ADDR	APUSNULI(R7,1)	174 = UNASSIGNED
	009FE 001F0000 (00356)	ADDR	APSSNULI(R7,1)	
	00A00 001021FC (00357)	ADDR	CSPUSNDS(,1,0)	
	00A02 001F0000 (00359)	ADDR	APUSNULI(R7,1)	175 = UNASSIGNED
	00A04 001F0000 (00360)	ADDR	APSSNULI(R7,1)	
	00A06 001021FC (00361)	ADDR	CSPUSNDS(,1,0)	
	00A08 001F0000 (00362)	ADDR	CVNULS(R7,1)	176 = COMPLEX VECTOR MULTIPLY
	00A0A 001021FC (00363)	ADDR	C2124AS(R7,1)	
	00A0C 001021FC (00364)	ADDR	CSPUSNDS(,1,0)	
U	00A0E 001F0000 F-00365	ADDR	CCVMULS(R7,1)	177 = COMPLEX CONJ. VECTOR MULTIPLY
U	00A10 001F0000 F-00369	ADDR	C2124AS(R7,1)	
	00A12 001021FC (00370)	ADDR	CSPUSNDS(,1,0)	
	00A14 001F0000 F-00369	ADDR	CVRCPS(R7,1)	178 = COMPLEX VECTOR RECIPICAL
U	00A16 001F0000 E-00372	ADDR	C1124AS(R7,1)	
	00A18 001021FC (00374)	ADDR	CSPUSNDS(,1,0)	
	00A1A 001F0000 (00375)	ADDR	CPHAPS(R7,1)	179 = COMPLEX POLAR CONVERSION
U	00A1C 001F0000 F-00373	ADDR	C1124AS(R7,1)	
	00A1E 001021FC (00374)	ADDR	CSPUSNDS(,1,0)	
	00A20 001F0000 (00379)	ADDR	APUSNULI(R7,1)	180 = COMPLEX RECTANGULAR CONVERSION
	00A22 001F0000 (00381)	ADDR	APSSNULI(R7,1)	CURRENTLY NOT IMPLEMENTED
	00A24 001021FC (00382)	ADDR	CSPUSNDS(,1,0)	
	00A26 001F0000 (00383)	ADDR	APUSNULI(R7,1)	181 = UNASSIGNED

00A2R	001F0000	(003M5)	ADDR	APUSNULL(R7,1)	
00A2A	001021FC	(003M6)	ADDR	CSPUSNDS(,1,0)	
U	00A2C	001F0000 F-00377	ADDR	CXNULLS(R7,1)	1R2 = COMPLEX EXPONENTIAL MULTIPLY
U	00A2F	001F0000 F-0038R	ADDR	C2120S(R7,1)	
	00A30	001021FC (00390)	ADDR	CSPUSNDS(,1,0)	
U	00A32	001F0000 F-00389	ADDR	CXNULLS(R7,1)	1R3 = COMPLEX EXPON. MULTIPLY BY REAL
U	00A34	001F0000 F-00392	ADDR	C1120S(R7,1)	
	00A36	001021FC (00394)	ADDR	CSPUSNDS(,1,0)	
U	00A3R	001F0000 F-00393	ADDR	VPOLYS(R7,1)	1R4 = VECTOR POLYNOMIAL
U	00A3A	001F0000 F-00396	ADDR	V2116AS(R7,1)	
U	00A3C	00100000 F-00397	ADDR	SHMSVPLY(,1,0)	
	00A3F	001F0000 F-0039R	ADDR	VRAMPS(R7,1)	1R5 = VECTOR RAMP
	00A40	001F4122 (00401)	ADDR	V1124S(R7,1)	
	00A42	001021FC (00402)	ADDR	CSPUSNDS(,1,0)	
	00A44	001F4020 (00403)	ADDR	VCUSS(R7,1)	1R6 = VECTOR COSINE
	00A46	001F41F6 (00405)	ADDR	V2134RS(R7,1)	
	00A4R	001021FC (00406)	ADDR	CSPUSNDS(,1,0)	
U	00A4A	001F0000 F-00400	ADDR	VPOWS(R7,1)	1R7 = VECTOR POWER
U	00A4C	001F0000 F-0040R	ADDR	V1112AS(R7,1)	
	00A4F	001021FC (00410)	ADDR	CSPUSNDS(,1,0)	
	00A50	001F0000 (00411)	ADDR	APUSNULL(R7,1)	1R8 = UNASSIGNED
	00A52	001E0000 (00413)	ADDR	APSSNULL(R7,1)	
	00A54	001021FC (00414)	ADDR	CSPUSNDS(,1,0)	
	00A56	001F0000 (00415)	ADDR	APUSNULL(R7,1)	1R9 = UNASSIGNED
	00A5R	001F0000 (00416)	ADDR	APSSNULL(R7,1)	
	00A5A	001021FC (00418)	ADDR	CSPUSNDS(,1,0)	
	00A5C	001F0000 (00419)	ADDR	APUSNULL(R7,1)	1R9 = UNASSIGNED
	00A5F	001E0000 (00421)	ADDR	APSSNULL(R7,1)	
	00A60	001021FC (00422)	ADDR	CSPUSNDS(,1,0)	
	00A62	001F0000 (00423)	ADDR	APUSNULL(R7,1)	1R1 = UNASSIGNED
	00A64	001F0000 (00424)	ADDR	APSSNULL(R7,1)	
	00A66	001021FC (00425)	ADDR	CSPUSNDS(,1,0)	
U	00A6R	001F0000 F-00409	ADDR	DDIFFS(R7,1)	1R2 = DISCRETE DIFFERENTIATION

U	00A6A	001F0000	F-0042H	ADDR	01113S(R7,1)	
	00A6C	001021FC	(00430)	ADDR	CSPUSNOS(,1,0)	
U	00A6E	001F0000	F-00429	ADDR	01113S(R7,1)	193 = DISCRETE INTEGRATION
U	00A70	001F0000	F-00432	ADDR	01113S(R7,1)	
	00A72	001021FC	(00434)	ADDR	CSPUSNOS(,1,0)	
U	00A74	001F0000	F-00435	ADDR	011127S(R7,1)	194 = DISCRETE FILTER(2-POLFS, 2-ZEROS)
U	00A76	001F0000	F-00436	ADDR	01116S(R7,1)	
	00A78	001021FC	(00438)	ADDR	CSPUSNOS(,1,0)	
U	00A7A	001F0000	F-00439	ADDR	011127S(R7,1)	195 = DISCRETE CONVOLUTION MULTIPLY
U	00A7C	001F0000	F-00440	ADDR	01116S(R7,1)	
U	00A7E	00100000	F-00441	ADDR	SMSDCVM(,1,0)	
	00A80	001F0000	(00443)	ADDR	APUSNULL(R7,1)	196 = UNASSIGNED
	00A82	001F0000	(00445)	ADDR	APSSNULL(R7,1)	
	00A84	001021FC	(00446)	ADDR	CSPUSNOS(,1,0)	
U	00A86	001F0000	(00447)	ADDR	APUSNULL(R7,1)	197 = UNASSIGNED
U	00A88	001F0000	(00449)	ADDR	APSSNULL(R7,1)	
	00A8A	001021FC	(00450)	ADDR	CSPUSNOS(,1,0)	
U	00A8C	001F0000	(00451)	ADDR	APUSNULL(R7,1)	198 = UNASSIGNED
	00A8E	001F0000	(00453)	ADDR	APSSNULL(R7,1)	
	00A90	001021FC	(00454)	ADDR	CSPUSNOS(,1,0)	
U	00A92	001F0000	(00455)	ADDR	APUSNULL(R7,1)	199 = UNASSIGNED
U	00A94	001F0000	(00457)	ADDR	APSSNULL(R7,1)	
	00A96	001021FC	(00458)	ADDR	CSPUSNOS(,1,0)	
U	00A98	001F0000	F-00459	ADDR	FSCRS(R7,1)	200 = FOURIER SCRAMBLE
U	00A9A	001F0000	F-00460	ADDR	F11CS(R7,1)	
	00A9C	001021FC	(00462)	ADDR	CSPUSNOS(,1,0)	
U	00A9E	001F0000	F-00463	ADDR	FRX4S(R7,1)	201 = FOURIER RADIX-4 TRANSFORM
U	00AA0	001F0000	F-00464	ADDR	F21HS(R7,1)	
	00AA2	001021FC	(00466)	ADDR	CSPUSNOS(,1,0)	
U	00AA4	001F0000	F-00467	ADDR	FFOSS(R7,1)	202 = FOURIER EVEN-ODD SEPERATE
U	00AA6	001F0000	F-00468	ADDR	F11AS(R7,1)	
	00AA8	001021FC	(00470)	ADDR	CSPUSNOS(,1,0)	
U	00AAA	001F0000	F-00471	ADDR	FSC4S(R7,1)	203 = FOURIER SCRAMBLE - EVEN PWRS OF 2

00AAC 001F0000 F-00472	ADDR	F11CS(R7,1)	
00AAE 001021FC (00474)	ADDR	CSPUSNDS(,1,0)	
00AR0 001F0000 (00475) *			
00AR0 001F0000 (00476)	ADDR	APUSNULL(R7,1)	204 = UNASSIGNED
00AR2 001F0000 (00477)	ADDR	AI SNUL(R7,1)	
00AR4 001021FC (00478)	ADDR	CSPUSNDS(,1,0)	
00AR6 001F0000 (00479) *			
00AR6 001F0000 (00480)	ADDR	APUSNULL(R7,1)	205 = UNASSIGNED
00AR8 001F0000 (00481)	ADDR	APSSNULL(R7,1)	
00AHA 001021FC (00482)	ADDR	CSPUSNDS(,1,0)	
00AHC 001F0000 (00483) *			
00AHC 001F0000 (00484)	ADDR	APUSNULL(R7,1)	206 = UNASSIGNED
00AHF 001F0000 (00485)	ADDR	APSSNULL(R7,1)	
00AC0 001021FC (00486)	ADDR	CSPUSNDS(,1,0)	
00AC2 001F0000 (00487) *			
00AC2 001F0000 (00488)	ADDR	APUSNULL(R7,1)	207 = UNASSIGNED
00AC4 001F0000 (00489)	ADDR	APSSNULL(R7,1)	
00AC6 001021FC (00490)	ADDR	CSPUSNDS(,1,0)	
00AC8 001F0000 (00491) *			
00AC8 001F0000 (00492)	ADDR	APUSNULL(R7,1)	208 = FORWARD FFT(COMPLEX-COMPLEX)
00ACA 001F0000 (00493)	ADDR	APSSNULL(R7,1)	
00ACC 00100000 F-00473	ADDR	FF3SSSM(,1,0)	
00ACF 001F0000 (00495) *			
00ACF 001F0000 (00496)	ADDR	APUSNULL(R7,1)	209 = INVERSE FFT(COMPLEX-COMPLEX)
00AD0 001F0000 (00497)	ADDR	APSSNULL(R7,1)	
00AD2 00100000 F-00494	ADDR	1FF3SSSM(,1,0)	
00AD4 001F0000 (00500)	ADDR	APUSNULL(R7,1)	210 = FORWARD FFT(REAL-COMPLEX)
00AD6 001F0000 (00501)	ADDR	APSSNULL(R7,1)	
00AD8 00100000 F-00498	ADDR	RF3SSSM(,1,0)	
00ADA 001F0000 (00503) *			
00ADA 001F0000 (00504)	ADDR	APUSNULL(R7,1)	211 = INVERSE FFT(REAL-COMPLEX)
00ADC 001F0000 (00505)	ADDR	APSSNULL(R7,1)	
00ADE 00100000 F-00502	ADDR	1RF3SSSM(,1,0)	
00AE0 001F0000 (00507) *			
00AE0 001F0000 (00508)	ADDR	APUSNULL(R7,1)	212 = UNASSIGNED
00AE2 001F0000 (00509)	ADDR	APSSNULL(R7,1)	
00AF4 001021FC (00510)	ADDR	CSPUSNDS(,1,0)	
00AE6 001F0000 (00511) *			
00AE6 001F0000 (00512)	ADDR	APUSNULL(R7,1)	213 = UNASSIGNED
00AER 001F0000 (00513)	ADDR	APSSNULL(R7,1)	
00AEA 001021FC (00514)	ADDR	CSPUSNDS(,1,0)	
00AEC 001F0000 (00515) *			
00AEC 001F0000 (00516)	ADDR	APUSNULL(R7,1)	214 = UNASSIGNED

00AF0 001F0000 (00517)	ADDR	APUSNUL(L(R7,1)	
00AF0 001021FC (00518)	ADDR	CSPUSNOS(,1,0)	
00AF2 001F0000 (00519) *			215 = UNASSIGNED
00AF2 001F0000 (00520)	ADDR	APUSNUL(L(R7,1)	
00AF4 001F0000 (00521)	ADDR	APUSNUL(L(R7,1)	
00AF6 001021FC (00522)	ADDR	CSPUSNOS(,1,0)	
00AF6 001021FC (00523) *			216 = UNASSIGNED
00AF8 001F0000 (00524)	ADDR	APUSNUL(L(R7,1)	
00AFA 001F0000 (00525)	ADDR	APUSNUL(L(R7,1)	
00AFC 001021FC (00526)	ADDR	CSPUSNOS(,1,0)	
00AFC 001021FC (00527) *			217 = UNASSIGNED
00AF0 001F0000 (00528)	ADDR	APUSNUL(L(R7,1)	
00H00 001F0000 (00529)	ADDR	APUSNUL(L(R7,1)	
00H02 001021FC (00530)	ADDR	CSPUSNOS(,1,0)	
00H02 001021FC (00531) *			218 = UNASSIGNED
00H04 001F0000 (00532)	ADDR	APUSNUL(L(R7,1)	
00H06 001F0000 (00533)	ADDR	APUSNUL(L(R7,1)	
00H08 001021FC (00534)	ADDR	CSPUSNOS(,1,0)	
00H08 001021FC (00535) *			219 = UNASSIGNED
00H0A 001F0000 (00536)	ADDR	APUSNUL(L(R7,1)	
00H0C 001F0000 (00537)	ADDR	APUSNUL(L(R7,1)	
00H0E 001021FC (00538)	ADDR	CSPUSNOS(,1,0)	
00H0E 001021FC (00539) *			220 = UNASSIGNED
00H10 001F0000 (00540)	ADDR	APUSNUL(L(R7,1)	
00H12 001F0000 (00541)	ADDR	APUSNUL(L(R7,1)	
00H14 001021FC (00542)	ADDR	CSPUSNOS(,1,0)	
00H14 001021FC (00543) *			221 = UNASSIGNED
00H16 001F0000 (00544)	ADDR	APUSNUL(L(R7,1)	
00H18 001F0000 (00545)	ADDR	APUSNUL(L(R7,1)	
00H1A 001021FC (00546)	ADDR	CSPUSNOS(,1,0)	
00H1A 001021FC (00547) *			222 = UNASSIGNED
00H1C 001F0000 (00548)	ADDR	APUSNUL(L(R7,1)	
00H1E 001F0000 (00549)	ADDR	APUSNUL(L(R7,1)	
00H20 001021FC (00550)	ADDR	CSPUSNOS(,1,0)	
00H20 001021FC (00551) *			223 = UNASSIGNED
00H22 001F0000 (00552)	ADDR	APUSNUL(L(R7,1)	
00H24 001F0000 (00553)	ADDR	APUSNUL(L(R7,1)	
00H26 001021FC (00554)	ADDR	CSPUSNOS(,1,0)	
00H26 001021FC (00555) *			224 = UNASSIGNED
00H28 001F0000 (00556)	ADDR	APUSNUL(L(R7,1)	
00H2A 001F0000 (00557)	ADDR	APUSNUL(L(R7,1)	
00H2C 001021FC (00558)	ADDR	CSPUSNOS(,1,0)	
00H2C 001021FC (00559) *			225 = UNASSIGNED
00H2E 001F0000 (00560)	ADDR	APUSNUL(L(R7,1)	

00R 0 001F0000 (00561)	ADDR	APUSNUL.(R7,1)	
00R12 001021FC (00562)	ADDR	CSPUSNUS(,1,0)	
00R14 001F0000 (00563) *	ADDR	APUSNUL.(R7,1)	226 = UNASSIGNED
00R16 001F0000 (00565)	ADDR	APUSNUL.(R7,1)	
00R18 001021FC (00566)	ADDR	CSPUSNUS(,1,0)	
00R1A 001F0000 (00567) *	ADDR	APUSNUL.(R7,1)	227 = UNASSIGNED
00R1C 001F0000 (00569)	ADDR	APUSNUL.(R7,1)	
00R1E 001021FC (00570)	ADDR	CSPUSNUS(,1,0)	
00R1F 001F0000 (00571) *	ADDR	APUSNUL.(R7,1)	228 = UNASSIGNED
00R20 001F0000 (00572)	ADDR	APUSNUL.(R7,1)	
00R22 001F0000 (00573)	ADDR	CSPUSNUS(,1,0)	
00R24 001021FC (00574) *	ADDR	APUSNUL.(R7,1)	229 = UNASSIGNED
00R26 001F0000 (00575)	ADDR	APUSNUL.(R7,1)	
00R28 001F0000 (00576)	ADDR	APUSNUL.(R7,1)	
00R2A 001F0000 (00577)	ADDR	CSPUSNUS(,1,0)	
00R2C 001021FC (00578) *	ADDR	APUSNUL.(R7,1)	230 = UNASSIGNED
00R2E 001F0000 (00579)	ADDR	APUSNUL.(R7,1)	
00R30 001F0000 (00580)	ADDR	APUSNUL.(R7,1)	
00R32 001F0000 (00581)	ADDR	CSPUSNUS(,1,0)	
00R34 001021FC (00582) *	ADDR	APUSNUL.(R7,1)	231 = UNASSIGNED
00R36 001F0000 (00583)	ADDR	APUSNUL.(R7,1)	
00R38 001F0000 (00584)	ADDR	APUSNUL.(R7,1)	
00R3A 001F0000 (00585)	ADDR	CSPUSNUS(,1,0)	
00R3C 001021FC (00586) *	ADDR	APUSNUL.(R7,1)	232 = UNASSIGNED
00R3E 001F0000 (00587)	ADDR	APUSNUL.(R7,1)	
00R40 001F0000 (00588)	ADDR	APUSNUL.(R7,1)	
00R42 001F0000 (00589)	ADDR	CSPUSNUS(,1,0)	
00R44 001021FC (00590) *	ADDR	APUSNUL.(R7,1)	233 = UNASSIGNED
00R46 001F0000 (00591)	ADDR	APUSNUL.(R7,1)	
00R48 001F0000 (00592)	ADDR	APUSNUL.(R7,1)	
00R4A 001F0000 (00593)	ADDR	CSPUSNUS(,1,0)	
00R4C 001021FC (00594) *	ADDR	APUSNUL.(R7,1)	234 = UNASSIGNED
00R4E 001F0000 (00595)	ADDR	APUSNUL.(R7,1)	
00R50 001F0000 (00596)	ADDR	APUSNUL.(R7,1)	
00R52 001F0000 (00597)	ADDR	CSPUSNUS(,1,0)	
00R54 001021FC (00598) *	ADDR	APUSNUL.(R7,1)	235 = UNASSIGNED
00R56 001F0000 (00599)	ADDR	APUSNUL.(R7,1)	
00R58 001F0000 (00600)	ADDR	APUSNUL.(R7,1)	
00R5A 001F0000 (00601)	ADDR	CSPUSNUS(,1,0)	
00R5C 001021FC (00602) *	ADDR	APUSNUL.(R7,1)	236 = UNASSIGNED
00R5E 001F0000 (00603)	ADDR	APUSNUL.(R7,1)	
00R60 001F0000 (00604)	ADDR	APUSNUL.(R7,1)	

00R72 001F0000 (00605)	ADDR	APSSNULL(R7,1)	
00R74 001021FC (00606)	ADDR	CSPUSNDS(,1,0)	
00R76 001F0000 (00608)	ADDR	APUSNULL(R7,1)	237 = UNASSIGNED
00R78 001F0000 (00609)	ADDR	APSSNULL(R7,1)	
00R7A 001021FC (00610)	ADDR	CSPUSNDS(,1,0)	
00R7C 001F0000 (00611)	ADDR	APUSNULL(R7,1)	238 = UNASSIGNED
00R7E 001F0000 (00612)	ADDR	APSSNULL(R7,1)	
00R80 001021FC (00613)	ADDR	CSPUSNDS(,1,0)	
00R82 001F0000 (00614)	ADDR	APUSNULL(R7,1)	239 = UNASSIGNED
00R84 001F0000 (00615)	ADDR	APSSNULL(R7,1)	
00R86 001021FC (00616)	ADDR	CSPUSNDS(,1,0)	
00R88 001F0000 (00617)	ADDR	APUSNULL(R7,1)	240 = UNASSIGNED
00R8A 001F0000 (00618)	ADDR	APSSNULL(R7,1)	
00R8C 001021FC (00619)	ADDR	CSPUSNDS(,1,0)	
00R8E 001F0000 (00620)	ADDR	APUSNULL(R7,1)	241 = UNASSIGNED
00R90 001F0000 (00621)	ADDR	APSSNULL(R7,1)	
00R92 001021FC (00622)	ADDR	CSPUSNDS(,1,0)	
00R94 001F0000 (00623)	ADDR	APUSNULL(R7,1)	242 = UNASSIGNED
00R96 001F0000 (00624)	ADDR	APSSNULL(R7,1)	
00R98 001021FC (00625)	ADDR	CSPUSNDS(,1,0)	
00R9A 001F0000 (00626)	ADDR	APUSNULL(R7,1)	243 = UNASSIGNED
00R9C 001F0000 (00627)	ADDR	APSSNULL(R7,1)	
00R9E 001021FC (00628)	ADDR	CSPUSNDS(,1,0)	
00RA0 001F0000 (00629)	ADDR	APUSNULL(R7,1)	244 = UNASSIGNED
00RA2 001F0000 (00630)	ADDR	APSSNULL(R7,1)	
00RA4 001021FC (00631)	ADDR	CSPUSNDS(,1,0)	
00RA6 001F0000 (00632)	ADDR	APUSNULL(R7,1)	245 = UNASSIGNED
00RA8 001F0000 (00633)	ADDR	APSSNULL(R7,1)	
00RAA 001021FC (00634)	ADDR	CSPUSNDS(,1,0)	
00RAC 001F0000 (00635)	ADDR	APUSNULL(R7,1)	246 = UNASSIGNED
00RAE 001F0000 (00636)	ADDR	APSSNULL(R7,1)	
00RA8 001021FC (00637)	ADDR	CSPUSNDS(,1,0)	
00RAA 001F0000 (00638)	ADDR	APUSNULL(R7,1)	247 = UNASSIGNED
00RAE 001F0000 (00639)	ADDR	APSSNULL(R7,1)	
00RAC 001F0000 (00640)	ADDR	CSPUSNDS(,1,0)	
00RAE 001021FC (00641)	ADDR	APUSNULL(R7,1)	
00RAA 001F0000 (00642)	ADDR	APSSNULL(R7,1)	
00RAE 001F0000 (00643)	ADDR	CSPUSNDS(,1,0)	
00RA8 001021FC (00644)	ADDR	APUSNULL(R7,1)	
00RAA 001F0000 (00645)	ADDR	APSSNULL(R7,1)	
00RAE 001021FC (00646)	ADDR	CSPUSNDS(,1,0)	
00RA8 001F0000 (00647)	ADDR	APUSNULL(R7,1)	
00RAA 001021FC (00648)	ADDR	APSSNULL(R7,1)	

00RM4 001F0000 (00649)	ADDR	APUSNULL(R7,1)	
00RM6 001021FC (00650)	ADDR	CSPUSNUS(,1,0)	
00RM8 001F0000 (00651) *			
00RMA 001F0000 (00652)	ADDR	APUSNULL(R7,1)	248 = UNASSIGNED
00RMB 001F0000 (00653)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00654)	ADDR	CSPUSNUS(,1,0)	
00RME 001F0000 (00655) *			
00RMF 001F0000 (00656)	ADDR	APUSNULL(R7,1)	249 = UNASSIGNED
00RMC 001F0000 (00657)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00658)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00659) *			
00RMC 001F0000 (00660)	ADDR	APUSNULL(R7,1)	250 = UNASSIGNED
00RMC 001F0000 (00661)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00662)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00663) *			
00RMC 001F0000 (00664)	ADDR	APUSNULL(R7,1)	251 = UNASSIGNED
00RMC 001F0000 (00665)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00666)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00667) *			
00RMC 001F0000 (00668)	ADDR	APUSNULL(R7,1)	252 = UNASSIGNED
00RMC 001F0000 (00669)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00670)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00671) *			
00RMC 001F0000 (00672)	ADDR	APUSNULL(R7,1)	253 = UNASSIGNED
00RMC 001F0000 (00673)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00674)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00675) *			
00RMC 001F0000 (00676)	ADDR	APUSNULL(R7,1)	254 = UNASSIGNED
00RMC 001F0000 (00677)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00678)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00679) *			
00RMC 001F0000 (00680)	ADDR	APUSNULL(R7,1)	255 = UNASSIGNED
00RMC 001F0000 (00681)	ADDR	APUSNULL(R7,1)	
00RMC 001021FC (00682)	ADDR	CSPUSNUS(,1,0)	
00RMC 001F0000 (00683) *			
00RMC 001F0000 (00684) #1=AFDTSORG+3*2*(143-128)			FCH #143
00RMC 001F0000 (00685)	ADDR	VMVS(R7,1)	
00RMC 001F0000 (00686)	ADDR	V1124S(R7,1)	
00RMC 001021FC (00687) *	ADDR	CSPUSNUS(,1,0)	

PAGE 20: SNAP-II MAP-300 ARITH. MODULES - PROG. 8830101.03 MAY 7, 1980
APU3-VCUS VECTOR COSINE

	APU3-VCUS	VECTOR COSINE
	(00761) *	HINDS TO APS3-V2134H
	(00762) *	FO. V(K) = U(K) * COS(A*V(K)+H*K+C)
	(00763) *	
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(00R05) *
(00R06) *CONTINUE WITH A7=20 WHERE -1<2H<0
(00R07) *
A11 04042 08F608F6 MOV(10A,A6) A6=C
A12 04044 02C002C0 R(A6) W=C
(00R10) *
(00R11) * DECREMENTATION LOOP ON C
(00R12) *
(00R13) *
A13 04046 08960896 MOV(R,A6) A6=C
A14 04048 911F0017 JUMPS(VCS4,T1) GO TO #4 OF C<0
(00R15) *
A15 0404A 40C040C0 SUB(A6,A5) W=C-1
A16 0404C 10000013 JUMP(VCS3) REPEAT DECM PROCESS
(00R18) *
(00R19) * INCREMENTATION LOOP ON C
(00R20) *
(00R21) *
A17 0404E 45D645D6 MOV(A6),ADD(A6,A5) A6=C, R=C+1
A18 04050 08R008R0 MOV(R,MUL) COMPUTE T1 FLAG UPDATE
A19 04052 911F0017 JUMPS(VCS4,T1) REPEAT IF C+1 NEGATIVE
(00R24) *
(00R25) * CONTINUE FOR A6=C, WHERE -1<C<0
(00R26) *
(00R27) *
A1A 0405A 4FC040C0 SUB(A6,A7)\SUB(A6,A0) R=-1<C-2H<1;R=-1<C-R<1
A1B 0405B 5D065D06 MOV(A6),ADD(T(A6,A5) A6=C-2H,C-H;R=0<C-2R<1,R=0<C-R<1
A1C 0405H 08960896 MOV(R,A6) A6=0<C-2H<1,0<C-R<1;
(00R30) *
(00R31) *
(00R32) * END OF INITIALIZATION*
(00R33) *
(00R34) *
(00R35) * INPUT/OUTPUT PARAMETERS AND COMPUTE ARGUMENT OF COSINE
(00R36) * ENTER LOOP WITH K=0
(00R37) *
A1D 0405A 47C047C0 VCS6 MOV(M0),ADD(A6,A7) M0=R23'; R1=(4K+1)R+C
A1E 0405C 64326432 MOV(A2),MUL(M0,M5) A2=P12'; P13'=R23'+U(4K-4), R23'+U(4K-3)
A1F 0405E 08F608F6 MOV(10A,M7) M7=A
A20 04060 5D105D10 MOV(A0),ADD(T(A0,A5) A0=R1, R2=H1+T(1)=0<-((4KK)R+C),((4K+1)R+C)<1
A21 04062 08F608F6 MOV(10A,M3) \ NOP M3=V(2K)
A22 0406A 000008F6 NOP \ MOV(10A,M3) M3=V(4K+1)
A23 0406B 47304730 MOV(A0),ADD(A1,A2) A0=R2,R24'=C0+P12'
A24 0406H 85F085F0 MOV(M5),MUL(M1,M7) M5=P13'; P1=V(4K)*A,V(4K+1)*A
A25 0406A 470C470C MOV(M4),ADD(A0,A7) M4=R24', R3=H2+R(=(4K+2)R+C),((4K+3)R+C)
A26 0406C 5D065D06 MOV(A6),ADD(T(A6,A5) A6=R3, R4=R3+T(1)=0<-((4K+2)H+C),((4K+3)R+C)<1
A27 0406E 84918491 MOV(A1),MUL(M1,M4) A1=P1; P14'=U(4K+2)*R24'+U(4K+3)*R24'

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A2H 04070 4C164C16 (00R49)      MOV(A6),SUH(A0,A4)
A2H 04072 08F80000 (00R50)      MOV(10A,M3) \ NOP
A2A 04074 000000F8 (00H51)      NOP \ MOV(10A,M3)
A2H 04076 41164110 (00H52)      MOV(A0),ADD(A0,A1)
A2C 04078 85F8F8F9 (00H53)      MOV(M1),MUL(M1,M7)
A2D 0407A 50105D10 (00H54)      MOV(A0),ADD(T(A0,A5))
A2F 0407C 4C104C10 (00H55)      MOV(A0),SUH(A0,A4)
A2F 0407E 85384531 (00H56)      MOV(A1),MUL(M2,M5)
A30 04080 32111210 (00H57)      MOV(A0),ARS(A0)
A31 04082 48704870 (00H58) *      MOV(A0),SUH(A3,A0)
A32 04084 840C840C (00H59) *      MOV(10),MUL(M1,M6)
A33 04086 08F808F8 (00H61)      MOV(R,M2)
A34 04088 4CCF4CCF (00H62)      MOV(M7),SUH(A6,A4)
A35 0408A 41164110 (00H63)      MOV(A0),ADD(A0,A1)
A36 0408C 857C857C (00H64)      MOV(00),MUL(M2,M7)
A37 0408E 911C0061 (00H65) *      JUMPS(VCS7,K0)
A38 04090 5D105D10 (00H67) *      *
A38 04092 08C808C8 (00H68) *      *
A3A 04094 4C104C10 (00H69) *      *
A3B 04096 08AF08AF (00H70) *      *
A3C 04098 84608460 (00H71) *      *
A3D 0409A 12101210 (00H72) *      *
A3E 0409C 08FC08FC (00H73) *      *
A3F 0409E 48704870 (00H74) *      *
A40 040A0 088F088F (00H75) *      *
A41 040A2 08800880 (00H76) *      *
A42 040A4 85D285D2 (00H77) *      *
A43 040A6 08F008F0 (00H78) *      *
A44 040A8 40404040 (00H79) *      *
A45 040AA 08A008A0 (00H80) *      *
A46 040AC 85808580 (00H81) *      *

A6=R4,R5=R2-1/2 (-1/2<R5<1/2)
M3=V(2K+1)
M5=V(4K+1)
A0=R5, R6=R2+P1
M1=P14; P2=V(4K+2)*A,V(4K+3)*A
A0=R6, R7=R6+T(1)
A0=R7, R8=R7-1/2 (=CARG(4K)), (=CARG(4K+1))
A1=P2;P15=Y(4K+4),Y(4K-3)
A0=R3, R8=ARS(R8) 0<CARG(2K)21/2
0<CARG(4K+1)<1/2
A0=R9,R10=1/4,-P9, -1/4<SARG(4K)<1/4
-1/4<SARG(4K+1)<1/4
00=Y(4K-4),Y(4K-3);P6=Y(4K-2),Y(4K-1)
M2=SARG(4K)(=X(4K)),SARG(4K+1)(=X(4K+1))
M7=X(4K),X(4K+1);R11=R4-1/2
A0=R11, R12=R11+P2
00=Y(4K-2),Y(4K-1);P3=X(4K)^2,X(4K+1)^2
EXIT

A0=R12, R13=R12+T(1)
M0=C4
A0=R13, R14=R13-1/2 (=CARG(4K+2))
CARG(4K+3)
M7=X(4K)^2,X(4K+1)^2
P4=C4*X(4K)^2,C4*X(4K+1)^2
A0=R14, R15=ARS(R14) (0<CARG(4K+2))
CARG(4K+3)<1/2)
M4=C4
A0=R15,R16=1/4-R15 (-1/4<SARG(4K+2))
SARG(4K+3)<1/4)
M6=SARG(4K+2)(=X(4K+2))
SARG(4K+3)(=X(4K+3))
M3=X(4K+2),X(4K+3)
A2=C4*X(4K)^2,C4*X(4K+1)^2,P5=X(4K+2)^2
X(4K+3)^2
A0=C3
R17=C3+C4*X(4K)^2,C3+C4*X(4K+1)^2
M3=X(4K+2)^2,X(4K+3)^2
P6=C4*X(4K+2)^2,X(4K+3)^2

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FFTCOEFFICIENTS AND EVALUATE POLYNOMIAL APPROX.
VIA HORNER METHOD

A47 040AF 08808888 (00893)	MOV(R,M0)	M0=M7
A48 040ND 08F10AF1 (00894)	MOV(T0A,A1)	A1=C2
A49 040M2 84728472 (00895)	MOV(A2),MUL(M0,M7)	A2=P8;P7=R11*X(4K)^2,R11*X(4K+1)^2
A4A 040R4 40A04040 (00896)	ADD(A2,A0)	M1R=C3+C4*X(4K+2)^2,C3+C4*X(4K+3)^2
A4B 040H6 088C088C (00897)	MOV(R,M4)	M4=M1H
A4C 040H8 85228522 (00898)	MOV(A2),MUL(M1,M4)	A2=P7;P8=X(4K+2)^2*H1R,X(4K+3)^2*H1R
A4D 040HA 41404140 (00899)	ADD(A2,A1)	P19=P7+C2
A4E 040HC 08F008F0 (00900)	MOV(T0A,A0)	A0=C1
A4F 040HE 08808888 (00901)	MOV(R,M0)	M0=M19
A50 040C0 84728472 (00902)	MOV(A2),MUL(M0,M7)	A2=P8;P9=R19*X(4K)^2,R19*X(4K+1)^2
A51 040C2 41404140 (00903)	ADD(A2,A1)	P20=P8+C2
A52 040C4 088C088C (00904)	MOV(R,M4)	M4=M20
A53 040C6 85228522 (00905)	MOV(A2),MUL(M1,M4)	A2=P9;P10=X(4K+2)^2*P20,X(4K+3)^2*P20
A54 040C8 40A04040 (00906)	ADD(A2,A0)	P21=P9+C1
A55 040CA 08F10AF1 (00907)	MOV(T0A,A1)	A1=C0
A56 040CC 08808888 (00908)	MOV(R,M0)	M0=R21
A57 040CF 84728472 (00909)	MOV(A2),MUL(M0,M7)	A2=P10;P11=R21*X(4K)^2,R21*X(4K+1)^2
A58 040D0 40A04040 (00910)	ADD(A2,A0)	R22=P10+C1
A59 040D2 08F008F0 (00911)	MOV(T0A,M5) \ NOP	M5=U(4K)
A5A 040D4 000008F0 (00912)	NOP \ MOV(T0A,M5)	M5=U(4K+1)
A5B 040D6 08F90000 (00913)	MOV(T0A,M1) \ NOP	M1=U(4K+1)
A5C 040D8 000008F9 (00914)	NOP \ MOV(T0A,M1)	M1=U(4K+3)
A5D 040DA 088C088C (00915)	MOV(R,M4)	M4=R22
A5E 040DC 85228522 (00916)	MOV(A2),MUL(M1,M4)	A2=P11;P12=X(4K+2)^2*P22,X(4K+3)^2*P22
A5F 040DE 41404140 (00917)	ADD(A2,A1)	R23=P11+C0(=SIN(4K)),C=SIN((4K+1))
A60 040E0 10000010 (00918)	JUMP(VCS6)	GO TO TOP OF LOOP
* EXIT		
A61 040E2 088C088C (00921)	VCS7	
A62 040E4 20322032 (00922)	CLFAP(RA)	
A63 040E6 00000000 (00923)	NOP	
A64 040E8 10000000 (00924)	JUMP(0)	
* VCS55Z=8A-VCS55A		
040EA	FND VCS55Z	
	FVEN	
* SPECIAL CONSTANTS TABLE		
040EA 13DAFF42 (00934)	DATA 39.71091766	C4
040EC A6499942 (00935)	DATA -76.57409714	C1
040EE 28C015C2 (00936)	DATA 81.60223119	C2

PAGE 242 SNAP-11 MAP-100 ANITH. MODULES - PRUG. 8830101.03 MAY 7, 1980
APU3-VCOS VECTOR COSINE

040F0 94AMRC42 (00937)	DATA -41.34167750	C1
040F2 3741FC1 (0093H)	DATA 6.283185272	C0
(00939) *		

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(00940) * APU3-VFLT
(00941) *
(00942) * VECTOR VECTOR FLOAT(VFLT, VFLTH)
(00943) *
(00944) * RINDS TO APS4-V1124A FOR VFLT AND V1124C FOR VFLTH
(00945) *
(00946) * F0, Y=NUMM(0)+H
(00947) *
(00948) * EVEN
(00949) * DATA VFLTSSA
(00950) * DATA VFLTSSZ
(00951) *
(00952) * VFLTS HFCIN APU(VFLT)
(00953) * SA=0
(00954) *
(00955) * VFLTSSA MOV(10A,M7)
(00956) * MOV(10A,A7)
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(00940) * APU3-VFLT
(00941) *
(00942) * VECTOR VECTOR FLOAT(VFLT, VFLTH)
(00943) *
(00944) * RINDS TO APS4-V1124A FOR VFLT AND V1124C FOR VFLTH
(00945) *
(00946) * F0, Y=NUMM(0)+H
(00947) *
(00948) * EVEN
(00949) * DATA VFLTSSA
(00950) * DATA VFLTSSZ
(00951) *
(00952) * VFLTS HFCIN APU(VFLT)
(00953) * SA=0
(00954) *
(00955) * VFLTSSA MOV(10A,M7)
(00956) * MOV(10A,A7)
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START ON WORD BOUNDARY
START ADDRESS
SIZE
START OF APU MODULE
M7=A
A7=H
A2=UEF/
OUT=UE',R=NO RM(UEF)=UE
M0=UE
A1=PD',PE=UE+A
R0'=PD'+R
A3=U00/
A3=U00
OUT=RO',R=NO RM(U00)=U0
M1=U0
A0=PE,PD=U0+A
PE=PE+H
HALT

MOV(10A,A2) \ NOP
NOP \ MOV(10A,A2)
MOV(00),NO RM(A2)
MOV(R,M0)
MOV(A1),MOV(M0,M7)
ADD(A1,A7)
MOV(10A,A3) \ NOP
NOP \ MOV(10A,A3)
MOV(00),NO RM(A3)
MOV(R,M1)
MOV(A0),MOV(M1,M7)
ADD(A0,A7)
JUMPC(M1,F0)
CLEAR(MA) \ NOP
NOP
JUMP(0)
VFLTSSZ=0A-VFLTSSA
END VFLTSSZ
EVEN

000023	1	APS3-V1124	
000023	*		
000024	*		
000025	*	INPUT STREAM - SA, SH:UK; FI	
000026	*	OUTPUT STREAM - D1,D2,D3,D4; YK; EO	
000027	*		
000028	*		
000029	*	START OF HEADER BLOCK	
000030	*		
000031	*	FVEN	START ON WORD BOUNDARY
0411A 0000416C		ADDR V1124S1	PTR TO CONSTR INSTR BLOCK
0411C 00004126		ADDR V1124S+2+V1124SS	PTR TO SCALAR BLOCK
0411F 0002		DATA 2	NUMBER OF SCALARS
0411F 0062		DATA V1124SZ	MODULE SIZE
04120 00004164		ADDR V1124SA	PTR TO CHAIN ANCHOR
000032	*	FVEN	END OF WORD BOUNDARY
000033	*		
000034	*		
000035	*	BEGIN APS(V1124)	
000036	*		
000037	*		
000038	*		
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000207	*		
000208	*		
000209	*		
000210	*		
000211	*		
000212	*		
000213	*		
000214	*		
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000220	*		
000221	*		
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000223	*		
000224	*		
000225	*		
000226	*		
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000230	*		
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000232	*		
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000256	*		
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000260	*		
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000263	*		
000264	*		
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000280	*		
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000299	*		
000300	*		
000301	*		
000302	*		
000303	*		
000304	*		
000305	*		
000306	*		
000307	*		
000308	*		
000309	*		
000310	*		
000311	*		
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000332	*		
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000339	*		
000340	*		
000341	*		
000342	*		
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000344	*		
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000349	*		
000350	*		
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000357	*		
000358	*		
000359	*		
000360	*		
000361	*		
000362	*		
000363	*		
000364	*		
000365	*		
000366	*		
000367	*		
000368	*		
000369	*		
000370	*		
000371	*		
000372			

A0F 0413F 1C8A9006	(01026) *	ADD(RW0,[4],TF)	GEN LAST 1-3 U-ADDR
A0F 04140 1F2906M1	(01027) B4	SUBL(RW2,1),JUMPP(86)	LOOP UNTIL FINISHED
	(01028) *		
	(01029) *		
A10 04142 20200031	(01030) V1124S7	CLEAR(R1)	HAIT INPUT
A11 04144 22000020	(01031) *	NUP(0)	
	(01032) *		
	(01033) *		
	(01034) *	OUTPUT PROGRAM	
	(01035) *		
A12 04146 24300032	(01036) V1124S3	SET(RA)	TURN-ON API
A13 04148 26C20794	(01037) *	LOAD(RW0,DWYS(1),TF)	GEN DUMMY-1 ADDRESS
A14 0414A 28C20794	(01038) *	LOAD(RW0,DWYS(1),TF)	
A15 0414C 2AC20794	(01039) *	LOAD(RW0,DWYS(1),TF)	
A16 0414E 2CC20794	(01040) *	LOAD(RW0,DWYS(1),TF)	GEN DUMMY-4 ADDR
A17 04150 2F400012	(01041) *	LOAD(RW0,10)	LOAD VECTOR-Y BASE ADDR
A18 04152 30500000	(01042) *	LOAD(RW1,MSS)	LOAD VECTOR-Y SIZE-1
A19 04154 32020000	(01043) *	SUB(RW0,MSS)	SET RW0 TO Y BASE-SPACING
A1A 04156 34111F79	(01044) *	ADDL(RW1,1),JUMP(V1124S8)	ENTER LOOP AT TEST
	(01045) *		
	(01046) *	OUTPUT ADDRESS GENERATION LOOP	
	(01047) *		
A1B 04158 36A80008	(01048) B4	ADD(RW0,[8],TF)	GEN Y-ELEMENT ADDR
A1C 0415A 38A80002	(01049) *	ADD(RW0,[8],TF)	
A1D 0415C 3AA80002	(01050) *	ADD(RW0,[8],TF)	
A1E 0415E 3CA80002	(01051) *	ADD(RW0,[8],TF)	
	(01052) *		
A1F 04160 3F111H44	(01053) V1124S8	SUBL(RW1,4),JUMPP(84)	LOOP UNTIL NEG
	(01054) *		
A20 04162 401123F8	(01055) *	ADDL(RW1,3),JUMPP(V1124S0)	SET UP CLEAN-UP LOOP
	(01056) *		
A21 04164 428A8006	(01057) B9	ADD(RW0,[8],TF)	GEN LAST 1-3 Y-ADDR
A22 04166 441121H1	(01058) *	SUBL(RW1,1),JUMPP(89)	LOOP UNTIL FINISHED
	(01059) *		
A23 04168 46200030	(01060) V1124S0	CLEAR(R0)	HAIT OUTPUT
A24 0416A 48000020	(01061) *	NUP(0)	
	(01062) *		
	(01063) *		
	(01064) V1124SA=8C		ASSIGN VALUE TO CHAIN ANCHOR
00004164	(01065) *		
	(01066) *		
0416C	(01067) *	END #A-1	END OF MODULE
	(01068) *		
	(01069) *	STORAGE BLICK FOR CONSTRUCTED INSTRUCTIONS	

PAGE 28: SNAP-11 MAP-100 ARITH. MODULES - PROG. #B30101.03 MAY 7, 1960
APS3-V1124

0416C 00000000 (01072) V11746I DATA 12F'0.0'
...
00000062 (01075) V11746I=01-V1124S

COMPUTE MODULE SIZE

A00 041A6 1A291000	(01120)	ADDL(RW2, 3), JUMP(V1124AS7)	SET UP CLEAN-UP LOOP
A01 041A7 100A0006	(01121)	ADD(RW0, [R], TF)	GEN LAST 1-3 D-ADDR
A02 041A8 1A290001	(01122)	SUML(RW2, 1), JUMPP(06)	LOOP UNTIL FINISHED
A03 041A9 1A290001	(01123)		HALT INPUT
A04 041AC 20200001	(01124)	CLEAR(R1)	
A05 041AD 20200002	(01125)	NOP(0)	
A06 041AE 20200002	(01126)		
A07 041AF 20200002	(01127)	OUTPUT PROGRAM	
A08 041AG 20200002	(01128)		
A09 041AH 20200002	(01129)		
A10 041AI 20200002	(01130)	SET(RA)	TURN-ON API
A11 041AJ 20200002	(01131)	LOAD(RW0, DMSYS(1), TF)	GEN DUMMY-1 ADDR
A12 041AK 20200002	(01132)	LOAD(RW0, DMSYS(1), TF)	GEN DUMMY-2 ADDR
A13 041AL 20200002	(01133)	LOAD(RW0, DMSYS(1), TF)	
A14 041AM 20200002	(01134)	LOAD(RW0, DMSYS(1), TF)	GEN DUMMY-4 ADDR
A15 041AN 20200002	(01135)	LOAD(RW0, [R], TF)	LOAD VECTOR-Y BASE ADDR
A16 041AO 20200002	(01136)	LOAD(RW0, [R], TF)	LOAD VECTOR-Y SIZE-1
A17 041AP 20200002	(01137)	SUML(RW0, MSS)	SET RW0, TO YBASE-SPACING
A18 041AQ 20200002	(01138)	ADDL(RW1, 1), JUMP(V1124ASR)	ENTER LOOP AT TEST
A19 041AR 20200002	(01139)		
A20 041AS 20200002	(01140)		
A21 041AT 20200002	(01141)	OUTPUT GENERATION LOOP	
A22 041AU 20200002	(01142)		
A23 041AV 20200002	(01143)	ADD(RW0, [R], TF)	GEN Y-ELEMENT ADDR
A24 041AW 20200002	(01144)	ADD(RW0, [R], TF)	
A25 041AX 20200002	(01145)	ADD(RW0, [R], TF)	
A26 041AY 20200002	(01146)	ADD(RW0, [R], TF)	
A27 041AZ 20200002	(01147)		
A28 041BA 20200002	(01148)	SUML(RW1, 4), JUMPP(04)	LOOP UNTIL CNTR NEG
A29 041BB 20200002	(01149)		
A30 041BC 20200002	(01150)	ADDL(RW1, 3), JUMP(V1124AS0)	SET UP CLEAN-UP LOOP
A31 041BD 20200002	(01151)		
A32 041BE 20200002	(01152)	ADD(RW0, [R], TF)	GEN LAST 1-3 Y-ADDR
A33 041BF 20200002	(01153)	SUML(RW1, 1), JUMPP(09)	LOOP UNTIL FINISHED
A34 041BG 20200002	(01154)		
A35 041BH 20200002	(01155)	CLEAR(R0)	HALT OUTPUT
A36 041BI 20200002	(01156)	NOP(0)	
A37 041BJ 20200002	(01157)		
A38 041BK 20200002	(01158)		
A39 041BL 20200002	(01159)		
A40 041BM 20200002	(01160)	V1124ASA=0C	ASSIGN VALUE TO CHAIN ANCHOR
A41 041BN 20200002	(01161)		
A42 041BO 20200002	(01162)	END BA-3	END OF MODULE
A43 041BP 20200002	(01163)		

PAGE 31: SNAP-11 MAP-100 ARITH. MODULES - PROG. 8830101.03 MAY 7, 1980
 APS-1-V1124A APS PCM FOR VFLT

(01164) * STORAGE BLOCK FOR CONSTRUCTED INSTRUCTIONS

(01165) *

(01166) *

04106 00000000 (01167) V1124AS1 DATA 124'0.0'

...

(01168) *

(01169) *

00000062 (01170) V1124AS2=01-V1124AS

COMPUTE MODULE SIZE

APS3-V2134H		APS PGM FOR VCUS	
(01171) *	APS3-V2134H	APS PGM FOR VCUS	
(01172) *			
(01173) *			
(01174) *	INPUT STREAM - SN,SC: SA,V(4K),...V(4K+1),C4,...C0,U(4K),...U(4K+1)FFI		
(01175) *	OUTPUT STREAM - D1,D2,D3,D4; VK; FI		
(01176) *			
(01177) *			
(01178) *	START OF HEADER BLOCK		
(01179) *			
(01180) *			
(01181) *	ADDR V2134HS1		
(01182) *	ADDR V2134HS+2+V2134HS		
(01183) *	DATA 3		
(01184) *	DATA V2134HSZ		
(01185) *	ADDR V2134HSA		
(01186) *			
(01187) *			
(01188) *			
(01189) *	V2134HS		
(01190) *			
(01191) *			
(01192) *	INPUT PROGRAM		
(01193) *			
(01194) *			
(01195) *	JSN(V2134HS1,P2)		
(01196) *	SET(R0)		
(01197) *			
(01198) *	V2134HS		
(01199) *	LOAD(RR3,MSS(1))		
(01200) *	LOAD(RR0,MSS(1),TF)		
(01201) *	MOV(RR3,RR3)		
(01202) *	LOAD(RR0,(1))		
(01203) *	LOAD(RR2,MSS)		
(01204) *	SUB(RR0,MSS)		
(01205) *	LOAD(RR1,(2))		
(01206) *	LOAD(RR2,MSS)		
(01207) *	SUB(RR1,MSS)		
(01208) *			
(01209) *			
(01210) *	INPUT ADDRESS GENERATION LOOP		
(01211) *			
(01212) *	MOV(RR3,RR3,TF)		
(01213) *	ADD(RR1,(10),TF)		
(01214) *	ADD(RR1,(10),TF)		

START ON WORD BOUNDARY
PTR TO CONSTR INSTR BLOCK
NUMBER OF SCALARS
MODULE SIZE
PTR TO CHAIN ANCHOR
END OF WORD BOUNDARY

SET OUTPUT PC
TURN ON APU
GET SA ADDR SAVE
GEN SH ADDR
GEN SC ADDR
SA ADDR IN HW3
LOAD VECTOR-U BASE ADDR
LOAD VECTOR-U SIZE-1
SET RRO TO U BASE-SPACING
LOAD VECTOR-V BASE ADDR
LOAD VECTOR-V SIZE-1
SET RRI TO V BASE-SPACING

GEN SA ADDR
GEN V-ELEMENT ADDRESS

A0F 04214 1F9A0002 (01215)	ADD(HW1,110),TF)	GEN V-ELEMENT ADDR
A10 04216 209A0002 (01216)	ADD(HW1,110),TF)	
A11 04218 229A000A (01217)	LOAD(HW1,VCHSUS(1),TF)	GEN VCSC4 ADDR
A12 0421A 249A003A (01218)	ADD(HW1,2,TF)	
A13 0421C 269A003A (01219)	ADD(HW1,2,TF)	
A14 0421E 289A003A (01220)	ADD(HW1,2,TF)	
A15 04220 2A9A003A (01221)	ADD(HW1,2,TF)	GEN VCSC0 ADDRESS
A16 04222 2C9A000C (01222)	ADD(HW1,19),TF)	GEN H-ELEMENT ADDR
A17 04224 2E9A0002 (01223)	ADD(HW1,19),TF)	
A18 04226 309A0002 (01224)	ADD(HW1,19),TF)	
A19 04228 329A0002 (01225)	ADD(HW1,19),TF)	
A1A 0422A 349A0004 (01226)	SUHL(HW1,4),JUMPP(02)	LOOP UNTIL FINISHED
A1B 0422C 36200031 (01227)	CLEAR(HW1)	
A1C 0422E 38000020 (01228)	NOOP(0)	
		TURN-ON APU
		GEN DUMMY-1 ADDR
A1D 04230 3A300032 (01233)	SET(RA)	
A1E 04232 3CC20794 (01234)	LOAD(HW0,DWYS(1),TF)	
A1F 04234 3EC20794 (01235)	LOAD(HW0,DWYS(1),TF)	
A20 04236 40C20794 (01236)	LOAD(HW0,DWYS(1),TF)	
A21 04238 42C20794 (01237)	LOAD(HW0,DWYS(1),TF)	
A22 0423A 44A00012 (01238)	LOAD(HW0,101)	
A23 0423C 46500000 (01239)	LOAD(HW1,MSS)	LOAD VECTOR-Y BASE ADDR
A24 0423F 48020000 (01240)	SUHL(HW0,MSS)	LOAD(VECTOR-Y SIZE-1
A25 04240 4A112A79 (01241)	ADDL(HW1,1),JUMP(V2134HS8)	SET RW0 TO Y BASE-SPACING
		ENTER LOOP AT TEST
		GEN Y-ELEMENT ADDRESS
A26 04242 4CH9A00H (01246)	ADD(HW0,101,TF)	
A27 04244 4F9A0002 (01247)	ADD(HW0,101,TF)	
A28 04246 509A0002 (01248)	ADD(HW0,101,TF)	
A29 04248 529A0002 (01249)	ADD(HW0,101,TF)	
A2A 0424A 541126H4 (01251)	SUHL(HW1,4),JUMPP(04)	LOOP UNTIL CNTR NEG
A2B 0424C 56312FEH (01252)	ADDL(HW1,1),JUMPN(V2134HS0)	SET UP CLEAN-UP LOOP
A2C 0424E 589A000A (01255)	ADD(HW0,101,TF)	GEN LAST 1-3 Y-ADDR
A2D 04250 5A112CH1 (01256)	SUHL(HW1,1),JUMPP(09)	LOOP UNTIL FINISHED
A2E 04252 5C200030 (01257)	CLEAR(HW0)	HALT OUTPUT

PAGE 34: SNAP-II MAP-300 ARITH. MODULES - PROG. 8830101.03 MAY 7, 1980
 APS3-V2134R APS PGM FOR VCONS

A2F 04254	5F000020	(01259)	NOP(0)	
		(01260)	*	
		(01261)	*	
		(01262)	*	
0000424F	(01263)	V2134HSA=0C		ASSIGN VALUE TO CHAIN ANCHOR
	(01264)	*		
04256		(01265)		END OF MODULE
		(01266)	*	
		(01267)	*	
		(01268)	*	
		(01269)	*	
		(01270)	*	
04256	00000000	(01271)	V2134HST DATA	16F'0.0'
...				
		(01272)	*	
		(01273)	*	
00000000	(01274)	V2134HSZ=BL-V2134HS		COMPUTE MODULE SIZE

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SNAP-11 MAP-300 ARITH. MODULES - PROG. 0010101.03 MAY 7, 1980
MAP-300 EXTENDED ARRAY FUNCTIONS - PROG. 0010102.03 - MAY 7, 1980
(01275) 'MAP-300 EXTENDED ARRAY FUNCTIONS - PROG. 0010102.03 - MAY 7, 1980
(01276) *
(01277) *
(01278) *

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(01279) *NOT-IN-PLACE FFT FOR ONE AND TWO DIMENSIONS
(01280) *
(01281) *
(01282) *FAST FOURIER TRANSFORM AND INVERSE TRANSFORM ALGORITHMS
(01283) *
(01284) * PERFORMS AN N POINT FFT OR IFFT ON DATA STORED IN
(01285) * THE U BUFFER USING THE V BUFFER AS THE
(01286) * COSINE TABLE AND THE W BUFFER AS A
(01287) * WORKING BUFFER AND LEAVES THE RESULT IN THE
(01288) * Y BUFFER. THIS ROUTINE CANNOT BE DONE IN
(01289) * PLACE (I.E. W AND U CANNOT BE THE SAME BUF-
(01290) * FER). THE NUMBER OF POINTS IN THE FFT,
(01291) * N, MUST BE A POWER OF TWO AND NOT GREATER
(01292) * THAN 1024.
(01293) *
(01294) *
(01295) * THE BUFFER DESCRIPTIONS ARE:
(01296) * Y BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(01297) * U BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(01298) * V BUFFER (10-39) REAL 32-BIT FLOATING POINT
(01299) * W BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(01300) *
(01301) * THE COSINE TABLE ENTRIES ARE:
(01302) * C(K)=COS(2*PI*K/CSIZE)
(01303) * WHERE CSIZE IS A MULTIPLE OF N
(01304) *
(01305) *
(01306) **ENTRY TO SPECIAL WINDING MODULE FOR 2-D FFT SETUP
(01307) **
(01308) ** ENTER WITH M1 POINTING TO FCH NUMBER
(01309) ** R2 POINTING TO DISPATCH TABLE
(01310) **
(01311) **
(01312) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01313) **
(01314) **
(01315) *RPTFT01 MOVW R5,M1 ; COPY FCH BLOCK POINTER
(01316) * FVEN ; TAG REPEATED FFT CALL
(01317) * MOVLM 2,FFTSTYP ; GET LOG2(FFT SIZE)
(01318) * MOVW R7,2*HS(45) ; FORCE TO BE <= 15
(01319) * ANDIR R7,5F ; SAVE FOR LATER PERMANENT STORAGE
(01320) * MOVW R7,LOG2SZ ; POINT TO PARAMETER STORAGE AREA
(01321) * MOVW R7,SMPSPC ; SET LOOP COUNTER FOR 4 TRANSFERS
(01322) *

```

```

(01323) **
(01324) CORPFT02 POPX1 R5,R4 ; LOAD PASSED PARAMETER INTO R4
(01325) ** EVEN
(01326) ** ANDIR R4,SE ; FORCE ALL TO BE <= 15
(01327) ** MOVNR R1,RPFT03 ; GET COPY OF SHIFT INSTRUCTION
(01328) ** TORNK R4,R1,SEFFO ; OR IN SHIFT COUNT
(01329) ** MOVNR R4,RPFT03 ; STORE SHIFT INSTRUCTION
(01330) ** MOVIR R4,1 ; INIT R4 TO 2**0
(01331) CORPFT03 LLS R4,0 ; COMPUTE 2**N
(01332) ** PUSHX1 R7,R4 ; SAVE IN PARAMETER AREA
(01333) ** DJP R6,RPFT02 ; LOOP OVER ALL 4 PARAMETERS
(01334) ** PUSHX1 R7,R4 ; NEED 2 COPIES OF FFT SPACING
(01335) ** EVEN
(01336) **
(01337) ** COMPUTE MAXSMP = (MAXIMUM SAMPLE INDEX) - 1
(01338) **
(01339) ** MAXSMP = (SAMPLE_SPACING)*(FFT_SIZE - 1) +
(01340) ** (NUMBER_OF_FFT'S - 1)*(FFT_SPACING)
(01341) ** = SA*(SD-1) + (SC-1)*SD
(01342) **
(01343) ** MOVNR R5,FFTSIZ ; GET SIZE
(01344) ** MOVNR R7,NFFT ; GET NUMBER OF FFT'S
(01345) ** DECR R5,1 ; CORRECT TO (FFTSIZE-1)
(01346) ** DECR R7,1 ; CORRECT TO (NFFT-1)
(01347) ** MOVNR R7,NFFT ; SAVE DECREMENTED COUNT FOR LOOPING
(01348) ** MULMR R5,SMSPSC ; FORM 1ST PRODUCT
(01349) ** MULMR R7,INSPC ; FORM 2ND PRODUCT
(01350) ** ADDR R5,R7 ; FORM SUM
(01351) ** LRS R5,1 ; COMMON CORRECTION FOR DOUBLING BY MU
(01352) ** MOVNR R5,MAXSMP ; SAVE FOR LATER
(01353) ** HOP RPFT04 ; HOP INTO FINDING CODE
(01354) **
(01355) *
(01356) ***** END REPEAT FFT CODE INSERTION *****
(01357) *

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```

(01358) ENTRY TO SPECIAL BINDING MODULE FOR VECTOR FFT SETUP
(01359) ?
(01360) ? DOES ALL BINDING NOT NEEDED EVERY TIME
(01361) ? AN FFT IS DONE.
(01362) ?
(01363) ? ENTER WITH R1 POINTING TO FOR NUMBER
(01364) ?
(01365) ?
(01366) ?
(01367) FFTSSPT = 01.
(01368) ** MOVZM FFTSTYP
(01369) RPTFT04 MOVZM R4, 2*HS(M1)
(01370) ANDIR R4, MSKSLV1
(01371) LRS R4, 7
(01372) EVEN
(01373) MOVZM R5, ACTSAT+HS(R4)
(01374) MOVZM R5, PWR2S
(01375) ?
(01376) ? RIND ALL USIZE-1'S
(01377) ?
(01378) ?
(01379) ?
(01380) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01381) ?
(01382) ** MOVZM R7, FFTSTYP
(01383) ** JMP RPTFT05(R7)
(01384) ?
(01385) ** RPTFT05 ADDR RPTFT07 ( , 1)
(01386) ** ADDR RPTFT06 ( , 1)
(01387) ?
(01388) ** RPTFT06 MOVZM R7, LOG2SZ
(01389) ** MOVZM R7, PWR2S
(01390) ** CMPR R6, MAXSMP
(01391) ** JMP PWRSS, LT
(01392) ** MOVZM R7, INSPC
(01393) ** MULR R7, R5
(01394) ** LRS R7, 1
(01395) ** MOVZM R7, INSPC
(01396) ** MULR R5, SMPSPC
(01397) ** LRS R5, 1
(01398) ** EVEN
(01399) ** MOVZM R6, FFTSZ
(01400) ** MOVZM R7, R6
(01401) ** DECR R7, 1
; TAG 'MIXED' FFT
; GET 0 BUFFER ID
; MASK OUT RIGHT HALF
; SET FOR FULL WORD INDEX
; SKIP TO EVEN BOUNDARY
; R5= 0 BUFFER ATTRIBUTES
; STORE FOR LATER REFERENCE
; GET LOG2(FFT SIZE)
; STORE FOR BINDING CODE USAGE
; CHECK INPUT BUFFER SIZE
; JUMP IF BUFFER TOO SMALL
; GET FFT INPUT VECTOR SPACING
; SCALE BY BUFFER SAMPLE SPACING
; CORRECT FOR FACTOR OF 2 BY MUL
; SAVE FOR USE IN POST-SUPPORT MODULE
; COMPUTE INPUT SAMPLE SPACING
; CORRECT FACTOR OF 2 FROM MUL
; GET CORRECT FFT SIZE
; GET 2ND COPY
; NEED TO BIND (FFT_SIZE - 1) 6 PLACES

```

```

(01402) 00      MOVW R7,CSMS + WS(CH4A03) + HS
(01403) 00      MOVW R7,CSMS + WS(CSML05) + HS
(01404) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01405) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01406) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01407) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01408) 00      HOP      REPTFOR
(01409) 00      EVFN
(01410) 00
(01411) ***** END REPEAT FFT CODE INSERTION *****
(01412) 00
(01413) 00      REPTFOR MOVW R6, CSMS+WS(CH4A03)+HS   STORE ALL USIZE-1'S
(01414) 00      MOVW R6, CSMS+WS(CSML05)+HS   ...
(01415) 00      MOVW R6, CSMS+WS(CH4A35)+HS   ...
(01416) 00      MOVW R6, CSMS+WS(CH4A35)+HS   ...
(01417) 00
(01418) 00
(01419) 00      ;SCALAR A ID IS REALLY THE NUMBER OF MINI-BUFFERS
(01420) 00      ;WITHIN THE U BUFFER.
(01421) 00      ;FOR EXAMPLE:
(01422) 00      ; IF U BUFFER HAS 1024 COMPLEX POINTS BUT IS
(01423) 00      ; REALLY 4 GROUPS OF 256 POINTS THEN SA ID
(01424) 00      ; SHOULD BE SET TO 4.
(01425) 00      ; IF U BUFFER HAS 1024 POINTS AND IS REALLY
(01426) 00      ; 16 GROUPS OF 64 POINTS THEN SA ID SHOULD BE
(01427) 00      ; 16.
(01428) 00      ; NORMAL OPERATION WHERE THE U BUFFER CONTAINS
(01429) 00      ; ONE BUFFER TO BE FFTED MEANS SA ID SHOULD
(01430) 00      ; BE 1.
(01431) 00      ; SA ID MUST BE A POWER OF 4. (I.E. 1, 4,
(01432) 00      ; 16, 64, 256)
(01433) 00
(01434) 00      MOVW R4, HS(R1)
(01435) 00      ANDIR R4, MSKSRHT
(01436) 00      MOVW R7, R6
(01437) 00      SRHC R7, R6
(01438) 00      HOP XX2S
(01439) 00      LPS R7,1
(01440) 00      LPS R4, 1
(01441) 00      HOP XX1S
(01442) 00      EVFN
(01443) 00      MOVW R7, CSMS+WS(CH4A03)+HS   STORE FFTSIZE-1
(01444) 00      MOVW R7, CSMS+WS(CH4A35)+HS   ...
(01445) 00
(01402) 00      MOVW R7,CSMS + WS(CH4A03) + HS
(01403) 00      MOVW R7,CSMS + WS(CSML05) + HS
(01404) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01405) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01406) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01407) 00      MOVW R7,CSMS + WS(CH4A35) + HS
(01408) 00      HOP      REPTFOR
(01409) 00      EVFN
(01410) 00
(01411) ***** END REPEAT FFT CODE INSERTION *****
(01412) 00
(01413) 00      REPTFOR MOVW R6, CSMS+WS(CH4A03)+HS   STORE ALL USIZE-1'S
(01414) 00      MOVW R6, CSMS+WS(CSML05)+HS   ...
(01415) 00      MOVW R6, CSMS+WS(CH4A35)+HS   ...
(01416) 00      MOVW R6, CSMS+WS(CH4A35)+HS   ...
(01417) 00
(01418) 00
(01419) 00      ;SCALAR A ID IS REALLY THE NUMBER OF MINI-BUFFERS
(01420) 00      ;WITHIN THE U BUFFER.
(01421) 00      ;FOR EXAMPLE:
(01422) 00      ; IF U BUFFER HAS 1024 COMPLEX POINTS BUT IS
(01423) 00      ; REALLY 4 GROUPS OF 256 POINTS THEN SA ID
(01424) 00      ; SHOULD BE SET TO 4.
(01425) 00      ; IF U BUFFER HAS 1024 POINTS AND IS REALLY
(01426) 00      ; 16 GROUPS OF 64 POINTS THEN SA ID SHOULD BE
(01427) 00      ; 16.
(01428) 00      ; NORMAL OPERATION WHERE THE U BUFFER CONTAINS
(01429) 00      ; ONE BUFFER TO BE FFTED MEANS SA ID SHOULD
(01430) 00      ; BE 1.
(01431) 00      ; SA ID MUST BE A POWER OF 4. (I.E. 1, 4,
(01432) 00      ; 16, 64, 256)
(01433) 00
(01434) 00      MOVW R4, HS(R1)
(01435) 00      ANDIR R4, MSKSRHT
(01436) 00      MOVW R7, R6
(01437) 00      SRHC R7, R6
(01438) 00      HOP XX2S
(01439) 00      LPS R7,1
(01440) 00      LPS R4, 1
(01441) 00      HOP XX1S
(01442) 00      EVFN
(01443) 00      MOVW R7, CSMS+WS(CH4A03)+HS   STORE FFTSIZE-1
(01444) 00      MOVW R7, CSMS+WS(CH4A35)+HS   ...
(01445) 00

```

PAGE 40: SNAU-11 MAP-100 ARITH. MODULES - PROG. #30101.03 MAY 7, 1980
ENTRY TO SPECIAL BINDING MODULE FOR VECTOR FFT SETUP

```

04298 2661 (01466) INCR R6, 1 R6<EN(USIZE)
04299 403A (01467) RPTSTOM MOVRR R3, R5 R3<USEP
0429A 445C (01468) MULRR R5, R6 R5<2(USEP)(USIZE)
0429B 4C52 (01469) LRS R5, 2 R5<0.5(USEP)(USIZE)
(01450) ?
(01451) ?
(01452) ?
(01453) ?
(01454) ?
(01455) ?
(01456) ?
(01457) ?
(01458) ?
(01459) ?
(01460) ?
(01461) ?
(01462) ?
(01463) ?
(01464) ?
(01465) ?
(01466) ?
(01467) ?
(01468) ?
(01469) ?
(01470) ?
(01471) ?
(01472) ?
(01473) ?
(01474) ?
(01475) ?
(01476) ?
(01477) ?

0429C 4040449D MOVRR R4, HUSTRL-1 ; POINT TO TABLE OF HU BINDING LOC'S
0429D 3044 POPXI R4, R7 ; GET NEXT BINDING ADDR
0429E 0270 TEST R7 ; CHECK FOR END OF TABLE
042A0 801042AA JMP HUSDN, F0 ; ZERO MARKS END OF TABLE
042A1 000F0000 CMH 0, 0(R7) ; RESET MSR OF DELTA FIELD
042A4 F05F0001 MOVRR R5, 1(R7) ; STORE LOW 16 BITS
042A6 1410 SKPL R4 ; IF NOT 0, DELTA IS OK
042A7 D20F0000 SMH 0, 0(R7) ; PRODUCT WAS 2**18 BEFORE CORRECTION
042A9 210C HUP HUSLP ; LOOP FOR ALL HU BINDING
(01462) ?
(01463) ?
(01464) ?
(01465) ?
(01466) ?
(01467) ?
(01468) ?
(01469) ?
(01470) ?
(01471) ?
(01472) ?
(01473) ?
(01474) ?
(01475) ?
(01476) ?
(01477) ?

042AA 0250 TEST R5 ; CHECK PRODUCT
042AB 1410 SKPL R5 ; IF NOT 0, DELTA IS OK
042AC F0510000 MOVRR R5, S10000 ; SET UP CORRECT DELTA FOR 2**18
042AE 3C51 LRS R5, 1 R5<=HU/2 (QU)
042AF 0400 EVFN ; TO TO EVEN BOUNDARY
(01469) ?
(01470) ?
(01471) ?
(01472) ?
(01473) ?
(01474) ?
(01475) ?
(01476) ?
(01477) ?

042B0 F0504577 MOVRR R5, CSMS+MS*CSM1, 1S+HS STORE QU'S
042B2 4F56 SUBRR R5, R3 R5<=QU-USEP
042B3 0400 EVFN
042B4 F050456F MOVRR R5, CSMS+MS*CSM1, 1S+HS STORE QU-USEP
(01476) ?
(01477) ?
EJECT

```

PAGE 413 SNAP-IT MAP-100 ARITH. MODULES - PROG. 040101.03 MAY 7, 1980
ENTRY TO SPECIAL WINDING MODULE FOR VECTOR FFT SETUP

042M6 405C	(01478)	MOVW R5, R6	R5<=N
042M7 0R00	(01479)	FVFN	
042M8 0450003	(01480)	MOVLW R5, 3	R5<=4*(3N/2)
	(01481) ;		
	(01482) ;	WIND DO=07'S	
	(01483) ;		
042M9 F05045R5	(01484)	MOVW R5, CSMS+WS*SCRM0+HS	STORE ALL D0'S
042M0 F05045H9	(01485)	MOVW R5, CSMS+WS*SCRM2S+HS	...
042M1 F05045H0	(01486)	MOVW R5, CSMS+WS*SCRM4S+HS	...
042M2 F05045Q1	(01487)	MOVW R5, CSMS+WS*SCRM6S+HS	...
042M3 3C51	(01488)	LRS R5, 1	R5<=D0/2 (D1)
042M4 0R00	(01489)	FVFN	
042M5 F05045R7	(01490)	MOVW R5, CSMS+WS*SCRM1S+HS	STORE ALL D1'S
042M6 F05045H4	(01491)	MOVW R5, CSMS+WS*SCRM5S+HS	...
042M7 3C51	(01492)	LRS R5, 1	R5<=D1/2 (D2)
042M8 0R00	(01493)	FVFN	
042M9 F05045H8	(01494)	MOVW R5, CSMS+WS*SCRM3S+HS	STORE D2
042M0 3C51	(01495)	LRS R5, 1	R5<=D2/2 (D3)
042M1 0R00	(01496)	FVFN	
042M2 F05045Q5	(01497)	MOVW R5, CSMS+WS*SCRM7S+HS	STORE D3
042M3 0R00	(01498)	LRS R5, 1	R5<=D3/2 (D4)
042M4 0R00	(01499)	FVFN	
042M5 F05045Q4	(01500)	MOVW R5, CSMS+WS*SCRM9S+HS	STORE D4
042M6 3C51	(01501)	LRS R5, 1	R5<=D4/2 (D5)
042M7 0R00	(01502)	FVFN	
042M8 F05045A1	(01503)	MOVW R5, CSMS+WS*SCRM9S+HS	STORE D5
042M9 3C51	(01504)	LRS R5, 1	R5<=D5/2 (D6)
042M0 0R00	(01505)	FVFN	
042M1 F05045A7	(01506)	MOVW R5, CSMS+WS*SCRM10S+HS	STORE D6
042M2 3C51	(01507)	LRS R5, 1	R5<=D6/2 (D7)
042M3 0R00	(01508)	FVFN	
042M4 F05045A8	(01509)	MOVW R5, CSMS+WS*SCRM7+HS	STORE D7
	(01510) ;		
	(01511)		
	(01512)		

EJECT

PAGE 42: SNAP-II MAP-100 ARITH. MODULES - PROG. 810101.03 MAY 7, 1960
ENTRY TO SPECIAL RINDING MODULE FOR VECTOR FFT SETUP

042E0 305C	(01513)	MOVW R5, R6	RSC=N (USIZE)
042F1 3A52	(01514)	LIS R5, 2	RSC=46N
042F2 2A53	(01515)	INCH R5, 4	RSC=46N+4
042F3 0A00	(01516)	EVEN	
	(01517)		
	(01518)	JECT	

AD-A691 663

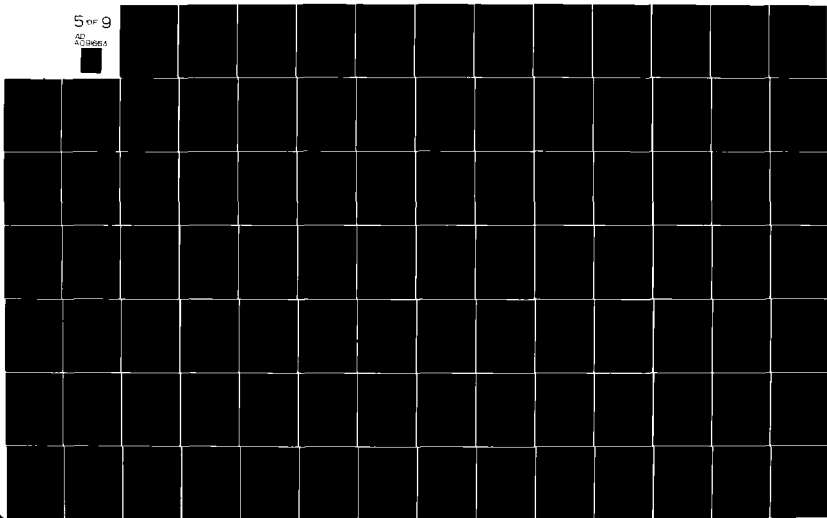
GTE PRODUCTS CORP. NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME S0--ETC (U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

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NL

5 of 9

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ADDERA



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(01519) ?
(01520) PRINT ALL 4N+4 AND 4N+12'S
(01521) ?
042E4 F05045F9      MOVW R5, CSMS+WS+CR4A2S+HS      STORE 4N+4'S
042E6 F050462F      MOVW R5, CSMS+WS+CR4A3+HS      ...
042E8 2658          INCW R5, R                    R5<=4*N+12
042E9 0800          FVEN
042FA F05045H5      MOVW R5, CSMS+WS+CSMH+HS
042FC F0420001      MOVW R4, HS(R1)
042FE 9A40F00       ANDIR R4, MSKSIRYT
042FF 3C17          LRS R4, 7
042F1 0800          FVEN
(01531) ?
(01532) PRINT YSEP
(01533) ?
042F2 C0500604      MOVW R5, RCTSD(R4)      R5<=YSEP, R6<=YSIZE-1
(01535) ?
(01536) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01537) ?
(01538) **          MOVW R7, FFTSTYP      ; GET FFT TYPE TAG
(01539) **          JMP  @RPTFT09(R7)      ; JUMP TO NEXT SECTION OF CODE
(01540) ?
(01541) ** RPTFT09 ADDR RPTFT11( , 1)
(01542) ** ADDR RPTFT10( , 1)
(01543) ?
(01544) ** RPTFT10 MOVW R7, OUTSPC
(01545) ** MULW R7, R5
(01546) ** LRS R7, 1
(01547) ** MOVW R7, OUTSPC
(01548) ** CMW R4, MAXSMP
(01549) ** JMP  @RHSJIT
(01550) ** MULW R5, SMPSPC
(01551) ** LRS R5, 1
(01552) ** FVEN
(01553) ** MOVW R5, CSMS + WS+CR4A12S + HS      ; STORE YSEP
(01554) ** MOVW R6, FFTS12
(01555) ** HOP RPTFT12
(01556) ** FVEN
(01557) ?
(01558) ** FRRS1CR = 29
(01559) ** FRRSS MOVW R7, FRRS1CR
(01560) ** **          JMP  ERRORS      ; SET IMPROPER BUFFER ERROR FLAG
(01561) ?
(01562) ***** END REPEAT FFT CODE INSERTION *****

```

[illegible]

PAGE 45: SNAP-II MAP-300 ARITH. MODULES - PROG. #R0101.03 MAY 7, 1980
ENTRY TO SPECIAL WINDING MODULE FOR VECTOR FFT SETUP

```

(01572) ?
(01573) ? RIND YSEPVSIZE
(01574) ?
042FA 000045DF CMR 0,CSMS + WS*CR4AUF ; RESET MSR OF DELTA FIELD
042FC 1H30 SKPL GE ; LEAVE MSR CLEAR WHEN PRODUCT POS
042FD 020045DF SMR 0,CSMS + WS*CR4AUF ; MSR SHOULD BE SET
042FF 81104306 JMP YSPSZ,NF ; IF NOT 0, ALL IS WELL
04301 CC0045DF MOVZM CSMS + WS*CR4AUF + HS ; PRODUCT IS 2*18 -- SET DELTA = 0
04303 9050M000 MOVIR R5,$R000 ; YSEPVSIZE/4 = 2*16
04305 2004 NOP YSPSZ4 ; GO CONTINUE WINDING
04306 F05045DF YSPSZ4 MOVHM R5, CSMS+WS*CR4AUF+HS STORE YSEPVSIZE
04308 3052 LRS R5, 2 R5<=YSEPVSIZE*YSEPV/4
04309 0000 EVEN
(01585) ?
(01586) ? RIND YSEPVSIZE/4
(01587) ?
0430A F05045DF MOVHM R5, CSMS+WS*CR4A11s+HS STORE YSEPVSIZE/4
0430C F0420003 YSPSZ4 MOVHM R4, 3*HS(R1) LOAD C BUFFER ID
0430F 9A40FF00 ANDIR R4, MSKSLAYT
04310 3C47 LRS R4, 7
04311 0000 EVEN
(01593) ?
(01594) FJFCT

```

CREATE FULL WORD INDEX

```

(01595) ?
(01596) ? RIND CHASE
(01597) ?
04312 006005R2 (01598) MOVMI R6, RCTSHA(R4)
04314 0050461F (01599) MOVMI R5, CSMS+WS*CR4A4S
04316 0460000F (01600) ANDIR R6, SF
04318 766AFFF0 (01601) TORMK R6, R5, SFFFO
0431A 846A0000 (01602) MOVMI R6, R(P5)
0431C 005R0604 (01603) MOVMI R5, RCTSD(R4)
0431E 2661 (01604) INCR R6, 1
0431F 485C (01605) MULIR R5, R6
04320 3C53 (01606) LRS R5, 3
04321 0800 (01607) EVFM
(01608) ?
(01609) ? RIND ALL HPI'S
(01610) ?
04322 F0504625 (01611) MOVMI R5, CSMS+WS*CR4A5S+HS
04324 F0504629 (01612) MOVMI R5, CSMS+WS*CR4A6S+HS
04326 F050462D (01613) MOVMI R5, CSMS+WS*CR4A7S+HS
(01614) ?
(01615) ? SIANG=HPI/SSI, RIGHT NOW ONLY RADIX 2 AND RADIX 4
(01616) ? HAVE BEEN IMPLEMENTED. SO ALL THAT IS NECESSARY
(01617) ? TO CALCULATE SSI IS TO KNOW IF USIZE IS AN EVEN OR
(01618) ? ODD POWER OF TWO. THIS IS DONE BY CHECKING THE
(01619) ? POWER OF TWO ENTRY FOR THE U BUFFER IN THE ACTSAT
(01620) ? TABLE. IF BIT 0 IS ON, THEN IT'S AN ODD POWER.
(01621) ?
04328 F010074D (01622) MOVMI R3, APSHSL
0432A 0400439D (01623) SMRC 0, PWR2S
0432C 2006 (01624) HOP SHMS1
0432D F00A45C7 (01625) MOVMI 4, CSMS+WS*CR4A01+HS
0432F F00A439C (01626) MOVMI F1GSG1, GSFLGS+HS
04331 3C52 (01627) LRS R5, 2
04332 2005 (01628) HOP SHMS2
04333 F00A45C7 (01629) SHMS1
04335 F0A4439C (01630) MOVMI F1GSG1+F1GSGSET, GSFLGS+HS
04337 3C51 (01631) LRS R5, 1
(01632) ?
(01633) SHMS2
(01634) ?
(01635) ? RIND SIANG TO STANG'S
(01636) ?
04338 F0504657 (01637) MOVMI R5, CSMS+WS*CR4A11S+HS
0433A 3C52 (01638) LRS R5, 2

```

LOAD C HASE ADDRESS
POINT TO LOAD CHASE INST.
AND OUT LOW FOUR BITS
ON IN NEW FOUR BITS
AND STORE HACK IN INST.
R5<=CSFP, R6<=CSIZE-1
R6<=CSIZE
R5<=2*(CSFP)(CSIZE)
R4<=0.25*(CSFP)(CSIZE) (HPI)

FETCH POINTER
SKIP IF EVEN POWER OF TWO
... ODD POWER
SET AND RIND SSI = 4
RESET G1
SIANG=HPI/4
SET AND RIND SSI = 2
SET G1
SIANG=HPI/2

STORE SIANG

PAGE 47: SNAP-II MAP-100 ARITH. MODULES - PROG. #H30101.03 MAY 7, 1980
ENTRY TO SPECIAL HANDLING MODULE FOR VECTOR FFT SETUP

0433H 0800	(01639)	EVFN	
0433C F0504659	(01640)	MOVFM R5,	CSMS+WS*ANGL2S+HS STORE S2ANG
0433E 3C52	(01641)	LRS R5, 2	
0433F 0800	(01642)	EVFN	
04340 F050465H	(01643)	MOVFM R5,	CSMS+WS*ANGL3S+HS STORE S3ANG
04342 3C52	(01644)	LRS R5, 2	
04343 0800	(01645)	EVFN	
04344 F050465D	(01646)	MOVFM R5,	CSMS+WS*ANGL4S+HS STORE S4ANG
04346 3C52	(01647)	LRS R5, 2	
04347 0800	(01648)	EVFN	
04348 F050465F	(01649)	MOVFM R5,	CSMS+WS*ANGL5S+HS STORE S5ANG
0434A 3C52	(01650)	LRS R5, 2	
0434H 0800	(01651)	EVFN	
0434C F0504661	(01652)	MOVFM R5,	CSMS+WS*ANGL6S+HS STORE S6ANG
0434E 3C52	(01653)	LRS R5, 2	
0434F 0800	(01654)	EVFN	
04350 F0504663	(01655)	MOVFM R5,	CSMS+WS*ANGL7S+HS STORE S7ANG

PAGE 48: SNAP-II MAP-300 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980
SPECIAL BINDING FOR WBASE, YBASE, AND URASE

```

(01656) ; SPECIAL BINDING FOR WBASE, YBASE, AND URASE
(01657) ;
(01658) ; THIS SECTION DOES THE FAST BINDING FOR
(01659) ; CHANGES IN WBASE, YBASE, AND URASE ONLY.
(01660) ;
(01661) ; SHMSFT EVEN START ON EVEN BOUNDARY
(01662) ; MOVNR R4, 4*HS(R1) GET W BUFFER ID
(01663) ; ANDIR R4, MSKSLAYT MASK THE LEFT HALF
(01664) ; IRS R4, 7 CREATE FULL WORD INDEX
(01665) ; EVEN
(01666) ;
(01667) ; BIND ALL WBASE'S
(01668) ;
(01669) ; MOVML R6, ACTSHA(R4) LOAD WASE ADDRESS IN R6, R7
(01670) ; MOVIR R5, CSMS+WS*CSMO1S POINT TO LOAD INSTRUCTION
(01671) ; ANDIR R6, SF ONLY LOW FOUR BITS
(01672) ; TORWK R6, R5, SFFF0 'OR'ED INTO LOAD INST.
(01673) ; MOVRL R6, 0(R5) STORE ALL WBASE'S
(01674) ; ANDIR R6, SF MASK OUT OLD LOAD INST.
(01675) ; MOVIR R5, CSMS+WS*CR4A1S POINT TO NEXT LOAD WBASE
(01676) ; TORWK R6, R5, SFFF0 OR WBASE INTO IT
(01677) ; MOVRL R6, 0(R5) STORE WBASE
(01678) ;
(01679) ; BIND URASE
(01680) ;
(01681) ; MOVNR R4, 2*HS(R1) LOAD U BUFFER ID
(01682) ; ANDIR R4, MSKSLAYT MASK THE LEFT HALF
(01683) ; IRS R4, 7 CREATE FULL WORD INDEX
(01684) ; EVEN
(01685) ; MOVML R6, ACTSHA(R4) LOAD URASE ADDRESS IN R6, R7
(01686) ; MOVIR R5, CSMS+WS*CSML6S POINT TO LOAD URASE INST.
(01687) ; ANDIR R6, SF ONLY LOW ORDER FOUR BITS
(01688) ; TORWK R6, R5, SFFF0 'OR' INTO INST.
(01689) ; MOVRL R6, 0(R5) STORE URASE
(01690) *
(01691) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01692) *
(01693) ** ADDR R7, INSPC ; RUMP TO NEXT INPUT VECTOR
(01694) ** ADC R6 ; COMPLETE THE RUMP
(01695) ** EVEN
(01696) ** MOVML R6, LOSURS ; SAVE NEXT <LOAD URASE> INSTRUCTION
(01697) *
(01698) ***** END REPEAT FFT CODE INSERTION *****
(01699) *

```

PAGE 49: SNAP-11 MAP-300 ARITH. MODULES - PROG. 8810101.03 MAY 7, 1980
SPECIAL HANDLING FOR YBASE, YBASE, AND YBASE

```

(01700) F
(01701) JHND YBASE
(01702) J
0437A F0420001 (01703) MOVRL R4, HS(R1) LOAD Y BUFFER TO
0437C 9A40FF00 (01704) ANDIR P4, MSLSHWT MASK LEFT HALF
0437E 3C47 (01705) LRS R4, 7 CREATE FULL WORD INDEX
0437F 0800 (01706) EVEN
04380 C06805R2 (01707) MOVRL R6, RCTSHA(R4) LOAD YBASE ADDRESS IN R6, R7
04382 905045DA (01708) MOVIR R5, CSMS*SCW4A10S POINT TO LOAD INST.
04384 9A60000F (01709) ANDIR R6, SF MASK LOW FOUR BITS
04386 766A0FF0 (01710) TOWRK R6, R5, SF*F0 ON INTO INST.
04388 846A0000 (01711) MOVRL R6, 0(R5) STORE YBASE
(01712) *
(01713) ***** BEGIN REPEAT FFT CODE INSERTION *****
(01714) *
(01715) ** ADDMR R7,OUTSPC ; RUMP TO NEXT OUTPUT VECTOR
(01716) ** ADC R6 ; COMPLETE THE RUMP
(01717) ** EVEN
(01718) ** MOVRL R6,INDSYHS ; SAVE NEXT <LOAD YBASE> INSTRUCTION
(01719) *
(01720) ***** END REPEAT FFT CODE INSERTION *****
(01721) *

```



```

(01766) **      PUSHML R3, RPTFT21      ; POST-SUPPORT ADDR FOR REPEAT FFT
(01767) **      ;
(01768) **RPTFT21 ADDR      RPTFT31( ,1,5F)      ; POST-SUPPORT ENTRY ADDR
(01769) **      ;
(01770) **      ;
(01771) **FFTSTYP DATA      MSS      ; FFT TYPE TAG
(01772) **LOG2SZ DATA      MSS      ; TEMPORARY STORAGE OF LOG2(FFT-SIZE)
(01773) **MAXSMP DATA      MSS      ; MAXIMUM SAMPLE INDEX - 1
(01774) **      ;
(01775) **      EVEN      ; MAKE THIS BLOCK EVEN FOR LATER TRANS
(01776) **SMPSPC DATA      MSS      ; INPUT SAMPLE SPACING
(01777) **FFTSIZ DATA      MSS      ; SINGLE FFT SIZE
(01778) **NFFT DATA      MSS      ; NUMBER OF FFT'S TO DO
(01779) **INSPC DATA      MSS      ; INPUT FFT VECTOR SPACING
(01780) **OUTSPC DATA      MSS      ; OUTPUT FFT VECTOR SPACING
(01781) **      ;
(01782) **      DATA      MSS      ; NEED FULL WORD BOUNDARY
(01783) **      ;
(01784) **LODSHS DATA      F'0.0'      ; ROUND <LOAD URASE> INSTR
(01785) **LDSHS DATA      F'0.0'      ; ROUND <LOAD YRASE> INSTR
(01786) *
(01787) ***** END REPEAT FFT CODE INSERTION *****
(01788) *
(01789) CSFLGS DATA      S4, S24      ; CODES FOR CONTROLLING FLAGS G0 & G1

(01790) PWR2S DATA      MSS      STORAGE FOR POWER OF TWO
(01791) ?
(01792) RUSTRL DATA      CSMS+MS*CSML      ; TABLE OF ADDRS FOR BINDING HU
(01793) DATA      CSMS+MS*CSME
(01794) DATA      CSMS+MS*CSML2S
(01795) DATA      CSMS+MS*CSML4S
(01796) DATA      CSMS+MS*CSML5S
(01797) DATA      0
(01798) **      REPEAT FFT POST-SUPPORT MODULE
(01799) **
(01799) **      EVEN
(01800) **
(01801) **RPTFT31 MOVIR      R7, RPTFT-2
(01802) **      MOVIR      R6, 3
(01803) **      MOVIR      R7, R6, RPTSPRM
(01804) **      MOVZM      APSASSS
(01805) **      HOP      RPTFT33
(01806) **
(01807) **RPTFT30 RET      ; RETURN FROM INTERRUPT
(01808) **      EVEN      ; GET TO FULL WORD BOUNDARY

```

0439H 0004
 0439C 0025
 0439D 0000
 0439F 456C
 0439F 4570
 043A0 4574
 043A1 4578
 043A2 457E
 043A3 0000

```

(01809) **RPTFT13 MOVIR W7,RPTSURS-2      ; POINT BEFORE <LOAD URASE> INSTRUCTIO
(01810) ** MOVIR W6,1                      ; 2 FULL WORDS TO TRANSFER TO APS
(01811) ** LPRDCL W7,R6,APSR              ; WRITE TO APS
(01812) ** MOVIR FLGSET+FLGSR1,SYSSFLGS   ; SET R1 -- TURN ON AP
(01813) ** DECM RPTSCT                    ; COUNT THIS FFT
(01814) ** SKP IF DONE                    ; SKIP THE HOP IF DONE
(01815) ** HOP RPTFT12                   ; GO COMPUTE NEXT SET OF BASE ADDR'S
(01816) **
(01817) ** RET                            ; RELEASE THE CPU
(01818) ** EVEN                          ; GET TO FULL WORD BOUNDARY
(01819) ** JMP APSDUNE                    ; ALL DONE -- LET EXEC CONTINUE
(01820) **
(01821) **RPTFT13 MOVIRL R4,RPTSURS        ; GET <LOAD URASE> INSTRUCTION
(01822) ** MOVIRL R6,RPTSURS              ; GET <LOAD URASE> INSTRUCTION
(01823) ** ADDMR R5,RPTSOSP               ; ADD SPACING TO NEXT VECTOR BASE
(01824) ** ADC R4                         ; COMPLETE THE COMPUTATION
(01825) ** ADDMR R7,RPTSISP               ; ADD SPACING TO NEXT VECTOR BASE
(01826) ** ADC R6                         ; COMPLETE THE COMPUTATION
(01827) ** MOVIRL R4,RPTSURS              ; STORE THE INSTRUCTION BACK
(01828) ** MOVIRL R6,RPTSURS              ; STORE THE INSTRUCTION BACK
(01829) ** HOP RPTFT10                   ; GO BACK TO LOOP START FOR RET
(01830) **
(01831) ** EVEN
(01832) **
(01833) **RPTSPRM **RPTSCNT DATA          ; DATA TABLE FOR UPDATING APS CODE
(01834) **RPTSCNT DATA M55              ; CURRENT COUNT FOR FFT
(01835) **RPTSISP DATA M55              ; CURRENT SPACING TO NEXT INPUT VECTOR
(01836) **RPTSOSP DATA M55              ; CURRENT SPACING TO NEXT OUTPUT VECTOR
(01837) ** DATA M55                     ; POSITION HOLDER
(01838) **RPTSURS DATA F'0.0'           ; CURRENT INPUT VECTOR BASE ADDR
(01839) **RPTSURS DATA F'0.0'           ; CURRENT OUTPUT VECTOR BASE ADDR

```


PAGE 54: SNAP-II MAP-300 ARITH. MODULES - PROG. #830101.03 MAY 7, 1980
FFT - AP PROGRAMS

AOR 043BC 00000000 (01825) NOP
AOC 043NE 1000002H (01846) JUMP(CR4FS)
(01884) ;
(01887) ;

```

(01888) ; SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD
(01889) ; SCRAMBLE AND FORWARD RADIX 4 STAGE OF FFT,G1 CLEAR
(01890) ;
(01891) ; FUNCTION
(01892) ; THE FIGHT SUCCESSIVE INPUTS ARE PROVIDED TO LOOP
(01893) ; R00,R01,I01,I00,I02,I03,R03,R02
(01894) ; THE INTERMEDIATE RADIX TWO RESULTS ARE CALCULATED
(01895) ; S0=I00+I01,S1=I00-I01,S2=I02+I03,S3=I02-I03
(01896) ; THE OUTPUTS THEN BEING GIVEN BY
(01897) ; Y0=S0+S2,Y1=S1-JS3,Y2=S0-S2,Y3=S1+JS3
(01898) ; THE ACTUAL OUTPUT SEQUENCE BEING
(01899) ; PY0,R01,IY0,IY1,IY2,IY3,R02,R03
(01900) ;
(01901) ; APU INITIALIZATION
(01902) ;
(01903) ;
(01904) ; CSM4F MOV(10A,A0) A0=R00\R00
(01905) ; MOV(10A,A1) A1=R01\R01
(01906) ; MOV(10A,A3) A3=I01\I01
(01907) ; ADD(A0,A1)\SUR(A0,A1)
(01908) ; MOV(10A,A2) A2=I00\I00
(01909) ; JUMP(CSM4FS)
(01910) ;
(01911) ;
(01912) ; CSM4F, APU INNER LOOP
(01913) ;
(01914) ; B1 MOV(00)\ADD(A5,A6)\MOV(00)\SUR(A5,A7) 00=RY0\RY1
(01915) ; MOV(10A,A0) A0=R00
(01916) ; MOV(00)\SUR(A5,A6)\MOV(00)\ADD(A5,A7) 00=IY0\IY1
(01917) ; MOV(10A,A1) A1=R01
(01918) ; MOV(00)\SUR(A4,A7)\MOV(00)\SUR(A4,A6) 00=IY2\IY3
(01919) ; MOV(10A,A3) A3=I01
(01920) ; MOV(00)\ADD(A0,A1)\MOV(00)\SUR(A0,A1) 00=RY2\RY3
(01921) ; MOV(10A,A2) A2=I00
(01922) ;
(01923) ; CSM4FS MOV(A4)\ADD(A7,A3)\MOV(A4)\SUR(A7,A3) A4=RS0\RS1
(01924) ; MOV(10A,A0) A0=I02
(01925) ; MOV(10A,A1) A1=I03
(01926) ; MOV(A5)\ADD(A0,A1)\MOV(A5)\SUR(A0,A1) A5=IS0\IS1
(01927) ; MOV(10A,A3) A3=R03
(01928) ;
(01929) ;
(01930) ;
(01931) ;

```

PAGE 56: SNAP-11 MAP-100 APITH. MODULES - PROG. BR30101.03 MAY 7, 1980
SCRAMBLE AND FIRST WADIX-4 STAGE, FORWARD

A20 043F6 08F20H2	(01932)	MOV(10A,A2)	A2=RU2
A21 043F8 43564H56	(01933)	MOV(A6),ADD(A7,A3)\MOV(A6),SUR(A2,A3)	A6=IS2\IS3
A22 043FA 47974697	(01934)	MOV(A7),ADD(A4,A7)\MOV(A7),ADD(A4,A6)	A7=RS2\RS3
A23 043FC 90160013	(01935)	JUMPC(81,FW1)	
A24 043FE 45HC4FRC	(01936)	MOV(00),ADD(A5,A6)\MOV(00),SUR(A5,A7)	00=RY0\RY1
A25 043F0 45HC47HC	(01937)	MOV(00),SUR(A5,A6)\MOV(00),ADD(A5,A7)	00=RY0\RY1
A26 043F2 4F9C4F9C	(01938)	MOV(00),SUR(A4,A7)\MOV(00),SUR(A4,A6)	00=RY2\RY3
A27 043F4 089C089C	(01939)	MOV(R,00)	00=RY2\RY3

SUCCESSIVE RADIX-4 STAGES, FORWARD

```

(01944) ;
(01945) ;
(01946) PUSES APS PROGRAM CR4A
(01947) ;
(01948) ; MATHEMATICS
(01949) ;
(01950) ; W=A+HF+CF2+DE3=P+Q
(01951) ; X=A-JHF-CK2+JHF3=R-S
(01952) ; Y=A-RF+CF2-DE3=P-Q
(01953) ; Z=A-JHF-CK2-JHF3=R+S
(01954) ;
(01955) ; P=A+CF2,H=A-CK2
(01956) ; Q=F(R+DF2),S=JL(H-DE2)
(01957) ;
(01958) ; PR=AR+CR+CS2X+CT12X
(01959) ; PT=AT+CT+CS2X-CR12X
(01960) ; PR=AR-CH+CS2X-CT12X
(01961) ; PT=AT-CI+CS2X+CH12X
(01962) ; OR=HR+CSX+H12X+DR+CS3X+D12X
(01963) ; OT=HT+CSX-HR12X+DR+CS3X-D12X
(01964) ; SR=SR+CSX-H12X+DR+CS3X-D12X
(01965) ; ST=SR+CSX+H12X-DR+CS3X-D12X
(01966) ;
(01967) ; SINX STORED IN M1\M5
(01968) ; SIN2X STORED IN M6\M2
(01969) ; SIN3X STORED IN M7\M3
(01970) ; COSX STORED IN M5\M1
(01971) ; COS2X STORED IN M2\M6
(01972) ; COS3X STORED IN M3\M7
(01973) ;
(01974) ;

```

FJFCT


```

(01975) JCR4F-PIPELINE STARTUP
(01976)
(01977)
A28 043F6 202A202A CR4FS CLEAR(AF2)
A29 043F8 202A202A CR4FSA CLEAR(AF1)
A2A 043FA 202A202A CR4FSA CLEAR(AF0)
(01981)
A2B 043FC 1A8016M0 K(1)
A2C 043FE 080908F0 MOV(ZERO,M1)\MOV(TQA,A0)
A2D 04400 08F0080D MOV(TQA,A0)\MOV(ZERO,M5)
(01985)
(01986)
A2F 04402 08D008H9 MOV(R,M5)\MOV(R,M1)
A2F 04404 08A008FF MOV(R,M2)\MOV(R,M6)
A30 04406 08H008FF MOV(R,M3)\MOV(R,M7)
(01990)
A31 04408 08F1080A MOV(TQA,A1)\MOV(ZERO,M2)
A32 0440A 080F08F1 MOV(ZERO,M6)\MOV(TQA,A1)
A33 0440C 080F0808 MOV(ZERO,M7)\MOV(ZERO,M3)
(01994)
A34 0440E 08F808FF MOV(TQA,M0)
A35 04410 8408440 MOV(M0,M6)
A36 04412 0A200720 NEG(A1)\R(A1)
(01998)
A37 04414 10000053 JUMP(CR4FF)
(02000)
(02001) JCR4F:COSINE ENTRY
(02002) ?
A38 04416 08F008F9 CR4FC MOV(TQA,M5)\MOV(TQA,M1)
A39 04418 08F908FF MOV(TQA,M1)\MOV(TQA,M5)
A3A 0441A 08A008A0 MOV(CP,NUCL)
A3B 0441C 08F008FA MOV(TQA,M6)\MOV(TQA,M2)
A3C 0441E 08F008FF MOV(TQA,M2)\MOV(TQA,M6)
A3D 04420 08F008FF MOV(TQA,M3)\MOV(TQA,M7)
A3E 04422 08F008FF MOV(TQA,M7)\MOV(TQA,M3)
(02010) ?
(02011) JCR4F-RUTTERFLY
(02012)
A3F 04424 08F008FF MOV(TQA,M0)
A40 04426 4A744474 MOV(A4),MULN(M0,M7)\MOV(A4),MUL(M0,M7)
A41 04428 4A954455 MOV(A5),SUR(A4,A2)\MOV(A5),ADD(A2,A4)
A42 0442A 08F008FF MOV(TQA,M4)
A43 0442C 4A724472 MOV(A2),SUR(A3,A2)
(02018) ?
A0=DI\DR
SINX=0=M1\M5

COSX=1=M5\M1
COS2X=1=M2\M6
COS3X=1=M3\M7

A1=R1\BR
SIN2X=0=M6\M2
SIN3X=0=M7\M3

M0=CR
P=CRSIN2X\CRCOS2X
R=-R1\RR

COSX TO M5\M1
SINX TO M1\M5

SIN2X TO M6\M2
COS2X TO M2\M6
COS3X TO M3\M7
SIN3X TO M7\M3

DR TO M0
A4=CRCOS2X\CISIN2X
A5=Q1\UR
DI TO M4
A2=CRCOS2X-CRSIN2X
\CISIN2X+CRCOS2X

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A44 0442E R540A590	(02019)	MOV(A0), MUL(M3, M4) \ MOV(A0), MUL(M3, M4)	A0=DRSIN3X \ DRCSIN3X
A45 04430 421R427H	(02020)	MOV(EX0), ADD(A3, A2)	FX=RI \ RR
A46 04432 0456R0R56	(02021)	MOV(EX1, A6)	A6=RR \ RI
A47 04434 0RFR0RFR	(02022)	MOV(10A, M0)	RI TO M0
A48 04436 47D447D4	(02023)	MOV(A4), ADD(A6, A7)	A4=PI \ PR
A49 04438 R431A431	(02024)	MOV(A1), MUL(M0, M5) \ MOV(A1), MUL(M0, M5)	A1=DI \ COS3X \ DISIN3X
A4A 0443A 4R3C403C	(02025)	MOV(00), SUR(A1, A0) \ MOV(00), ADD(A1, A0)	00=ZR \ ZI
A4B 0443C 0RRC0RRC	(02026)	MOV(10A, M4)	RR TO M4
A4C 0443F 4D904D90	(02027)	MOV(A0), SUR(A4, A5)	A0=DI \ COS3X - DRSIN3X \ DISIN3X + DRCSIN3X
A4D 04440 A491R491	(02028)	MOV(A1), MUL(M1, M4) \ MOV(A1), MUL(M1, M4)	
A4E 04442 4R0C4F0C	(02029)	MOV(00), SUR(A6, A7)	A1=RICOSX \ RISINX
A4F 04444 0RFR0RFR	(02030)	MOV(10A, M0)	00=YI \ YR
A50 04446 450C450C	(02031)	MOV(00), ADD(A4, A5)	CR TO M0
A51 04448 A452R452	(02032)	MOV(A2), MUL(M0, M6) \ MOV(A2), MUL(M0, M6)	00=XR \ XI
A52 0444A 495C429C	(02033)	MOV(00), SUR(A2, A1) \ MOV(00), ADD(A1, A2)	
A53 0444C 0RRC0RRC	(02034)	MOV(10A, M4)	A2=ARSINX \ ARCOSX
A54 0444F 4111R431	(02035)	MOV(A1), ADD(A0, A1) \ MOV(A1), SUR(A1, A0)	00=WR \ WI
A55 04450 R512A512	(02036)	MOV(A2), MUL(M2, M4) \ MOV(A2), MUL(M2, M4)	CI TO M4
A56 04452 0RFR4037	(02037)	MOV(10A, A3) \ MOV(A7), ADD(A1, A0)	A1=RRSINX - RICUSX \ RRCUSX + RISINX
A57 04454 49170R53	(02038)	MOV(A7), SUR(A0, A1) \ MOV(10A, A1)	
A58 04456 9028003F	(02039)	JUMPC(CR4FH, AF0), CLEAR	A2=CRSIN2X \ CRCSIN2X
A59 04458 90240038	(02040)	JUMPC(CR4FC, AF1), CLEAR	A3=AI \ A7=SI
A5A 0445A 0RRA0RRA	(02041)	MOV(P, A4)	A7=SR \ A3=AR
A5B 0445C 4A954455	(02042)	MOV(A5), SUR(A4, A2) \ MOV(A5), ADD(A2, A4)	AF1: STAGE DONE
A5C 0445E 4A72A77	(02043)	MOV(A2), SUR(A3, A2)	AF0: COSINES USED
	(02044)		
	(02045)		
	(02046)		
	(02047)		
	(02048)		
	(02049)		
	(02050)		
	(02051)		
	(02052)		
	(02053)		
	(02054)		
	(02055)		
	(02056)		
	(02057)		
	(02058)		
	(02059)		
	(02060)		
	(02061)		
	(02062)		

[illegible]

PAGE 62: SNAP-11 MAP-300 ARITH. MODULES - PROG. #R10101.03 MAY 7, 1980
SCRAMBLE AND FIRST RADIX-2 STAGE, REVERSE

(02134) :

(02137) : FUNCTION

AOD 0449C 08F00HEO (02150) CSMAT (02150)

```
(0215H) ; (SM41, APU INNER LOOP
```

A15 044AC AFNC AFHC (02163)

A1A 044HR 43544M54 (02172) CSM41S

AIE 044AF 41154915 (02176)

PAGE 64: SNAP-II MAP-300 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980
SCRAMBLE AND FIRST RADIX-4 STAGE, REVERSE

A20 044C2 08F208F2 (02178)	MOV(10A,A2)	A2=RU2
A21 044C4 43564856 (02179)	MOV(A6),ADD(A2,A3)\MOV(A6),SUB(A2,A3)	A6=IS2\IS3
A22 044C6 47974847 (02180)	MOV(A7),ADD(A4,A7)\MOV(A7),SUB(A4,A6)	A7=RS2\RS3
A23 044C8 90160013 (02181)	JUMPC(R1,FWI)	
A24 044CA 46HC478C (02182)	MOV(00),ADD(A5,A6)\MOV(00),ADD(A5,A7)	00=RY0\RY1
A25 044CC 4FHC488C (02183)	MOV(00),SUB(A5,A6)\MOV(00),SUB(A5,A7)	00=IY0\IY1
A26 044CE 4F4C498C (02184)	MOV(00),SUB(A4,A7)\MOV(00),ADD(A4,A6)	00=IY2\IY3
A27 044D0 089C089C (02185)	MOV(R,00)	00=RY2\RY3

```

(02190) ;      SUCCESSIVE RADIX-4 STAGES, REVERSE
(02191) ;
(02192) ;USES APS PROGRAM CMAA
(02193) ;
(02194) ;MATHEMATICS
(02195) ;
(02196) ; W=A+R*CF2+DE3=PA+Q
(02197) ; X=A+J*H-CE2-J*DE3=H+S
(02198) ; Y=A-H*CF2-DE3=P-Q
(02199) ; Z=A-J*H-CE2+J*DE3=H-S
(02200) ;
(02201) ; P=A+CF2,P=A-CE2
(02202) ; Q=F(R+DE2),S=JF(R-DE2)
(02203) ;
(02204) ; PR=AR+CR*CS2X-CISIN2X
(02205) ; PI=AI+CI*CS2X+CRSIN2X
(02206) ; PH=AP-CP*CS2X+CSIN2X
(02207) ; RI=AI-CI*CS2X-CRSIN2X
(02208) ; QR=PR*CSX-HISINX+DR*CSX+DISIN3X
(02209) ; OI=RI*CSX+HSINX+DI*CSX+DRSIN3X
(02210) ; SH=RI*CSX-HRSINX+DI*CSX-DRSIN3X
(02211) ; SI=PR*CSX-HISINX-DR*CSX+DISIN3X
(02212) ;
(02213) ; SINX STORED IN M1\M5
(02214) ; SIN2X STORED IN M6\M2
(02215) ; SIN3X STORED IN M7\M3
(02216) ; COSX STORED IN M5\M1
(02217) ; COS2X STORED IN M2\M6
(02218) ; COS3X STORED IN M3\M7
(02219) ;
(02220) ;      FJECT

```



```

(02221) ;CR41-PIPELINE STARTUP
(02222)
(02223)
A28 04402 202A202A (02224) CR41S CLEAR(AF2)
A29 04404 202A202A (02225) CR41SA CLEAR(AF1)
A2A 04406 202A202A (02226) CLEAR(AF0)
(02227)
A2H 04408 1A5016M0 (02228) K(1)
A2C 0440A 08090M0 (02229) MOV(ZERO,M1)\MOV(10A,A0)
A2D 0440C 08090M0 (02230) MOV(10A,A0)\MOV(ZERO,M5)
(02231)
(02232)
A2E 0440F 08090M0 (02233) MOV(R,M5)\MOV(R,M1)
A2F 04410 08090M0 (02234) MOV(R,M2)\MOV(R,M6)
A30 04412 08090M0 (02235) MOV(R,M3)\MOV(R,M7)
(02236)
A31 04414 08090M0 (02237) MOV(10A,A1)\MOV(ZERO,M2)
A32 04416 08090M0 (02238) MOV(ZERO,M6)\MOV(10A,A1)
A33 04418 08090M0 (02239) MOV(ZERO,M7)\MOV(ZERO,M3)
(02240)
A34 0441A 08090M0 (02241) MOV(10A,M0)
A35 0441C 08090M0 (02242) MUL(M0,M6)
A36 0441E 0A200220 (02243) NEG(A1)\N(A1)
A37 04420 10000053 (02244) JUMP(CR41E)
(02245)
(02246)
(02247) ;CR41-CUSINE ENTRY
(02248) ?
A38 04422 08090M0 (02249) MOV(10A,M5)\MOV(10A,M1)
A39 04424 08090M0 (02250) MOV(10A,M1)\MOV(10A,M5)
A3A 04426 08090M0 (02251) MOV(P, NULL)
A3B 04428 08090M0 (02252) MOV(10A,M6)\MOV(10A,M2)
A3C 0442A 08090M0 (02253) MOV(10A,M2)\MOV(10A,M6)
A3D 0442C 08090M0 (02254) MOV(10A,M3)\MOV(10A,M7)
A3E 0442E 08090M0 (02255) MOV(10A,M7)\MOV(10A,M3)
(02256) ?
(02257) ;CR41-PUTTEFLY
(02258)
A3F 04430 08090M0 (02259) MOV(10A,M0)
A40 04432 08090M0 (02260) MOV(A4)\MUL(M0,M7)\MOV(A4)\MUL(M0,M7)
A41 04434 08090M0 (02261) MOV(A5)\SUB(A4,A2)\MOV(A5)\ADD(A2,A4)
A42 04436 08090M0 (02262) MOV(10A,M4)
A43 04438 08090M0 (02263) MOV(A2)\SUB(A3,A2)
(02264) ?
A0=01\DR
SINX=0=M1\M5

COSX=1=M5\M1
COS2X=1=M2\M6
COS3X=1=M3\M7

A1=R1\RR
SIN2X=0=M6\M2
SIN3X=0=M7\M3

MO=CR
P=CRSIN2X\CRCOS2X
R=-R1\RR

COSX TO M5\M1
SINX TO M1\M5

SIN2X TO M6\M2
COS2X TO M2\M6
COS3X TO M3\M7
SIN3X TO M7\M3

DR TO M0
A4=CRSIN2X\CRSIN2X
A5=01\OR
01 TO M4
A2=CRCOS2X+CRSIN2X
\CRSIN2X+CRCOS2X

```


APS PROGRAMS

(02340) ; APS PROGRAMS

(02341) ;

(02342) ;

(02343) ; START OF HEADER BLOCK

(02344) ;

EVEN

(02345)

0455C 00004664 (02346)

0455F 00004564 (02347)

04560 0000 (02348)

04561 0100 (02349)

04562 00000000 (02350)

(02351)

(02352) ;

(02353) ;

START ON WORD BOUNDARY
PTR TO CONSTR INSTR BLOCK (NONE)
PTR TO SCALAR BLOCK (NONE)
NO SCALARS
MODULE SIZE
PTR TO CHAIN ANCHOR
END OF BOUNDARY

ADDR CSMS1
ADDR CSMS+2*CSMS
DATA 0
DATA CH4AS7
ADDR CH4ASA
EVEN

```

(02354) : CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)
(02355) : APS PROGRAM PROVIDING SCRAMBLE FOR COMPLEX FFT
(02356) : MAY BE USED WITH THE MAP-300 APU PROGRAMS
(02357) : (CSM2(Y,U), CSM4(Y,U), CSM4(Y,U))
(02358) :
(02359) : RESTRICTIONS
(02360) : IN PLACE OPERATION NOT PERMITTED
(02361) : BUFFER SIZES MUST BE 1024 OR LESS
(02362) : Y BUFFER MUST BE COMPACT 32 BIT FLOATING
(02363) :
(02364) :
(02365) : BINDING PARAMETERS
(02366) :
(02367) : N=8 OF POINTS IN FFT (USIZE)
(02368) : HU=USER*N/2
(02369) : QU=HU/2
(02370) : APS-INPUT ADDRESS SEQUENCE
(02371) : RU(K), RU(K+N/2), IU(K+N/2), IU(K), IU(K+N/4)
(02372) : IU(K+N/4), RU(K+N/4), RU(K+N/4)), FOR 0<K<N/4
(02373) : THUS PROVIDING REVERSAL OF TWO BITS
(02374) :
(02375) : APS-OUTPUT ADDRESS SEQUENCE
(02376) : RU(J), RU(J+1), IU(J), IU(J+1), RU(J+2),
(02377) : RU(J+3), IU(J+2), IU(J+3)
(02378) : WHERE JE BIT REVERSAL OF K
(02379) : THE DIFFERENCE, D(K) = J(K+1)-J(K) IS
(02380) : PROVIDED BY THE P3 SUBROUTINE
(02381) :
(02382) :
(02383) : CSM5 REGIN APS(CSM)
(02384) :
(02385) : CSM55 JSN(CSMU,P2)
(02386) :
(02387) : CSM1.65 LOAD(HR0,MSS,I,TF)
(02388) : CSM1.05 LOAD(HR1,MSS)
(02389) : JUMP(CSMF,PA),SET
(02390) :
(02391) : CSM1. SUR(HR0,MSS,TF)
(02392) :
(02393) : CSM1.15 SUR(HR0,MSS,TF)
(02394) : CSM4 ADD(HR0,MSS,TF)
(02395) : ADD(HR0,2,TF)
(02396) : CSM1.25 SUR(HR0,MSS,TF)
(02397) :

INPT RU(0) (URASE ROUND)
(USIZE-1 ROUND)
TURN ON APU

INPT RU(K+N/4) (HU ROUND)

INPT RU(K) (OU-USER ROUND)
INPT RU(K+N/2) (HU ROUND)
INPT IU(K+N/2)
INPT IU(K) (HU ROUND)

```

PAGE 71: SNAP-II MAP-100 ARITH. MODULES - PROG. #R30101.03 MAY 7, 1980
CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)

A09 04576 12H00000 (02398) CSM1.3S	ADD(RK0,MSS,TF)	INPT IU(K+N/4) (QU ROUND)
A0A 04578 14H00000 (02399) CSM1.4S	ADD(RK0,MSS,TF)	INPT IU(K+N/4) (HU ROUND)
A0H 0457A 16H20002 (02400)	SUB(RK0,2,TF)	INPT RU(K+N/4)
A0C 0457C 181904H4 (02401) ?		
A0C 0457C 181904H4 (02402)	SUB(RK1,4),JUMPP(CSM1.)	
A0D 0457E 1A20000 (02403) ?		
A0E 0457F 1A20000 (02404) CSM1.5S	SUB(RK0,MSS,TF)	INPT RU(LAST) (HU ROUND)
A0E 04580 1C305977 (02405)	JUMP(CR4AF,WI),SFT	
(02406) ?		

PAGE 72: SNAP-11 MAP-300 APITH. MODULES - PROG. W30101.03 MAY 7, 1980
CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)

(02407) ; CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)
(02408) ;
(02409) ; DATA POINT ADDRESS AT ENTRY 4*(J(K)-N)+PHASE
(02410) ; DATA OUTPUT ADDRESS GENERATED (4J(K+1))+PHASE
(02411) ;
(02412) ; ITERATION EQUATION, J=J(K)=K, HIT REVERSED
(02413) ; 4J(K+1)=4*(J(K)-N)+DM
(02414) ; WHENF=NUMBER OF TRAILING 1'S IN (K)
(02415) ;
(02416) ; BINDING CONSTANTS
(02417) ;
(02418) ;
(02419) ;
(02420) ;
(02421) ;
(02422) ;
(02423) ;
(02424) ;
(02425) ;
(02426) ;
(02427) ;

P0=4*(3*N/2)
P1=D0/2
P2=D1/2
P3=D2/2
P4=D3/2
P5=D4/2
P6=D5/2
P7=D6/2

LOADS P1 FOR CR4A

(D0 ROUND)
(D1 ROUND)
(D0 ROUND)
(D2 ROUND)
(D0 ROUND)
(D1 ROUND)
(D0 ROUND)

JSN(ANGLE,P1)

ADF 04582 1F101840 (02428) SCRM

A10 04584 20H00000 (02430) SCRM0 ADD(HW0,MSS,TF,C)
A11 04586 22H00000 (02431) SCRM1S ADD(HW0,MSS,TF,C)
A12 04588 24H00000 (02432) SCRM2S ADD(HW0,MSS,TF,C)
A13 0458A 26H00000 (02433) SCRM3S ADD(HW0,MSS,TF,C)
A14 0458C 28H00000 (02434) SCRM4S ADD(HW0,MSS,TF,C)
A15 0458E 2AH00000 (02435) SCRM5S ADD(HW0,MSS,TF,C)
A16 04590 2CH00000 (02436) SCRM6S ADD(HW0,MSS,TF,C)
A17 04592 2F201AFH (02438) JUMPS(SCRM4,AF0),CLEAR
A18 04594 30H00000 (02439) ADD(HW0,MSS,TF,C)
A19 04596 37301068 (02440) JUMP(SCRM0,AF0),SET

A1A 04598 3A2010F9 (02442) SCRM4 JUMPS(SCRM5,AF1),CLEAR
A1M 0459A 3CH00000 (02443) SCRM6S ADD(HW0,MSS,TF,C)
A1C 0459C 3H301069 (02444) JUMP(SCRM0,AF1),SET

A1D 0459E 3A2020FA (02446) ;
A1F 045A0 3CH00000 (02447) SCRM5 JUMPS(SCRM6,AF2),CLEAR
A1F 045A2 3F30106A (02449) SCRM6S ADD(HW0,MSS,TF,C)
JUMP(SCRM0,AF2),SET

(D3 ROUND)

(D4 ROUND)

(D5 ROUND)

PAGE 73: SNAP-II MAP-100 ARITH. MODULES - PROG. 810101.03 MAY 7, 1980
CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)

A20 045A4 402023F0	(02451) SCRM6	JUMPS(SCRM7,AF3),CLEAR	
A21 045A6 420F0000	(02452) SCRM105	ADD(HW0,MSS,TH,C)	106 ROUND)
A22 045A8 4430106H	(02453)	JUMP(SCRM0,AF3),SFT	
	(02454) ?		
A23 045AA 460F0000	(02455) SCRM7	ADD(HW0,MSS,TH,C)	107 ROUND)
A24 045AC 48001060	(02456)	JUMP(SCRM0)	
	(02457) ?		

PAGE 74: SNAP-11 MAP-300 AMITH. MODULES - PROG. #R10101.03 MAY 7, 1980
CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)

```

(02458) ? CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)
(02459) ?
(02460) ?
A25 045AE 4A300F40 CSM0 JSM(SCRM,P3)
A26 045B0 4C000000 CSM01S LOAD(HW0,MSS,L,TF)
A27 045B2 4F112953 CSM01S MOVH(RW1,RR1),JUMP(CSM0F)
A28 045B4 50060000 CSM0L SUR(HW0,MSS,C)
(02466) ?
(02467) ? PRIOR TO JUMP TO P3,HW0=4J(K)-4N
(02468) ? SCRAMBLE SUBROUTINE (OUTPUTS RW(J)
(02469) ? LEAVING HW0=4J(K+1)
(02470)
A29 045B6 528A0004 CSM0F ADD(HW0,4,TF)
A2A 045B8 54B20002 CSM0F SUR(HW0,2,TF)
A2B 045BA 568A0004 CSM0F ADD(HW0,4,TF)
(02474) ?
A2C 045BC 588A0004 CSM0F ADD(HW0,4,TF)
A2D 045BE 5A8A0004 CSM0F ADD(HW0,4,TF)
A2E 045C0 5C820006 CSM0F SUR(HW0,6,TF)
A2F 045C2 5F8A0004 CSM0F ADD(HW0,4,TF)
(02479) ?
A30 045C4 601128H4 CSM0L SUR1(RW1,4),JUMPP(CSM0L)
(02481) ?

```

OTPT RW(0) (WHAPE ROUND)
RW1=WSIZE-1

(4N+12 ROUND)

OTPT RW(J+1)
OTPT IW(J)
OTPT IW(J+1)

OTPT IW(J+2)
OTPT IW(J+3)
OTPT RW(J+2)
OTPT RW(J+3)

PAGE 75: SNAP-11 MAP-300 ARITH. MODULES - PROG. #H0101.03 MAY 7, 1980

SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)

(02482) ; SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)

(02483) ; PROGRAM ASSUMES SCRAMBLE COMPLETED

(02484) ;

(02485) ;

(02486) ; 12/14/77

(02487) ; SCRAMBLE LEAVES APS AS FOLLOWS

(02488) ; INPUT - LAST COMMAND, JUMPICR4AP, W11, SET

(02489) ; INPUT-P1 LOCATED AT ANGLE

(02490) ; OUTPUT - ACTIVE, IN P2

(02491) ;

(02492) ; SS=STAGE SEPARATION, DATA SEPARATION A H C D

(02493) ; STARTING VALUES(4)APF

(02494) ; SSI=1, SCRAMBLE ONLY PRECEDING N=4^M

(02495) ; SSI=2, SCRAMBLE PLUS RADIX2 PRECEDING N=2^4^M

(02496) ; SSI=3, SCRAMBLE PLUS RADIX3 PRECEDING N=3^4^M

(02497) ; SSI=4, SCRAMBLE PLUS RADIX4 PRECEDING N=4^4^M

(02498) ; SSI=6, SCRAMBLE PLUS RADIX6 PRECEDING N=6^4^M

(02499) ;

(02500) ;

(02501) ; COSINE TABLE IS A REAL BUFFER WITH CONTENTS:

(02502) ; CT(K)=COS(2*PI*K/CSIZE)

(02503) ; RESTRICTION.

(02504) ; CSIZE MUST BE MULTIPLE OF N, FFT SIZE

(02505) ;

(02506) ;

(02507) ;

(02508) ;

(02509) ; MPI=CSIZE*(CSIZE/4)

(02510) ; 90 DEG SEPARATION

(02511) ;

(02512) ; REGISTER USAGE

(02513) ; RRO/RW0

(02514) ; RW1/RW1

(02515) ; RW2

(02516) ; RW3

(02517) ; RR2

(02518) ; RR3

(02519) ; FJFCT

DATA ADDRESSES

OF USES OF A COSINE, COUNTER

OF COSINES USED, COUNTER

STAGE SEPARATION

SEP BETWEEN COSINES WITHIN A SET

SEP BETWEEN SETS OF COSINES

A31 045C6 62700000	(02518) CH4A01 LOAD(RW3,MSS)	{SSI ROUND}
	(02520) ?	
	(02521) ? CP4A-APS OUTPUT PROGRAM-STAGE INITIALIZATION	
	(02522) ?	
	(02523) ?	
A32 045C8 6410024	(02524) CH4A02 ADDR(RW3,RW3)	
A33 045CA 6631024	(02525) ADDR(RW3,RW3)	
A34 045CC 6840000	(02526) CH4A1S LOAD(RW0,MSS)	SET SEPARATION FOR STAGE
A35 045CE 6A01034	(02527) SURD(RW0,4)	SET RW0=WR-4 {WPHASE ROUND}
A36 045D0 6C21016	(02528) MOV(RW2,RW3)	SET WPHASE-4
A37 045D2 6E090010	(02529) MOV(RW0,RW0)	SET UP INPUT RAS
A38 045D4 70200037	(02530) CLEAR(W1)	START UP INPUT
	(02531) ?	
	(02532) ? TEST FOR LAST STAGE	
	(02533) ? IF SD, CHANGE OUTPUT TO Y BUFFER INSTEAD	
	(02534) ? OF THE W BUFFER	
	(02535) ?	
A39 045D6 72500000	(02536) CH4A3S LOAD(RW1, MSS)	{USIZE-1 ROUND}
A3A 045D8 741149A6	(02537) SURD(RW1, RW3), JUMP(CR4A03) JUMP IF NOT LAST STAGE	
	(02538) ?	
	(02539) ? LAST STAGE OUTPUT ADDRESSES	
	(02540) ?	
A3B 045DA 76400000	(02541) CH4A10S LOAD(RW0, MSS)	{WPHASE ROUND}
A3C 045DC 78500000	(02542) CR4A11S LOAD(RW1, MSS)	{YSEP*YSIZE/4 ROUND}
	(02543) ?	
A3D 045DE 7ADA0000	(02544) CH4A0F ADD(RW0, MSS)	{YSEP*YSIZE ROUND}
	(02545) ?	
A3E 045F0 7C810022	(02546) SURD(RW0,RW1,TF)	ZH
A3F 045F2 7E8A0002	(02547) ADD(RW0,2,TF)	ZI
A40 045F4 80810022	(02548) SURD(RW0,RW1,TF)	YI
A41 045F6 82820002	(02549) SURD(RW0,2,TF)	YR
A42 045F8 84810022	(02550) SURD(RW0,RW1,TF)	XR
A43 045FA 868A0002	(02551) ADD(RW0,2,TF)	XI
A44 045FC 88810022	(02552) SURD(RW0,RW1,TF)	WI
A45 045FE 8A820002	(02553) SURD(RW0,2,TF)	WR
	(02554) ?	
	(02555) ? TEST FFT DONE	
	(02556) ?	
A46 045F0 8C8A0000	(02557) CR4A12S ADD(RW0,MSS)	{YSEP ROUND}
A47 045F2 8E203DAA	(02558) JUMP(CR4A0F,AF2),CLEAR	HAUT APS OUTPUT
A48 045F4 90203470	(02559) JUMP(CR4A1S,R0),CLEAR	
	(02560) ?	
A49 045F6 92500000	(02561) CH4A03 LOAD(RW1,MSS)	SET BUTTERFLY COUNT {USIZE-1 ROUND}

PAGE 77: SNAP-11 MAP-300 ARITH. MODULES - PRG. #30101.03 MAY 7, 1980
SUCCESSIVE RADIX-4 STACKS (OUTPUT - P2)

SUCCESSIVE RADIX-4 STAGES (INPUT - P0)

```

A5R 04614 H0300037 (02540) CR4A1 SFT(W1)
(02540) ;STAGE INITIALIZATION
(02591) ?
A5R 04616 H2600000 (02592) CR4AF L0AD(HR2,0)
A5A 04618 H4300015 (02593) MOVH(HR3,HR2,C)
SFT COSINE VALUE=0

```

(02595) ; ANGLE SURROUTING, P1, INSERTS COSINE SEPARATION
(02596) ; FOR STAGE INTO HQ

```

A5H 0461A H#216571 {02599)
      SIN1.(HW2,1),JUMP(CP4A3)
      RW/=81F 'COSINFS=1

```

A\$C	0461C	NNNNnnn37	(02601) CHA42	SINH(CHR0,2,TF)
			(02602) %COSINE	
			(02603) ?	
A\$D	0461F	NAS00000	(02604) CHA4\$	LOAD(PRR1,MSS,I.)
A\$E	04620	BQ'90002F	(02605)	ADDR(CHR2,PRI)
				(CHASE MODIFI- HYZEMENT ANGLE)
				AM

[illegible]

A65	0462C	CA040000	(02612)	CH443	ADD(HR0,MSS)	SPT HR0 TO 4N+CSB [4N+4 ROUND]
A66	0462F	CA040000	(02613)	CH443		
A66	04630	CC500000	(02614)	CH443	LOAD(HR1,MSS)	SPT CSBINE USE COUNTER [USIZE=1 ROUND]

(02616) : CHAA-APS BUTTERFLY INPUT
(02617) :

[illegible]

```
(02620)
(02627) ; TEST IF NEW COSINE NEEDED, ELSE INPUT NEXT 4 POINTS
```

A6F 04640 DC1976A6 (02629)
A6F 04642 DF30002R (02630)
SUH (HPI, FW3), JUMPP (CH4A5)
SFT (AFU)

TELL, API COSINES COMING

PAGE 79: SNAP-11 MAP-100 ARITH. MODULES - PROG. 0030101.03 MAY 7, 1980
SUCCESSIVE RADIX-4 STAGES (INPUT - 00)

```

(02631) ; TEST IF STAGE DONE, ELSE GET NEW COSINES
(02632)
A70 04644 10215C44 (02633)      SURL(RW2,4),JUMPP(CR4A2)      TELL API STAGE DONE
A71 04646 12300029 (02634)      SET(API)
(02635) ;***** CHANGE VALUE HERE TO STOP SHORT *****
(02636) ;TO STOP SHORT 1 STAGE, REND USIZE/4-1 HERE
A72 04648 14500000 (02637)      CHA49S  LOAD(RR1,MSS)      LEFTSIZE-1 ROUND)
A73 0464A 16490032 (02638)      SURL(RR0,2,TF)      LAST DATA POINT
(02639)
(02640) ; TEST IF FFT DONE, ELSE START NEXT STAGE
(02641)
A74 0464C 181958A6 (02642)      SURL(RR1,RW3),JUMPP(CR4A1)      GO HACK
A75 0464E 1A205071 (02643)      JUMP(CR4AF,RT),CLEAR      HALT APS INPUT
(02644)
A76 04650 1C890032 (02645)      CR4AS  SURL(RR0,2,TF)      AP
A77 04652 1F006760 (02646)      JUMP(CR4A4)      BACK TO BUTTERFLY
(02647)

```

PAGE 80: SNAP-II MAP-100 ARITH. MODULES - PROG. BR40101.03 MAY 7, 1980

ANGULAR SEPARATION SUBROUTINE (INPUT - P1)

(0264H) ; ANGULAR SEPARATION SUBROUTINE (INPUT - P1)

(02649) ; STANG=HPI/SSI

(02650) ; S2ANG=SIANG/4

(02651) ; S3ANG=S2ANG/4

(02652) ; S4ANG=S3ANG/4

(02653) ; S5ANG=S4ANG/4

(02654) ; S6ANG=S5ANG/4

(02655) ; S7ANG=S6ANG/4

(02656) ;

(02657) ;

A78 04654 F0300030 ANGLE SFT(R0)

A79 04656 F23F0000 ANGLE15 ADD(HR3,MSS,C)

A7A 04658 F43F0000 ANGLE25 ADD(HR3,MSS,C)

A7B 0465A F63F0000 ANGLE35 ADD(HR3,MSS,C)

A7C 0465C F83F0000 ANGLE45 ADD(HR3,MSS,C)

A7D 0465E FA3F0000 ANGLE55 ADD(HR3,MSS,C)

A7E 04660 FC3F0000 ANGLE65 ADD(HR3,MSS,C)

A7F 04662 FE3F0000 ANGLE75 ADD(HR3,MSS,C)

(02666) ;

(02667) ; CH4ASA=BC

(02668) ; FND

(02669) ;

00004664 (02670) CSMS1 BL = BL + 2*0

(02671) ;

00000100 (02672) CH4ASZ = BL - CSMS

(02673) ;

(02674) ;

(S1ANG ROUND)

(S2ANG ROUND)

(S3ANG ROUND)

(S4ANG ROUND)

(S5ANG ROUND)

(S6ANG ROUND)

(S7ANG ROUND)

ASSIGN VALUE TO CHAIN

#A-1 END OF MODULE

ADDRESS	OPERATION	COMMENT
02675	VMOV	
02676	*	
02677	AP ROUTINES FOR VMOV	
02678	*	
02679	*	
02680	*	
02681	APU3-VMOV	
02682	*	
02683	*	WINDS TO APS3-V1124
02684	*	
02685	*	EQ. Y=U
02686	*	
02687	*	
02688	04664 0000	START ON WORD BOUNDARY
02689	04665 0004	START ADDRESS
02690	*	SIZE
02691	VMOV	START OF APU MODULE
02692	BA=0	
02693	VMOVSSA	DUMMY SCALAR 1 TO DUMMY OUT 1
02694	MOV(10,00)\NOP	DUMMY SCALAR 1 TO DUMMY OUT 2
02695	MOV(10,00)\NOP	DUMMY SCALAR 1 TO DUMMY OUT 3
02696	MOV(10A,00)\NOP	DUMMY SCALAR 2 TO DUMMY OUT 4
02697	MOV(10A,00)\NOP	
02698	*	
02699	BA=BA	LOOP TO MOVE U INTO Y
02700	MOV(10A,00)\NOP	
02701	JUMPC(B1,F0)	HALT
02702	CLFAR(BA)	
02703	*	
02704	NOP	
02705	JUMPC(VMOVSSA)	IF RESTART WITHOUT RELOAD
02706	VMOVSSZ=BA-VMOVSSA	
02707	FND	

PAGE 82: SNAP-IT MAP-300 ARITH. MODULES - PROG. BH30101.03 MAY 7, 1980

DEFINING TOP OF MODULE

(02708) DEFINING TOP OF MODULE

(02709) *

0000467H (02710) TUESDAY=81.

(02711) *

(02712) *

(02713)

END

0467H

DEFINING TOP LOCATION OF MODULE

APDSUNC:	00018	(00027)	(00165)	(00684)	(00701)	(00706)	(00711)	(00716)	(01724)	(01728)
ANGLE1:	00079	(01637)	(02659)							
ANGLE2:	00078	(01640)	(02660)							
ANGLE3:	00074	(01643)	(02661)							
ANGLE4:	00070	(01646)	(02662)							
ANGLE5:	00070	(01649)	(02663)							
ANGLE6:	00074	(01652)	(02664)							
ANGLE7:	00074	(01655)	(02665)							
ANGLE8:	00074	(02428)	(02658)							
APSSSS:	00245	(00028)								
APSHND:	00018	(00029)								
APSHND0:	00018	(00030)								
APSHND1:	00020	(00031)	(01756)							
APSHND2:	00240	(00032)	(01622)							
APSSSC:	00248	(00033)								
APSDNF:	00042	(00034)								
APSGO:	00000	(00035)								
APSG1:	00000	(00036)								
APSGRF:	00010	(00037)								
APSSAD:	00008	(00038)								
APSSCLR:	00000	(00039)								
APSSSS:	00009	(00040)								
APSSHND:	00004	(00041)								
APSSNULL:	00000	(00042)								
		(00257)	(00180)	(00193)	(00197)	(00201)	(00221)	(00225)	(00229)	(00233)
		(00381)	(00261)	(00265)	(00293)	(00297)	(00321)	(00325)	(00329)	(00351)
		(00477)	(00385)	(00413)	(00417)	(00421)	(00425)	(00445)	(00449)	(00457)
		(00517)	(00481)	(00485)	(00489)	(00493)	(00497)	(00501)	(00505)	(00513)
		(00557)	(00521)	(00525)	(00529)	(00533)	(00537)	(00541)	(00545)	(00553)
		(00597)	(00561)	(00565)	(00569)	(00573)	(00577)	(00581)	(00585)	(00593)
		(00637)	(00601)	(00605)	(00609)	(00613)	(00617)	(00621)	(00625)	(00633)
		(00677)	(00641)	(00645)	(00649)	(00653)	(00657)	(00661)	(00665)	(00673)
APSW:	1FFC0	(00042)								
APUSNULL:	00000	(00168)	(00188)	(00192)	(00196)	(00200)	(00212)	(00220)	(00224)	(00232)
		(00252)	(00256)	(00260)	(00264)	(00268)	(00272)	(00276)	(00280)	(00284)
		(00456)	(00360)	(00384)	(00417)	(00416)	(00420)	(00424)	(00428)	(00432)
		(00512)	(00456)	(00480)	(00484)	(00488)	(00492)	(00496)	(00500)	(00504)
		(00552)	(00516)	(00520)	(00524)	(00528)	(00532)	(00536)	(00540)	(00544)
		(00592)	(00556)	(00560)	(00564)	(00568)	(00572)	(00576)	(00580)	(00584)
		(00632)	(00596)	(00600)	(00604)	(00608)	(00612)	(00616)	(00620)	(00624)
		(00672)	(00636)	(00640)	(00644)	(00648)	(00652)	(00656)	(00660)	(00664)
			(00676)	(00680)						
RCTSD:	00604	(00044)	(01374)	(01534)	(01603)					
RCTSAT:	00686	(00045)	(01373)							

RCTSR:	00002 (00046)	(01598)	(01669)	(01685)	(01707)
RITSG:	00000 (00047)				
U C1120S:	00000 (00393)				
U C1124AS:	00000 (00373)				
U C1124MS:	00000 (00377)				
U C2120S:	00000 (00389)				
U C2124AS:	00000 (00365)	(00369)			
U CVMUUS:	00000 (00368)				
CLMSGGCI:	00742 (00049)				
U CPILARS:	00000 (00376)				
CR4SSZ:	00060 (01848)	(02084)			
CR4ASA:	00000 (02350)	(02667)			
CR4ASZ:	00100 (02349)	(02672)			
CR4A1:	00054 (02589)	(02642)			
CR4A1S:	00034 (01675)	(02526)	(02559)	(02584)	
CR4A10S:	00038 (01708)	(02541)			
CR4A11S:	00030 (01588)	(02542)			
CR4A12S:	00046 (01564)	(02557)			
CR4A13S:	00055 (01444)	(02582)			
CR4A2:	00050 (02601)	(02533)			
CR4A2S:	00044 (01522)	(02562)			
CR4A3:	00065 (01523)	(02599)	(02613)		
CR4A3S:	00039 (01415)	(02536)			
CR4A4:	00067 (02619)	(02646)			
CR4A4S:	00050 (01599)	(02604)			
CR4A5:	00076 (02629)	(02645)			
CR4A5S:	00060 (01611)	(02608)			
CR4A6S:	00062 (01612)	(02610)			
CR4A7S:	00064 (01613)	(02612)			
CR4A8S:	00066 (01416)	(02614)			
CR4A9S:	00072 (01443)	(02637)			
CR4AF:	00059 (02405)	(02592)	(02643)		
CR4A01:	00031 (01625)	(01629)	(02519)		
CR4A02:	00032 (02524)	(02583)			
CR4A03:	00049 (01413)	(02537)	(02561)	(02578)	
CR4A04:	00044 (02565)	(02575)			
CR4A0F:	00030 (01575)	(01577)	(01579)	(01582)	(02544)
CR4FR:	00034 (02013)	(02052)			
CR4FC:	00034 (02003)	(02053)			
CR4FF:	00053 (01949)	(02044)			
CR4FS:	00024 (01886)	(01978)			
CR4FSA:	00029 (01474)	(02078)			
CR4ISSZ:	00060 (02108)	(02130)			
CR4IB:	00034 (02259)	(02294)			

CRAIC:	0003M (02249) (02299)								
CRAIF:	0004A (02245) (02290)								
CRAIS:	0002M (02133) (02224)								
CRAISA:	0002G (02225) (02324)								
CSMS:	0456A (00703) (00708)	(00713)	(00718)	(01413)	(01414)	(01415)	(01416)	(01443)	(01444)
	(01472) (01475)	(01484)	(01485)	(01486)	(01487)	(01490)	(01491)	(01494)	(01497)
	(01500) (01503)	(01506)	(01509)	(01522)	(01523)	(01526)	(01564)	(01575)	(01577)
	(01579) (01582)	(01588)	(01590)	(01611)	(01612)	(01613)	(01625)	(01629)	(01637)
	(01640) (01643)	(01646)	(01649)	(01652)	(01655)	(01670)	(01675)	(01686)	(01708)
	(01742) (01743)	(01794)	(01795)	(01796)	(02347)	(02383)	(02672)		
CSMS1:	0466A (02346) (02670)								
CSMS5:	00000 (02347) (02345)								
CSMS2:	043A6 (00702) (00707)	(01865)							
CSMS2SSA:	00000 (01847) (01868)	(02082)	(02084)						
CSMS2F:	00003 (01864) (01873)								
CSMS2L:	00002 (01871) (01881)								
CSMS4F:	00000 (01868) (01904)								
CSMS4FS:	0001P (01909) (01926)								
CSMS41:	00000 (02115) (02150)								
CSMS41S:	0001M (02155) (02172)								
CSMSF:	00006 (01793) (02389)	(02394)							
CSMSL:	00004 (01792) (02391)	(02402)							
CSMSL0S:	00002 (01414) (02388)								
CSMSL1S:	00005 (01475) (02393)								
CSMSL2S:	00008 (01794) (02396)								
CSMSL3S:	00009 (01472) (02398)								
CSMSL4S:	0000A (01795) (02399)								
CSMSL5S:	00000 (01796) (02404)								
CSMSL6S:	00001 (01686) (02387)								
CSMS0:	00025 (02385) (02461)								
CSMS01S:	00026 (01670) (02462)								
CSMS0F:	00024 (02463) (02471)								
CSMS0L:	00028 (01526) (02465)	(02480)							
CSPUSMS:	021FC (00050) (00174)	(00178)	(00182)	(00186)	(00190)	(00194)	(00198)	(00202)	(00206)
	(00210) (00214)	(00218)	(00222)	(00226)	(00230)	(00234)	(00238)	(00242)	(00246)
	(00250) (00254)	(00258)	(00262)	(00266)	(00270)	(00274)	(00278)	(00282)	(00286)
	(00290) (00294)	(00298)	(00302)	(00306)	(00310)	(00314)	(00318)	(00322)	(00326)
	(00330) (00334)	(00338)	(00342)	(00346)	(00350)	(00354)	(00358)	(00362)	(00366)
	(00370) (00374)	(00378)	(00382)	(00386)	(00390)	(00394)	(00402)	(00406)	(00410)
	(00414) (00418)	(00422)	(00426)	(00430)	(00434)	(00438)	(00446)	(00450)	(00454)
	(00458) (00462)	(00466)	(00470)	(00474)	(00478)	(00482)	(00486)	(00490)	(00494)
	(00514) (00518)	(00522)	(00526)	(00530)	(00534)	(00538)	(00542)	(00546)	(00550)
	(00554) (00558)	(00562)	(00566)	(00570)	(00574)	(00578)	(00582)	(00586)	(00590)
	(00594) (00598)	(00602)	(00606)	(00610)	(00614)	(00618)	(00622)	(00626)	(00630)

PAGE 07: SNAP-11 MAP-300 ARITH. MODULES - PROG. 8830101.03 MAY 7, 1980
 BEING TOP OF MODULE

MUSDM:	(01637) (01640) (01643) (01646) (01649) (01652) (01655) (01662) (01681) (01703)
MUSLP:	(01751)
MUSTH1:	042AA (01456) (01463)
MUSTH2:	04296 (01454) (01461)
U RF3SSM:	0439F (01453) (01792)
U RF3SSM:	00000 (00498)
ISW2S:	04082 (00712) (00717) (02112)
ISW2SSA:	00000 (02107) (02115) (02328) (02340)
ISW2P:	00003 (02116) (02120)
ISW2L:	00002 (02118) (02128)
LOADSAP:	01A7F (00065)
LOADSAP1:	01A8F (00066)
MS8:	00000 (00070) (01000) (01010) (01012) (01013) (01042) (01043) (01104) (01105) (01107)
MS8SLMT:	00000 (01108) (01136) (01137) (01198) (01199) (01200) (01203) (01204) (01206) (01207)
MS8SHRT:	00000 (01234) (01240) (01700) (02387) (02388) (02391) (02393) (02394) (02396) (02398)
PMW2S:	04390 (01374) (01623) (01790)
U RF3SSM:	00000 (00507)
RPTFT04:	04276 (01369)
RPTFT07:	04282 (01413)
RPTFT08:	04299 (01447)
RPTFT11:	042F4 (01564)
RPTFT12:	042F7 (01566)
U S0021S:	00000 (00173) (00177) (00181) (00185)
U S1002S:	00000 (00345) (00349)
U S1011S:	00000 (00333) (00337) (00341)
U S2011S:	00000 (00353)
U S400S:	00000 (00172)
SHMS1:	04333 (01624) (01629)
SHMS2:	04338 (01628) (01633)
U SHMSDCVM:	00000 (00442)
U SHMSVPLV:	04352 (00709) (00719) (01661)
SCRM:	00000 (00398)
SCRM0:	00004 (02428) (02461)
SCRM1:	00010 (01484) (02430)
SCRM1S:	00011 (01490) (02431)
SCRM10S:	00021 (01506) (02452)
SCRM2S:	00012 (01485) (02432)

PAGE 001 SNAP-11 MAP-300 ARITH. MODULES - PROG. BR30101.03 MAY 7, 1980
 DEFINE TOP OF MODULE

SCHM3S:	00013 (01494) (02433)	
SCHM4:	00014 (0243M) (02442)	
SCHM4S:	00014 (014R6) (02434)	
SCHM5:	0001D (02442) (02447)	
SCHM5S:	0001S (01491) (02445)	
SCHM6:	00020 (02447) (02451)	
SCHM6S:	00016 (014R7) (02436)	
SCHM7:	00023 (01509) (02451) (02455)	
SCHM7S:	0001R (01497) (02439)	
SCHM8S:	0001M (01500) (02441)	
SCHM9S:	0001F (01503) (0244R)	
U SDIVS:	00000 (001R4)	
U SDOTS:	00000 (00352)	
SMTLSRS:	0078F (00072)	
SMAISSA:	00000 (00732) (0073R) (00757)	
SMAISSZ:	0000F (00733) (00757) (00759)	
U SMAIS:	00000 (00344)	
U SMAIARS:	00000 (0034R)	
U SMUIS:	00000 (001R0)	
U SSURS:	00000 (00176)	
U SSUMS:	00000 (00332)	
U SSUMARS:	00000 (00336)	
U SSUMSQS:	00000 (00340)	
START:	04000 (00096) (00722)	
SVIS:	003M2 (00073)	
SVTSUM1:	003CF (00074)	
SYSSPLGS:	1FECF (00075)	
TEMS0:	007M4 (00077)	
TOPS:	021FF (00078)	
TORSCUR:	0467R (00090) (02710)	
TORSPTR:	002M (00079) (0008R)	
V1124S:	04122 (00237) (00269) (00273) (00277) (00301) (00305) (00309) (00317) (00401)	
	(006M6) (00993) (01000) (01075)	
V1124SU:	00023 (01055) (01060)	
V1124S3:	00012 (01006) (01036)	
V1124S4:	0000C (01014) (01023)	
V1124S7:	00010 (01025) (01030)	
V1124SM:	0001F (01044) (01053)	
V1124SA:	041M4 (00996) (01064)	
V1124S1:	041M (00992) (01072)	
V1124SS:	00002 (00993) (01009)	
V1124S2:	000M2 (00995) (01075)	
V1124AS:	041M (00205) (01089) (01096) (01170)	
V1124AS0:	00023 (01150) (01155)	

PAGE 89: SNAP-IT MAP-300 ARITH. MINUTES - PROG. 8430101.03 MAY 7, 1980
 TAKING TOP OF MONTH

V112483:	00012 (01101)	(01130)
V112485:	00000 (01109)	(01118)
V112487:	00010 (01120)	(01125)
V112489:	00018 (01138)	(01148)
V112491:	00014 (01092)	(01160)
V112493:	00016 (01088)	(01167)
V112495:	00002 (01089)	(01104)
V112497:	00062 (01091)	(01170)
U V112499:	00000 (00209)	
U V11251:	00000 (00213)	
U V11253:	00000 (00217)	
U V11255:	00000 (00397)	
U V11257:	00000 (00289)	
U V11259:	00000 (00241)	(00245) (00281) (00285) (00313)
V11261:	00116 (00405)	(01182) (01184) (01274)
V11263:	00024 (01253)	(01258)
V11265:	00010 (01195)	(01233)
V11267:	00024 (01241)	(01251)
V11269:	00024 (01185)	(01263)
V11271:	00256 (01181)	(01271)
V11273:	00002 (01142)	(01198)
V11275:	00000 (01184)	(01274)
U V11277:	00000 (00409)	
VCS2:	00009 (00786)	(00793) (00796)
VCS3:	00013 (00813)	(00817)
VCS4:	00017 (00814)	(00821) (00823)
VCS5:	0001A (00827)	
VCS6:	00010 (00838)	(00841)
VCS7:	00061 (00846)	(00922)
VCS8:	0000F (00802)	(00804)
U VCLIPS:	00000 (00300)	
U VCUMPS:	00000 (00312)	
VCS9:	00020 (00404)	(00771)
VCS10:	00000 (00768)	(00777) (00927)
VCS11:	00065 (00769)	(00927) (00929)
VCS12:	000FA (00934)	(01217)
U VFIXS:	00000 (00208)	(00216)
VFLTS:	000F6 (00204)	(00952)
VFLTSSA:	00000 (00949)	(00955) (00978) (00980)
VFLTSZ:	00012 (00950)	(00978)
U VLIMITS:	00000 (00304)	
U VLIMITS:	00000 (00288)	
U VMAGS:	00000 (00284)	
U VMAGSOS:	00000 (00280)	

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PAGE 1: SNAP-II IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # 840001.01A

(00001) * SNAP-II IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # 840001.01A
(00002) *
(00003) *
(00004) * RELEASE 0.0 -- FIRST DELIVERED VERSION
(00005) *
(00006) * RELEASE 1.0A
(00007) * 1. CORRECTED HEAD SCROLL REGISTERS FCB. PR #4000 11/20/79
(00008) * 2. CORRECTED SPECIAL BINDING MODULE FOR ADMMRS. PR #4001 11/20/79
(00009) * 3. PR #4002 NOT INCLUDED. ONLY FOR PIV 12 CSPO. MUST PATCH OBJECT FILE.
(00010) * 4. FIXED START ADDRESSES FOR IUS-3. PR #4003 11/20/79.
(00011) * 5. ADDED IUS-4 SUPPORT. PR #4008 2/13/80
(00012) *
(00013) *
(00014) *
(00015) *
(00016) * DEFINE SYMBOLS FOR SNAP-II EXECUTIVE REL. 3.05 5/22/79
(00017) *
(00018) *
000000F5 (00019) ADMMRFCB = 229
000000F6 (00020) AFDTSORG = SFR
000000FFA (00021) APSHNDR = SFEA
(00022) *
000000K04 (00023) RCTSD = $K04
000000K86 (00024) RCTSAT = $K86
000000S42 (00025) RCTSHA = $S42
00000040 (00026) RCTSSIZ = 64
000000000 (00027) WITERN = 11
000000006 (00028) RITSTORD = 6
000000009 (00029) HITSIOWT = 9
(00030) *
0000002H6 (00031) CSWS1161 = $2H6
000000372 (00032) CSWS105 = $372
000000405 (00033) CSWS105H = $405
(00034) *
00000014 (00035) ERRSINS = 20
00001AFA (00036) FRONPS = $1AFA
(00037) *
0000007ER (00038) FDT5 = $7ER
(00039) *
000000001 (00040) HS = 1
(00041) *
00000003F (00042) IDVSA3 = $3F
00001R50 (00043) IDIVS = $1R50
000000001 (00044) TLVLS1 = 1

```

ROTATE BUFFER FOR #
 AP DISPATCH TABLE ORIGIN
 AP BINDER ROUTINE

BUFFER UNDEFINED BIT

INTEGER DIVIDF ROUTINE

```

00000002 (00045) ILVLS2 = 2
00000003 (00046) ILVLS3 = 3
00000502 (00047) ISVTS = $502
00000040 (00048) ISVTS17 = 128
00000040 (00049) *
00000000 (00050) MSKSIRY1 = $F000
000000FF (00051) MSKSHY1 = $00FF
00000000 (00052) MSS = 0
00000000 (00053) *
00001C12 (00054) SFTSSMR = $1C12
000000FF (00055) SNAPSINI = $FF
000000FF (00056) *
00000784 (00057) TMS0 = $784
00000785 (00058) TMS1 = $785
00000786 (00059) TMS2 = $786
00000788 (00060) TMSPTR = $288
00000002 (00061) *
00000002 (00062) WS = 7
00000003 (00063) *
00000004 (00064) *
00000005 (00065) *-----DEFINE START LOCATION FOR I/O PACKAGE
00000006 (00066) *
00000007 (00067) *
00000008 (00068) *
00000009 (00069) *
0000000A (00070) #L = TMSPTR
0000000B (00071) *
00288 0010515A (00072) ADDR TMSCUR(,1)
0000000C (00073) *

```

TEMPORARY STORAGE

TOP OF EXEC POINTER

DEFINE START LOCATION

UPDATE TOP OF EXEC POINTER

FOR DISPATCH TABLE FOR I/O ROUTINES

(00074) : FOR DISPATCH TABLE FOR I/O ROUTINES

(00075) *

(00076) *

(00077) * EL = FOTS + 2 * 66

(00078) *

(00079) * ADDR

(00080) * LOSS

(00081) * ADDR

(00082) * ADDR

(00083) * EL = FOTS + 2 * 70

(00084) *

(00085) * ADDR

(00086) * ADDR

(00087) *

(00088) * EL = FOTS + 2 * 73

(00089) *

(00090) * ADDR

(00091) * ADDR

(00092) *

(00093) *

(00094) *

(00095) * FOR DISPATCH TABLE ENTRY FOR ADMR

(00096) *

(00097) *

(00098) *

(00099) *

(00100) *

(00101) *

(00102) *

(00103) *

EL = AFDTSORG + 6 * (ADMNHECR - 128)

ADDR ADMHAPU (K7, 1)

ADDR ADMHAPS (P7, 1)

ADDR ADMHPS (, 1, 0)

66=LOAD I/O SCROLL
67=RUN I/O SCROLL
68=RUN ADM AND ADM

70=READ SCROLL REGISTERS
71=WRITE SCROLL REGISTERS

73=SINGLE OR DUAL CHANNEL SAMPLING
74=ADAM-16 ROUTINES

(00104) : IUS INTERRUPT TABLES		SYSTEM STATEWORDS FOR IUS DEVICES	
(03105) *			
(00106) *			
(00107) *			
(00108) *			
(00109) *			
(00110) *			
(00111) *			
(00112) *			
(00113) *			
(00114) *			
(00115) *			
(00116) *			
(00117) *			
(00118) *			
(00119) *			
(00120) *			
(00121) *			
(00122) *			
(00123) *			
(00124) *			
(00125) *			
(00126) *			
(00127) *			
(00128) *			
(00129) *			
(00130) *			
(00131) *			
(00132) *			
(00133) *			
(00134) *			
(00135) *			
(00136) *			
(00137) *			
(00138) *			
(00139) *			
(00140) *			
00000206	00110	RL = CSWS1161	
00206	40F21014	CSW	CSWSSTDH,016SINT1
00207	4A001014	CSWS1162 CSW	CSWSSTDH,016SINT2
00208	4A011014	CSWS1163 CSW	CSWSSTDH,016SINT3
00209	4A1A1014	CSWS1171 CSW	CSWSSTDH,017SINT1
00210	4A2A1014	CSWS1172 CSW	CSWSSTDH,017SINT2
00211	4A321014	CSWS1173 CSW	CSWSSTDH,017SINT3
00212	4A411014	CSWS1181 CSW	CSWSSTDH,018SINT1
00213	4A511014	CSWS1182 CSW	CSWSSTDH,018SINT2
00214	4A611014	CSWS1183 CSW	CSWSSTDH,018SINT3
00215	4A7A1014	CSWS1191 CSW	CSWSSTDH,019SINT1
00216	4A811014	CSWS1192 CSW	CSWSSTDH,019SINT2
00217	4A911014	CSWS1193 CSW	CSWSSTDH,019SINT3
00218	4AA11014	CSWS1201 CSW	CSWSSTDH,020SINT1
00219	4AB11014	CSWS1202 CSW	CSWSSTDH,020SINT2
00220	4AC11014	CSWS1203 CSW	CSWSSTDH,020SINT3
00221	4AD11014	CSWS1211 CSW	CSWSSTDH,021SINT1
00222	4AE11014	CSWS1212 CSW	CSWSSTDH,021SINT2
00223	4AF11014	CSWS1213 CSW	CSWSSTDH,021SINT3
00224	4B011014	CSWS1221 CSW	CSWSSTDH,022SINT1
00225	4B111014	CSWS1222 CSW	CSWSSTDH,022SINT2
00226	4B211014	CSWS1223 CSW	CSWSSTDH,022SINT3
00227	4B311014	CSWS1231 CSW	CSWSSTDH,023SINT1
00228	4B411014	CSWS1232 CSW	CSWSSTDH,023SINT2
00229	4B511014	CSWS1233 CSW	CSWSSTDH,023SINT3

FFFFF

		INITIAL VALUES FOR SYSTEM STATEWORDS FOR IUS DEVICES	
	(00141) *		
	(00142) *		
	(00143) *		
	(00144) *		
00000322	BL = CSWSINTS		
	(00145) *		
	(00146) *		
	(00147) *		
00327	4A21014 (00148)	CSWSSTDR,D16SINT1	IUS-2 DEVICE 16: LINE 1
00328	4A01014 (00149)	CSWSSTDR,D16SINT2	IUS-2 DEVICE 16: LINE 2
00329	4A0F1014 (00150)	CSWSSTDR,D16SINT3	IUS-2 DEVICE 16: LINE 3
0032A	4A161014 (00151)	CSWSSTDR,D17SINT1	IUS-2 DEVICE 17: LINE 1
0032B	4A241014 (00152)	CSWSSTDR,D17SINT2	IUS-2 DEVICE 17: LINE 2
0032C	4A321014 (00153)	CSWSSTDR,D17SINT3	IUS-2 DEVICE 17: LINE 3
0032F	4A3A1014 (00154)	CSWSSTDR,D18SINT1	IUS-2 DEVICE 18: LINE 1
00330	4A481014 (00155)	CSWSSTDR,D18SINT2	IUS-2 DEVICE 18: LINE 2
00332	4A561014 (00156)	CSWSSTDR,D18SINT3	IUS-2 DEVICE 18: LINE 3
00334	4A641014 (00157)	CSWSSTDR,D19SINT1	IUS-2 DEVICE 19: LINE 1
00336	4A6C1014 (00158)	CSWSSTDR,D19SINT2	IUS-2 DEVICE 19: LINE 2
00338	4A7A1014 (00159)	CSWSSTDR,D19SINT3	IUS-2 DEVICE 19: LINE 3
0033A	4A821014 (00160)	CSWSSTDR,D20SINT1	IUS-2 DEVICE 20: LINE 1
0033C	4A901014 (00161)	CSWSSTDR,D20SINT2	IUS-2 DEVICE 20: LINE 2
0033F	4A9F1014 (00162)	CSWSSTDR,D20SINT3	IUS-2 DEVICE 20: LINE 3
00340	4AA61014 (00163)	CSWSSTDR,D21SINT1	IUS-2 DEVICE 21: LINE 1
00342	4AA41014 (00164)	CSWSSTDR,D21SINT2	IUS-2 DEVICE 21: LINE 2
00344	4AC21014 (00165)	CSWSSTDR,D21SINT3	IUS-2 DEVICE 21: LINE 3
00346	4ACA1014 (00166)	CSWSSTDR,D22SINT1	IUS-2 DEVICE 22: LINE 1
00348	4AB81014 (00167)	CSWSSTDR,D22SINT2	IUS-2 DEVICE 22: LINE 2
0034A	4AF61014 (00168)	CSWSSTDR,D22SINT3	IUS-2 DEVICE 22: LINE 3
0034C	4AF1014 (00169)	CSWSSTDR,D23SINT1	IUS-2 DEVICE 23: LINE 1
0034F	4AFC1014 (00170)	CSWSSTDR,D23SINT2	IUS-2 DEVICE 23: LINE 2
00350	4H0A1014 (00171)	CSWSSTDR,D23SINT3	IUS-2 DEVICE 23: LINE 3

		INITIALIZE IUS BUSY FLAGS	
000000FF	BL = SNAPSINI, * 6		
	(00172) *		
	(00173) *		
	(00174) *		
	(00175) *		
	(00176) *		
	(00177) *		
	(00178) *		
000000FF	BL = SNAPSINI, * 6		
	(00179) *		
0000F	40040F1	JMP	INSSINI,
	(00180) *		
	(00181) *		
	(00182) *		
	(00183) *		
	(00184) *		

SPT PC TO ENTRY IN SCHEDULER

PAGE 6: SNAP-11 IUS PACKAGE ----- APR 18, 1980 ----- PROGRAM # H40001.01A
IUS INTERRUPT TABLES

00004900 (00185) #L = START
(00186) *
(00187) *

RESET START LOCATION FOR MODULE

PAGE 7: SNAP-IT IIS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A

LOAD SCROLL ERROR CODES

LOAD SCROLL ERROR CODES	ERROR MEANING
(00188) *	TRANSFER DIRECTION ILLEGAL
(00189) *	WID 0 ILLEGAL (OUT OF RANGE OR UNDEFINED)
(00190) *	WID 1 ILLEGAL (OUT OF RANGE OR UNDEFINED)
(00191) *	WID 2 ILLEGAL (OUT OF RANGE OR UNDEFINED)
(00192) *	WID 3 ILLEGAL (OUT OF RANGE OR UNDEFINED)
(00193) *	SIZE(WID 0) .NE. INTEGRAL MULTIPLE OF # CHANNELS
(00194) *	IF DOUBLE BUFFERED MODE, AND SIZE(WID 0) .NE. SIZE(WID 2)
(00195) *	1ST REGISTER # TO WRITE IS OUT OF RANGE
(00196) *	# REGISTERS TO WRITE IS OUT OF RANGE
(00197) *	INTEGER SCALAR ID OF OFFSET .GT. MAXIMUM INTEGER ID
(00198) *	WINDING CHAIN IN ERROR
(00199) *	
(00200) *	
(00201) *	
(00202) *	
(00203) *	
(00204) *	
(00205) *	
(00206) *	
(00207) *	
(00208) *	
(00209) *	
(00210) *	SCROLL ERROR INDICATOR
(00211) *	BUSY FLAG PER IIS DEVICE
(00212) *	0 => FREE, 1 => BUSY
(00213) *	
(00214) *	ACQUISITION MODE PER IIS DEVICE
(00215) *	
(00216) *	

PAGE	8:	SNAP-11 IUS PACKAGE	----	APR 18, 1980	----	PROGRAM # R40001.01A
		LOAD SCHEDULE ERROR CODES				
		(00217) *				
		(00218) *	INITIALIZE IUS BUSY FLAGS			
		(00219) *				
049F1	R4001C12	(00220)	IUSSTMI, CALL	R0, SFTSSMR		REPLACE INSTRUCTION IN SCHEDULER
		(00221) *				
049F3	1F200007	(00222)	LRPPT	R2, 7		SET TO CLEAR 8 WORDS
049F5	CC044901	(00223)	MOVZM	IUSSTMI, R2		
		(00224) *				
049F7	R00000F0	(00225)	JMP	SNAPSTMI+8		RETURN TO SCHEDULER
		(00226) *				
049F9	0R00	(00227)	EVEN			

PAGE 9: SNAP-II IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A

IUS WAIT ROUTINE

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(00228) * IUS WAIT ROUTINE
(00229) *
(00230) *
(00231) * ENTERED WITH R7 = SCROLL ID
(00232) *
(00233) *
(00234) *
(00235) *
(00236) *
(00237) *
(00238) *
(00239) *
(00240) *
(00241) *
(00242) *

049FA DC0F49C1 WAITSTOS SHRS 0, IUSFLG-16(R7) IF IUS FREE,
049FC 0F70 RETURN RETURN
049FD 0190003F WAITSO WAITS IDEVS63, ILVIS1 IF BUSY, WAIT FOR I/O DONE
049FF 2106 HOP WAITSTOS REPEAT TEST
049F0 0F70 RETURN
049F1 0800 EVEN

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INTERRUPT ROUTINES FOR IOS-SCHOLL BUFFER MANAGEMENT		
(00241) *		
(00244) *		
(00245) *		
(00246) *		
(00247) *		
049F7 90700000	D16SINT1	MOVIR R7, 0
049F4 H6004B12	CALL	RO, IUSINT
049F6 0F70	PFT	
049F7 0800	FVFN	
049F8 90700001	MOVIR	R7, 1
049FA H6004B12	CALL	RO, IUSINT
049FC 0F70	PFT	
049FD 0800	FVFN	
049FE H00049F2	JMP	D16SINT1
(00257) *		
(00258) *		
(00259) *		
(00260) *		
(00261) *		
04A00 90700010	D16SINT2	MOVIR R7, 16
04A02 H6004B12	CALL	RO, IUSINT
04A04 0F70	PFT	
04A05 0800	FVFN	
04A06 90700011	MOVIR	R7, 17
04A08 H6004B12	CALL	RO, IUSINT
04A0A 0F70	PFT	
04A0B 0800	FVFN	
04A0C H0004A00	JMP	D16SINT2
(00271) *		
(00272) *		
(00273) *		
(00274) *		
(00275) *		
04A0F 90700000	D16SINT3	MOVIR R7, 0
04A10 H6004B24	CALL	RO, IUSINTC
04A12 0F70	PFT	
04A13 0800	FVFN	
04A14 H0004A0F	JMP	D16SINT3
(00281) *		
(00282) *		

POINT TO VALUES FOR FIRST INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

POINT TO VALUES FOR SECOND INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

REPEAT LOOP ON NEXT INTERRUPT

DEVICE NUMBER 16 - LINE 2

POINT TO VALUES FOR FIRST INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

POINT TO VALUES FOR SECOND INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

REPEAT LOOP ON NEXT INTERRUPT

DEVICE NUMBER 16 - LINE 3

POINT TO VALUES FOR INTERRUPT 16
 GO TO INTERRUPT COMPLETION SUBROUTINE

04A16 90700002	(00284) *	D17SINT1	MOVIP	R7, 2	POINT TO VALUES FOR FIRST INTERRUPT
04A18 86004012	(00285)		CALL	PO, INSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A1A 0F70	(00286)		RFT		
04A1B 0800	(00287)		EVEN		
04A1C 90700003	(00288)		MOVIP	R7, 3	POINT TO VALUES FOR SECOND INTERRUPT
04A1F 86004012	(00289)		CALL	PO, INSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A20 0F70	(00290)		RFT		
04A21 0800	(00291)		EVEN		
04A22 80004016	(00292)		JMP	D17SINT1	REPEAT LOOP ON NEXT INTERRUPT
	(00293) *				
	(00294) *				
	(00295) *				
	(00296) *				
	(00297) *				
	(00298) *				
04A24 90700012	(00299)	D17SINT2	MOVIP	R7, 14	POINT TO VALUES FOR FIRST INTERRUPT
04A26 86004012	(00300)		CALL	PO, INSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A28 0F70	(00301)		RFT		
04A29 0800	(00302)		EVEN		
04A2A 90700013	(00303)		MOVIP	R7, 19	POINT TO VALUES FOR SECOND INTERRUPT
04A2C 86004012	(00304)		CALL	PO, INSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A2E 0F70	(00305)		RFT		
04A2F 0800	(00306)		EVEN		
04A30 80004024	(00307)		JMP	D17SINT2	REPEAT LOOP ON NEXT INTERRUPT
	(00308) *				
	(00309) *				
	(00310) *				
	(00311) *				
	(00312) *				
04A32 90700002	(00313)	D17SINT3	MOVIP	R7, 2	POINT TO VALUES FOR INTERRUPT 17
04A34 86004024	(00314)		CALL	PO, INSSINT	GO TO INTERRUPT COMPLETION SUBROUTINE
04A36 0F70	(00315)		RFT		
04A37 0800	(00316)		EVEN		
04A38 80004012	(00317)		JMP	D17SINT3	
	(00318) *				
	(00319) *				

DEVICE NUMBER 17 - LINE 3

DEVICE NUMBER 17 - LINE 2

DEVICE NUMBER 17 - LINE 1

(00320) !	(00321) *	DEVICE NUMBER 18 - LINE 1	POINT TO VALUES FOR FIRST INTERRUPT GO TO INTERRUPT PROCESSING SUBROUTINE
04A3A 90700004	D18SINT1	MOVIR R7, 4	
04A3C 86000412		CALL R0, I0SSINT	
04A3E 0670	RFT		
04A3F 0800	EVEN		
04A40 90700005			
04A42 86000412		MOVIR R7, 5	POINT TO VALUES FOR SECOND INTERRUPT GO TO INTERRUPT PROCESSING SUBROUTINE
04A44 0670	RFT	CALL R0, I0SSINT	
04A45 0800	EVEN		
04A46 8000041A	JMP	D18SINT1	REPEAT LOOP ON NEXT INTERRUPT
(00331) *			
(00332) *			
(00333) !		DEVICE NUMBER 18 - LINE 2	
(00334) *			
(00335) *			
04A48 90700014	D18SINT2	MOVIR R7, 20	POINT TO VALUES FOR FIRST INTERRUPT GO TO INTERRUPT PROCESSING SUBROUTINE
04A4A 86000412		CALL R0, I0SSINT	
04A4C 0670	RFT		
04A4D 0800	EVEN		
04A4F 90700015			
04A50 86000412		MOVIR R7, 21	POINT TO VALUES FOR SECOND INTERRUPT GO TO INTERRUPT PROCESSING SUBROUTINE
04A52 0670	RFT	CALL R0, I0SSINT	
04A53 0800	EVEN		
04A54 800004A8	JMP	D18SINT2	REPEAT LOOP ON NEXT INTERRUPT
(00345) *			
(00346) *			
(00347) !		DEVICE NUMBER 18 - LINE 3	
(00348) *			
(00349) *			
04A56 90700004	D18SINT3	MOVIR R7, 4	POINT TO VALUES FOR INTERRUPT 18 GO TO INTERRUPT COMPLETION SUBROUTINE
04A58 86000424		CALL R0, I0SSINTC	
04A5A 0670	RFT		
04A5B 0800	EVEN		
04A5C 800004A6	JMP	D18SINT3	
(00355) *			
(00356) *			

PAGE 13: SNAP-11 I/O PACKAGE ---- APR 18, 1960 ---- PROGRAM # R40001.01A
 INTERRUPT ROUTINES FOR I/O-SCHEDULE BUFFER MANAGEMENT

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(00357) !
(00358) *
04A5F 90700006 (00359) D19SINT1 MOVIR R7, 6 POINT TO VALUES FOR FIRST INTERRUPT
04A60 R6004A12 (00360) CALL R0, I0SSINT GO TO INTERRUPT PROCESSING SURROUTINE
04A62 0F70 (00361) RPT
04A63 0800 (00362) EVEN
04A64 90700007 (00363) MOVIR R7, 7 POINT TO VALUES FOR SECOND INTERRUPT
04A66 R6004A12 (00364) CALL R0, I0SSINT GO TO INTERRUPT PROCESSING SURROUTINE
04A68 0F70 (00365) RPT
04A69 0800 (00366) EVEN
04A6A R0004A5F (00367) JMP D19SINT1 REPEAT LOOP ON NEXT INTERRUPT
(00368) *
(00369) *
(00370) !
(00371) *
(00372) *
(00373) *
04A6C 90700016 (00373) D19SINT2 MOVIR R7, 22 POINT TO VALUES FOR FIRST INTERRUPT
04A6E R6004A12 (00374) CALL R0, I0SSINT GO TO INTERRUPT PROCESSING SURROUTINE
04A70 0F70 (00375) RPT
04A71 0800 (00376) EVEN
04A72 90700017 (00377) MOVIR R7, 23 POINT TO VALUES FOR SECOND INTERRUPT
04A74 R6004A12 (00378) CALL R0, I0SSINT GO TO INTERRUPT PROCESSING SURROUTINE
04A76 0F70 (00379) RPT
04A77 0800 (00380) EVEN
04A78 R0004A6C (00381) JMP D19SINT2 REPEAT LOOP ON NEXT INTERRUPT
(00382) *
(00383) *
(00384) !
(00385) *
(00386) *
04A7A 90700006 (00387) D19SINT3 MOVIR R7, 6 POINT TO VALUES FOR INTERRUPT 16
04A7C R6004A24 (00388) CALL R0, I0SSINTC GO TO INTERRUPT COMPLETION SURROUTINE
04A7E 0F70 (00389) RPT
04A7F 0800 (00390) EVEN
04A80 R0004A7A (00391) JMP D19SINT3
(00392) *
(00393) *
  
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		DEVICE NUMBER 20 - LINE 1		
04A82	90700008	(00394) *		
		(00395) *		
04A84	R6004H12	D20SINT1	MOVIR R7, R	POINT TO VALUES FOR FIRST INTERRUPT
04A86	0F70		CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A87	0800		RFT	
			FVFN	
04A88	90700009	(00396)		
04A8A	R6004H12	(00400)	MOVIR R7, 9	POINT TO VALUES FOR SECOND INTERRUPT
04A8C	0F70	(00401)	CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A8D	0800	(00402)	RFT	
			FVFN	
04A8E	R0004A82	(00403)	JMP D20SINT1	REPEAT LOOP ON NEXT INTERRUPT
		(00404)		
		(00405) *		
		(00406) *		
		(00407) *		
		(00408) *		
		(00409) *		
		DEVICE NUMBER 20 - LINE 2		
04A90	90700018	(00410)	D20SINT2	
04A92	R6004H12	(00411)	MOVIR R7, 24	POINT TO VALUES FOR FIRST INTERRUPT
04A94	0F70	(00412)	CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A95	0800	(00413)	RFT	
			FVFN	
04A96	90700019	(00414)	MOVIR R7, 25	POINT TO VALUES FOR SECOND INTERRUPT
04A98	R6004H12	(00415)	CALL R0, IOSSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04A9A	0F70	(00416)	RFT	
04A9B	0800	(00417)	FVFN	
04A9C	R0004A90	(00418)	JMP D20SINT2	REPEAT LOOP ON NEXT INTERRUPT
		(00419) *		
		(00420) *		
		(00421) *		
		(00422) *		
		(00423) *		
		DEVICE NUMBER 20 - LINE 3		
04A9F	90700008	(00424)	D20SINT3	
04AA0	R6004H24	(00425)	MOVIR R7, R	POINT TO VALUES FOR INTERRUPT 20
04AA2	0F70	(00426)	CALL R0, IOSSINT	GO TO INTERRUPT COMPLETION SUBROUTINE
04AA3	0800	(00427)	RFT	
			FVFN	
04AA4	R0004A9F	(00428)	JMP D20SINT3	
		(00429) *		
		(00430)		
			FUNCT	

		DEVICE NUMBER 21 - LINE 1		
(00431) !				
(00432) *				
04AA6 9070000A (00433) D21SINT1	MOVIR	R7, 10		POINT TO VALUES FOR FIRST INTERRUPT
04AA8 86004R12 (00434)	CALL	R0, IOSSINT		GO TO INTERRUPT PROCESSING SUBROUTINE
04AAA 0E70 (00435)	RPT			
04AA8 0800 (00436)	EVEN			
04AAC 9070000H (00437)	MOVIR	R7, 11		POINT TO VALUES FOR SECOND INTERRUPT
04AAE 86004R12 (00438)	CALL	R0, IOSSINT		GO TO INTERRUPT PROCESSING SUBROUTINE
04AR0 0E70 (00439)	RPT			
04AH1 0400 (00440)	EVEN			
04AR2 80004AA6 (00441)	JMP	D21SINT1		REPEAT LOOP ON NEXT INTERRUPT
(00442) *				
(00443) *				
(00444) !				
(00445) *				
(00446) *				
		DEVICE NUMBER 21 - LINE 2		
04AA4 9070001A (00447) D21SINT2	MOVIR	R7, 26		POINT TO VALUES FOR FIRST INTERRUPT
04AA6 86004R12 (00448)	CALL	R0, IOSSINT		GO TO INTERRUPT PROCESSING SUBROUTINE
04AA8 0E70 (00449)	RPT			
04AA9 0800 (00450)	EVEN			
04ARA 9070001R (00451)	MOVIR	R7, 27		POINT TO VALUES FOR SECOND INTERRUPT
04ARC 86004R12 (00452)	CALL	R0, IOSSINT		GO TO INTERRUPT PROCESSING SUBROUTINE
04AHF 0E70 (00453)	RPT			
04AH6 0800 (00454)	EVEN			
04AC0 80004AA4 (00455)	JMP	D21SINT2		REPEAT LOOP ON NEXT INTERRUPT
(00456) *				
(00457) *				
(00458) !				
(00459) *				
(00460) *				
		DEVICE NUMBER 21 - LINE 3		
04AC2 9070000A (00461) D21SINT3	MOVIR	R7, 10		POINT TO VALUES FOR INTERRUPT 21
04AC4 86004R24 (00462)	CALL	R0, IOSSINTC		GO TO INTERRUPT COMPLETION SUBROUTINE
04AC6 0E70 (00463)	RPT			
04AC7 0800 (00464)	EVEN			
04AC8 80004AC2 (00465)	JMP	D21SINT3		
(00466) *				
(00467)	FJFCT			

(00458) *	DEVICE NUMBER 22 - LINE 1		
(00459) *			
04ACA 9070000C (00470) D22SINT1	MOVIR	R7, 12	POINT TO VALUES FOR FIRST INTERRUPT
04ACC 86004H12 (00471)	CALL	R0, IUSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04ACD 0F70 (00472)	RFT		
04ACE 0800 (00473)	EVEN		
04AD0 90700000 (00474)	MOVIR	R7, 13	POINT TO VALUES FOR SECOND INTERRUPT
04AD2 86004H12 (00475)	CALL	R0, IUSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04AD4 0F70 (00476)	RFT		
04AD5 0800 (00477)	EVEN		
04ADA 80004ACA (00478)	JMP	D22SINT1	REPEAT LOOP ON NEXT INTERRUPT
(00479) *			
(00480) *			
(00481) *	DEVICE NUMBER 22 - LINE 2		
(00482) *			
(00483) *			
04AD8 9070001C (00484) D22SINT2	MOVIR	R7, 28	POINT TO VALUES FOR FIRST INTERRUPT
04ADA 86004H12 (00485)	CALL	R0, IUSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04ADC 0F70 (00486)	RFT		
04ADD 0800 (00487)	EVEN		
04ADF 9070001D (00488)	MOVIR	R7, 29	POINT TO VALUES FOR SECOND INTERRUPT
04AE0 86004H12 (00489)	CALL	R0, IUSINT	GO TO INTERRUPT PROCESSING SUBROUTINE
04AE2 0F70 (00490)	RFT		
04AE3 0800 (00491)	EVEN		
04AE4 80004ADR (00492)	JMP	D22SINT2	REPEAT LOOP ON NEXT INTERRUPT
(00493) *			
(00494) *			
(00495) *	DEVICE NUMBER 22 - LINE 3		
(00496) *			
(00497) *			
04AE6 9070000C (00498) D22SINT3	MOVIR	R7, 12	POINT TO VALUES FOR INTERRUPT 22
04AE8 86004H24 (00499)	CALL	R0, IUSINTC	GO TO INTERRUPT COMPLETION SUBROUTINE
04AEA 0F70 (00500)	RFT		
04AEF 0800 (00501)	EVEN		
04AEC 80004AF6 (00502)	JMP	D22SINT3	
(00503) *			
(00504) *	FINCT		

		DEVICE NUMBER 23 - LINE 1		
04AF0	9070000F	(00505) *	MOVIR	R7, 14
04AF0	R6004H12	(00506) *	CALL	R0, I0SSINT
04AF2	0F70	(00507) *	RPT	
04AF3	0800	(00508)	EVEN	
04AF4	9070000F	(00509)	MOVIR	R7, 15
04AF6	R6004H12	(00510)	CALL	R0, I0SSINT
04AF8	0F70	(00511)	RPT	
04AF9	0800	(00512)	EVEN	
04AFA	R0004AFC	(00513)	JMP	D23SINT1
		(00514)		
		(00515)		
		(00516)		
		(00517)		
		(00518)		
		(00519)		
		(00520)		
		DEVICE NUMBER 23 - LINE 2		
04AFC	9070001F	(00521) *	MOVIR	R7, 30
04AFF	R6004H12	(00522)	CALL	R0, I0SSINT
04H00	0F70	(00523)	RPT	
04H01	0800	(00524)	EVEN	
04H02	9070001F	(00525)	MOVIR	R7, 31
04H04	R6004H12	(00526)	CALL	R0, I0SSINT
04H06	0F70	(00527)	RPT	
04H07	0800	(00528)	EVEN	
04H08	R0004AFC	(00529)	JMP	D23SINT2
		(00530)		
		(00531)		
		(00532)		
		(00533)		
		(00534)		
		DEVICE NUMBER 23 - LINE 3		
04H0A	9070000F	(00535) *	MOVIR	R7, 14
04H0C	R6004H24	(00536)	CALL	R0, I0SSINTC
04H0E	0F70	(00537)	RPT	
04H0F	0800	(00538)	EVEN	
04H10	R0004H0A	(00539)	JMP	D23SINT3
		(00540)		
		(00541)		

POINT TO VALUES FOR FIRST INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

POINT TO VALUES FOR SECOND INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

REPEAT LOOP ON NEXT INTERRUPT

POINT TO VALUES FOR FIRST INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

POINT TO VALUES FOR SECOND INTERRUPT
 GO TO INTERRUPT PROCESSING SUBROUTINE

REPEAT LOOP ON NEXT INTERRUPT

POINT TO VALUES FOR INTERRUPT 23
 GO TO INTERRUPT COMPLETION SUBROUTINE

PAGE 14: SNAP-11 IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # H40001.01A
 INTERRUPT ROUTINES FOR IUS-SCROLL BUFFER MANAGEMENT

GENERAL SUBROUTINES TO PROCESS IUS INTERRUPTS

```

(00542) !
(00543) *
(00544) *
04H12 F05F4B4C IUSINTC MOVRR R5, IUSINTC(R7) GET READ/WRITE INDEX
04H14 406F MOVRR R6, R7 COMPUTE LOGICAL BUFFER INDEX
04H15 3C61 LRS R6, 1
04H16 4C7C ADDR R7, R6
04H17 F06F4B4C MOVRR R6, IUSINTC(R7) IF HID LEGAL,
04H19 1A10 SKPL F0Z FLAG CURRENT BUFFER AVAILABLE
04H1A F20A4B46 XCT IUSINTC+WS(R5) IF HID LEGAL,
04H1C F07F4B4C MOVRR R7, IUSINTC+1(R7) IF HID LEGAL,
04H1E 1A10 SKPL F0Z FLAG NEXT BUFFER BUSY
04H1F F20A4B46 XCT IUSINTC(R5) SIGNAL I/O OPERATION COMPLETE
04H21 1200003F AINT IUSVS63, IUSVS1
04H23 0F70 RETURN
EVEN

(00561) *
(00562) *
(00563) *
(00564) *
04H24 402F IUSINTC MOVRR R2, R7
04H25 F05F4B4C MOVRR R5, IUSINTC(R7) SAVE INDEX FOR LATER USE
04H27 406F MOVRR R6, R7 GET READ/WRITE INDEX
04H28 3C61 LRS R6, 1 COMPUTE INDEX TO LOGICAL BUFFERS IDS
04H29 4C7C ADDR R7, R6
04H2A C03F4B46 MOVRR R3, IUSINTC+24(R7) GET LOGICAL BUFFER IDS (1 AND 3)
04H2C C06F4B4C MOVRR R6, IUSINTC(R7) GET LOGICAL BUFFER IDS (0 AND 2)
04H2E 0260 TEST R6, R7 IF HID LEGAL,
04H2F 1A10 SKPL F0Z FLAG BUFFER 0 AVAILABLE
04H30 F20A4B46 XCT IUSINTC+WS(R5) IF HID LEGAL,
04H32 406F MOVRR R6, R7 FLAG BUFFER 2 AVAILABLE
04H33 1A10 SKPL F0Z IF HID LEGAL,
04H34 F20A4B46 XCT IUSINTC+WS(R5) FLAG BUFFER 1 AVAILABLE
04H36 406F MOVRR R6, R3 IF HID LEGAL,
04H37 1A10 SKPL F0Z FLAG BUFFER 1 AVAILABLE
04H38 F20A4B46 XCT IUSINTC+WS(R5) IF HID LEGAL,
04H3A 406F MOVRR R6, R4
  
```

PAGE 19: SNAP-II IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # H40001.01A
 INTERRUPT ROUTINES FOR IUS-SCROLL BUFFER MANAGEMENT

04R3H 1A10	(00546)	SKPG	FOZ		
04R3C 820A4H46	(00547)	XCT	IUSMTHL+WS(45)		FLAG BUFFER 3 AVAILABLE
04R3F 3C21	(00548) *	IPS	R2, 1		
04R3F 0C0449D1	(00549)	MOVZP	IUSFLG(42)		CLEAR IUS DEVICE BUSY FLAG
04R41 1200003F	(00591) *	ATNT	DEV563, 11V151		SIGNAL I/O OPERATION COMPLETE
04R43 0F70	(00592)	RETURN			
	(00593)	EVEN			
	(00594)				
	(00595) *				
	(00596)	EJECT			

(00597) * BUFFER CONTROL TABLE FOR I/O INTERRUPTS			
(00598) *			
(00599) *			
04R43 02650686	HIT/SHWT, HCTSAT(R7)	HEAD DEVICE	
04R46 00600686	CMH	HITSAT(R6)	
04R48 03100686	SMH	HITSAT(R7)	WRITE DEVICE
04R4A 03100686	CMH	HITSAT(R6)	
(00604) *			
(00605) *			
(00606) *	HIT/SET READ/WRITE INDEX FOR I/O INTERRUPTS		
(00607) *			
(00608) *	SFT EACH HALFWORD TO 0 FOR READ, 4 FOR WRITE		
(00609) *			
(00610) *			
(00611) *			
(00612) 10585H1	DATA	0,0	DEVICE 16 - LINE 1
(00613)	DATA	0,0	DEVICE 17 - LINE 1
(00614)	DATA	0,0	DEVICE 18 - LINE 1
(00615)	DATA	0,0	DEVICE 19 - LINE 1
(00616)	DATA	0,0	DEVICE 20 - LINE 1
(00617)	DATA	0,0	DEVICE 21 - LINE 1
(00618)	DATA	0,0	DEVICE 22 - LINE 1
(00619)	DATA	0,0	DEVICE 23 - LINE 1
(00620) *			
(00621)	DATA	0,0	DEVICE 16 - LINE 2
(00622)	DATA	0,0	DEVICE 17 - LINE 2
(00623)	DATA	0,0	DEVICE 18 - LINE 2
(00624)	DATA	0,0	DEVICE 19 - LINE 2
(00625)	DATA	0,0	DEVICE 20 - LINE 2
(00626)	DATA	0,0	DEVICE 21 - LINE 2
04R4C 0000			
04R4D 0000			
04R4E 0000			
04R4F 0000			
04R50 0000			
04R51 0000			
04R52 0000			
04R53 0000			
04R54 0000			
04R55 0000			
04R56 0000			
04R57 0000			
04R58 0000			
04R59 0000			
04R5A 0000			
04R5B 0000			
04R5C 0000			
04R5D 0000			
04R5E 0000			
04R5F 0000			
04R60 0000			
04R61 0000			
04R62 0000			
04R63 0000			
04R64 0000			
04R65 0000			
04R66 0000			
04R67 0000			

PAGE 71: SNAP-II IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # R40001.01A
INTERRUPT ROUTINES FOR IOS-SCROLL BUFFER MANAGEMENT

04R68 0000	(00627)	DATA	0,0	DEVICE 22 - LINE 2
04R69 0000				
04R6A 0000	(00628)	DATA	0,0	DEVICE 23 - LINE 2
04R6B 0000				
	(00629) *			
	(00630)	FINCT		

(00631) *	LOGICAL BUFFER IDS * 2 FOR I/O INTERRUPTS	
(00632) *		
(00633) *		
(00634) *	SFT THE HALFWORD TRIPLET TO RUF0, RUF2, RUF0	
(00635) *		
(00636) *		
(00637) INSR	DATA 0,0,0	DEVICE 16 - LINE 1
04R6C 0000		
04R6D 0000		
04R6E 0000		
04R6F 0000	DATA 0,0,0	DEVICE 17 - LINE 1
04R70 0000		
04R71 0000	DATA 0,0,0	DEVICE 18 - LINE 1
04R72 0000		
04R73 0000	DATA 0,0,0	DEVICE 19 - LINE 1
04R74 0000		
04R75 0000	DATA 0,0,0	DEVICE 20 - LINE 1
04R76 0000		
04R77 0000	DATA 0,0,0	DEVICE 21 - LINE 1
04R78 0000		
04R79 0000	DATA 0,0,0	DEVICE 22 - LINE 1
04R7A 0000		
04R7B 0000	DATA 0,0,0	DEVICE 23 - LINE 1
04R7C 0000		
04R7D 0000	DATA 0,0,0	
04R7E 0000		
04R7F 0000	DATA 0,0,0	
04R80 0000		
04R81 0000	DATA 0,0,0	
04R82 0000		
04R83 0000		
(00645) *		
(00646) *	SFT THE HALFWORD TRIPLET TO RUF1, RUF3, RUF1	
(00647) *		
(00648) *	DATA 0,0,0	DEVICE 16 - LINE 2
04R84 0000		
04R85 0000		
04R86 0000	DATA 0,0,0	DEVICE 17 - LINE 2
04R87 0000		
04R88 0000	DATA 0,0,0	DEVICE 18 - LINE 2
04R89 0000		
04R8A 0000	DATA 0,0,0	DEVICE 19 - LINE 2
04R8B 0000		
04R8C 0000	DATA 0,0,0	
04R8D 0000		
04R8E 0000		

PAGE 23: SWAP-11 IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # 840001.01A
 INTERRUPT ROUTINES FOR IUS-SCHILL BUFFER MANAGEMENT

04884 0000				
04890 0000	(00652)	DATA	0,0,0	DEVICE 20 - LINE 2
04891 0000				
04892 0000				
04893 0000	(00653)	DATA	0,0,0	DEVICE 21 - LINE 2
04894 0000				
04895 0000				
04896 0000	(00654)	DATA	0,0,0	DEVICE 22 - LINE 2
04897 0000				
04898 0000				
04899 0000	(00655)	DATA	0,0,0	DEVICE 23 - LINE 2
04900 0000				
04901 0000				

PAGE 25: SNAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A
 LOSS MODULE TO PROCESS THE LOAD IOS SCROLL FOR

LOGICAL BUFFER FORMAT FOR IOS PROGRAM

(00696) *	1	
(00697) *	0	
(00698) *	0	
(00699) *	0	
(00700) *	0	
(00701) *	0	
(00702) *	0	
(00703) *	0	
(00704) *	0	
(00705) *	0	
(00706) *	0	
(00707) *	0	
(00708) *	0	
(00709) *	0	
(00710) *	0	
(00711) *	0	
(00712) *	0	
(00713) *	0	
(00714) *	0	
(00715) *	0	
(00716) *	0	
(00717) *	0	
(00718) *	0	
(00719) *	0	
(00720) *	0	
(00721) *	0	
(00722) *	0	
(00723) *	0	
(00724) *	0	
(00725) *	0	
(00726) *	0	
(00727) *	0	
(00728) *	0	
(00729) *	0	
(00730) *	0	
(00731) *	0	
(00732) *	0	
(00733) *	0	
(00734) *	0	
(00735) *	0	
(00736) *	0	
(00737) *	0	

HN
 --
 CONTENTS

 N = SCROLL PROGRAM SIZE IN HALFWORDS
 MID1 : MID0
 SCROLL PROGRAM IN HALFWORDS
 2 - N+1
 N+2 0 IF MAP -> SCROLL TRANSFER, ELSE 2 (ADAM=2, ADM=0)
 N+3 # CHANNELS : ACQUISITION MODE
 N+4 MID1 : MID0
 N+5 N = NUMBER OF SCROLL REGISTERS TO LOAD
 N+6 # OF FIRST SCROLL REGISTER TO LOAD
 N+7 - N+M+6 VALUES TO LOAD INTO SCROLL REGISTERS
 N+M+7 INTEGER SCALAR ID OF OFFSET
 N+M+8 BUFFER 0 CHAIN ANCHOR
 N+M+9 BUFFER 1 CHAIN ANCHOR
 N+M+10 BUFFER 2 CHAIN ANCHOR
 N+M+11 BUFFER 3 CHAIN ANCHOR

ACQUISITION MODE:
 0 => DISCRETE OR BURST
 .NE. 0 => CONTINUOUS
 1 => DOUBLE BUFFERED
 2 => CIRCULAR

 EJECT

SNAP-11 IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A
 L0SS MODULE TO PROCESS THE LOAD IOS SCROLL FOR

	(00738) *	(00739) *	(00740) *	(00741) *	(00742) *	(00743) *	(00744) *	(00745) *	(00746) *	(00747) *	(00748) *	(00749) *	(00750) *	(00751) *	(00752) *	(00753) *	(00754) *	(00755) *	(00756) *	(00757) *	(00758) *	(00759) *	(00760) *	(00761) *	(00762) *	(00763) *	(00764) *	(00765) *	(00766) *	(00767) *	(00768) *	(00769) *	(00770) *	(00771) *	(00772) *	(00773) *	(00774) *	(00775) *	(00776) *	(00777) *	(00778) *	(00779) *	(00780) *	(00781) *
04R0C 707200FF																																												
04R0F 3A71																																												
04R0F 0A00																																												
04R0D F03F05H3																																												
04R02 F0420001																																												
04R04 A07H																																												
04R05 F00040FA																																												
04R07 C00040H0																																												
04R0V A076																																												
04R0A R42007H4																																												
04R0C 4056																																												
04R0D 4C54																																												
04R0E F06A0003																																												
04R0H 4A6000FF																																												
04R0Z F06R40C9																																												
04R04 9C49FFFA																																												
04R06 F05A0002																																												
04R0R 1910																																												
04R0Q 2005																																												
04R0A 92500002																																												
04R0C 1A10																																												
04R0D R00040FC																																												
04R0F 3A51																																												
04R0C F05R4R4C																																												

REGISTER USAGE

P1 POINTER TO FCR # IN FCR BLOCK
 P2 SCROLL PROGRAM SIZE = N
 P3 PA = IOS LOGICAL BUFFER BASE ADDRESS
 P4 SCROLL INDEX (16 => 0, 17 => 2, ... , 23 => 14)

MOVW R7, R1, *MSKSHYT EXTRACT LOGICAL BUFFER ID FROM FCR
 LLS R7, 1 CONVERT TO WORD INDEX
 EVEN
 MOVW R3, HCTSHA+HS(R7) GET LOGICAL BUFFER START ADDRESS
 MOVW R4, 1(R1) GET SCROLL ID FROM FCR
 MOVW R7, R4
 CALL R0, WAITSIOS WAIT TILL SCROLL DEVICE FREE
 MOVW R0, WAITSIOS
 MOVW R0, WAITSIOS
 MOVW R2, TMSO SAVE RA & N
 MOVW R5, R3 COMPUTE PNTR TO SCROLL PROGRAM END
 ADDR R5, R2 (RA+N)
 MOVW R6, 3(R5) GET ACQUISITION MODE
 ANDIR R6, *MSKSHYT
 MOVW R6, ACUSLIST-16(R4) SAVE ACQUISITION MODE FOR RUN SCROLL
 ADDIR R4, -32(R4) CONVERT SCROLL TO SCROLL INDEX
 MOVW R5, 2(R5) GET TRANSFER DIR. FOR BUFFER CONTROL
 TEST IF TRANSFER DIRECTION LEGAL (0 OR 2)
 SKP NZ
 HOP L0SSC 0 => OK
 CMPIR R5, 2 2 => OK
 SKPT FO OTHERWISE, ERROR
 JMP L0SSFR1
 LLS R5, 1 CONVERT TO 0 OR 4
 MOVW R5, L0SSRPM(R4) MOVE READ/WRITE FLAG INTO TABLE

PAGE 27: SNAP-II IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A

LOSS MODULE TO PROCESS THE LOAD IUS SCRUB PCH

```

04HC2 F05A44D0 (00782) *      MOVDM    R5, IUSKSRW1+1(P4)
(00783) *
04HC4 F0760001 (00784) *      MOVDM    R7, HS(P3)
(00785) *      MOVDM    R6, R7, MSKSRHT
(00786) *      LLS      R6, 1
(00787) *      LPS      R7, 7
(00788) *
04HC6 405C     (00789) *      MOVDM    R5, R6
(00790) *      CALL     R6, HINSCBK
(00791) *      TEST     R5
(00792) *      JMP      IUSSEW2, LTZ
(00793) *
04HD0 405F     (00794) *      MOVDM    R5, R7
(00795) *      CALL     R6, HINSCBK
(00796) *      TEST     R5
(00797) *      JMP      IUSSEW3, LTZ
(00798) *
04HD6 405H     (00799) *      MOVDM    R5, R4
(00800) *      LPS      R5, 1
(00801) *      ADDR     R5, R4
(00802) *      EVEN     R5
(00803) *      MOVDM    R6, IUSLRI+2(R5)
(00804) *      MOVDM    R6, IUSLRI+2(R5)
(00805) *
04HD8 F07A44R4 (00806) *      MOVDM    R7, IUSLRI+24(R5)
(00807) *      MOVDM    R7, IUSLRI+26(R5)
(00808) *
04HF2 90760004 (00809) *      MOVDM    R7, 4(R3)
(00810) *      ADDR     R7, R2
(00811) *      MOVDM    R7, R7
(00812) *
04HF6 506F00FF (00813) *      MOVDM    R6, R7, MSKSRHT
(00814) *      LLS      R6, 1
(00815) *      LPS      R7, 7
(00816) *
04HFA F06A446D (00817) *      MOVDM    R6, IUSLRI+1(R5)
(00818) *      MOVDM    R7, IUSLRI+25(R5)
(00819) *
04HFE 405C     (00820) *      MOVDM    R5, R6
(00821) *      CALL     R6, HINSCBK
(00822) *      TEST     R5
(00823) *      JMP      IUSSEW4, LTZ
(00824) *
04HFA 405F     (00825) *      MOVDM    R5, R7
(00826) *

```

EXTRACT LOGICAL BUFFER IDENTIFIERS
(RID 0) * 2
(RID 1) * 2
CHECK RID 0
ERROR ENCOUNTERED?
YES, EXIT
CHECK RID 1

COMPUTE INDEX INTO TRIPLET RID TABLE

STORE RID 0 FOR INTERRUPT LINE 1

STORE RID 1 FOR INTERRUPT LINE 2

COMPUTE POINTER TO BUFFER 2 & 3 IDS
(R4+4)
GET BUFFER 2 & 3 IDS

EXTRACT THEM
(RID 2) * 2
(RID 3) * 2

STORE RID 2 FOR INTERRUPT LINE 1
STORE RID 3 FOR INTERRUPT LINE 2

CHECK RID 2
ERROR ENCOUNTERED?
YES, EXIT
CHECK RID 3

PAGE 28: SNAP-11 IUS PACKAGE ---- APR 14, 1980 ---- PROGRAM # 840001.01A
 IUS5 MODULE TO PROCESS THE LOAD IUS SCROLL FOR

```

04HFS R6604CRC (00R26) CALL R6, R1USCH
04HF7 0250 (00R27) TEST R5
04HF8 R0304C(R (00R28) JMP IUS5PR5, IZ
(00R29) *
(00R30) * TEST IF SIZE OF RID 0 = INTEGRAL MULTIPLE OF # CHANNELS
(00R31) *
(00R32) *
(00R33) * MOVW R5, R3
(00R34) * MOVW R5, R2
(00R35) * MOVW R6, 3(P5)
(00R36) * LRS R6, R
(00R37) *
(00R38) * MOVW R7, 1(CR3)
(00R39) * ANDIR R7, MSKSRVY
(00R40) * LLS R7, 1
(00R41) *
(00R42) * MOVW R7, ACTSAD+HS(P7)
(00R43) * INCR R7, 1
(00R44) * MOVW R7, TEMS7
(00R45) *
(00R46) * TEST R6
(00R47) * JMP IUS5A, IZ
(00R48) *
(00R49) * SET UP LOOP -- R6 = # CHANNELS, R7 = BUFFER SIZE
(00R50) *
(00R51) * SUBRR R7, R6
(00R52) * JMP IUS5A, FQZ
(00R53) *
(00R54) *
(00R55) *
(00R56) *
(00R57) *
(00R58) *
(00R59) *
(00R60) *
(00R61) *
(00R62) *
(00R63) *
(00R64) *
(00R65) *
(00R66) *
(00R67) *
(00R68) *
(00R69) *
  
```

(00R26) CALL R6, R1USCH
 (00R27) TEST R5
 (00R28) JMP IUS5PR5, IZ
 (00R29) *
 (00R30) * TEST IF SIZE OF RID 0 = INTEGRAL MULTIPLE OF # CHANNELS
 (00R31) *
 (00R32) *
 (00R33) * MOVW R5, R3
 (00R34) * MOVW R5, R2
 (00R35) * MOVW R6, 3(P5)
 (00R36) * LRS R6, R
 (00R37) *
 (00R38) * MOVW R7, 1(CR3)
 (00R39) * ANDIR R7, MSKSRVY
 (00R40) * LLS R7, 1
 (00R41) *
 (00R42) * MOVW R7, ACTSAD+HS(P7)
 (00R43) * INCR R7, 1
 (00R44) * MOVW R7, TEMS7
 (00R45) *
 (00R46) * TEST R6
 (00R47) * JMP IUS5A, IZ
 (00R48) *
 (00R49) * SET UP LOOP -- R6 = # CHANNELS, R7 = BUFFER SIZE
 (00R50) *
 (00R51) * SUBRR R7, R6
 (00R52) * JMP IUS5A, FQZ
 (00R53) *
 (00R54) *
 (00R55) *
 (00R56) *
 (00R57) *
 (00R58) *
 (00R59) *
 (00R60) *
 (00R61) *
 (00R62) *
 (00R63) *
 (00R64) *
 (00R65) *
 (00R66) *
 (00R67) *
 (00R68) *
 (00R69) *

IUS5A: IF R6 = 0, GO TO IUS5B
 IUS5B: IF R7 = 0, GO TO IUS5C
 IUS5C: IF R7 = 0, GO TO IUS5D
 IUS5D: IF R7 = 0, GO TO IUS5E
 IUS5E: IF R7 = 0, GO TO IUS5F
 IUS5F: IF R7 = 0, GO TO IUS5G
 IUS5G: IF R7 = 0, GO TO IUS5H
 IUS5H: IF R7 = 0, GO TO IUS5I
 IUS5I: IF R7 = 0, GO TO IUS5J
 IUS5J: IF R7 = 0, GO TO IUS5K
 IUS5K: IF R7 = 0, GO TO IUS5L
 IUS5L: IF R7 = 0, GO TO IUS5M
 IUS5M: IF R7 = 0, GO TO IUS5N
 IUS5N: IF R7 = 0, GO TO IUS5O
 IUS5O: IF R7 = 0, GO TO IUS5P
 IUS5P: IF R7 = 0, GO TO IUS5Q
 IUS5Q: IF R7 = 0, GO TO IUS5R
 IUS5R: IF R7 = 0, GO TO IUS5S
 IUS5S: IF R7 = 0, GO TO IUS5T
 IUS5T: IF R7 = 0, GO TO IUS5U
 IUS5U: IF R7 = 0, GO TO IUS5V
 IUS5V: IF R7 = 0, GO TO IUS5W
 IUS5W: IF R7 = 0, GO TO IUS5X
 IUS5X: IF R7 = 0, GO TO IUS5Y
 IUS5Y: IF R7 = 0, GO TO IUS5Z
 IUS5Z: IF R7 = 0, GO TO IUS5A

PAGE 30: SNAP-11 IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM 8 R40001.01A
 LOSS MODULE TO PROCESS THE LOAD IUS SCROLL FCH

```

(00914) *
04C4A 2721 INCR R2, 1 ADJUST COUNT
04C4B 2631 INCP R3, 1 AND START ADDRESS
04C4C R20H4C7C XCT LDSSCR12(R4) EXECUTE PROPER BLOCK MOVE

(00918) *
(00919) *----- RIND THE LOGICAL BUFFERS
(00920) *
(00921) * SET UP FOR CALLS TO I02SHDR ROUTINE
(00922) *

04C4F 0070 CLR R7 SET UP NULL OFFSET VALUE
04C4F 2651 INCR P5, 1 POINT INTEGER SCALAR ID OF OFFSET
04C50 606A MOVWR R6, R5 GET SCALAR ID

04C51 R0104C59 JMP LDSSNULL, I7Z IF < 0, => NULL
04C53 92600080 CMPJR R6, JSVTSS17 IF > MAX => ERROR
04C55 R1104C07 JMP IUSSE10, GE

04C57 F07C0502 MOVWR R7, JSVTS(R6) LEGAL ID, GET OFFSET VALUE
04C59 F0704F30 MOVWR R7, I02SHDR STORE FOR I02SHDR ROUTINE
LDSSNULL, I02SHDR

04C5A C02007H4 MOVMPLE R2,TFMS0 RESTORE R2=N, R3=NA
04C5B 4C26 ADDR R2, R3 (NA + N)
04C5F F0740004 MOVWR R7, 4(R2) R7 = RID 3 : 2 FOR LATER USE
04C60 F04H4C7D MOVWR R4, LDSSCR12+H5(R4) SET UP SCROLL LOAD ADDRESS

04C62 F0260001 MOVWR R2, 1(R3) GET RID 1 : 0
04C64 506400FF MOVWR R6, R2, MSKSRHVT R6 = RID 0
04C66 2651 INCR R5, 1 R5 = POINTER TO BINDING CHAIN ANCHOR
04C67 2632 INCR R3, 7 R3 = POINTER TO SCROLL PROGRAM

04C68 R6104D08 CALL R1, I02SHDR RIND BUFFER 0
(00943) *
(00944) *
(00945) *

04C6A 2651 INCP R5, 1 STEP TO NEXT CHAIN ANCHOR
04C6B 506400FF MOVWR R6, R2, MSKSRHVT GET RID 1
04C6D 3C68 LRS R6, R RIND BUFFER 1

04C6E R6104D08 CALL R1, I02SHDR RIND BUFFER 1
(00949) *
(00950) *
(00951) *

04C70 2651 INCR R5, 1 STEP TO NEXT CHAIN ANCHOR
04C71 506F00FF MOVWR R6, R7, MSKSRHVT GET RID 2
(00954) *
(00955) *
(00956) *

04C73 R6104D08 CALL R1, I02SHDR RIND BUFFER 2
(00957) *
04C75 2651 INCR R5, 1 STEP TO NEXT CHAIN ANCHOR

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PAGE 31: SNAP-II IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM 8 840001.01A
 IOSS MODULE TO PROCESS THE LOAD IOS SCROLL FCH

04C76 506FF00 (00958)	MOVW	R6, R7, MSKSHFT	GET RID 3
04C7N 3C6H (00959)	LRS	R6, R	
04C7Q 461040CR (00960) 0----	CALL	R1, INDSHDR	HIND BUFFER 3
04C7N 0D70 (00962) *			
(00963) *			
04C7N 0D70 (00964)	RETURN		
(00965)	EVEN		
(00966) *			
(00967) *			
(00968) *	IOS-2 LOAD INSTRUCTIONS		
(00969) *			
(00970) *			
04C7C 0535F00 (00971) INSSCR1,2	MOVE	R3, R2, S1F000	LOAD SCROLL 16
04C7E 0535F200 (00972)	MOVE	R3, R2, S1F200	LOAD SCROLL 17
04C80 0535F100 (00973)	MOVE	R3, R2, S1F400	LOAD SCROLL 18
04C82 0535F600 (00974)	MOVE	R3, R2, S1F600	LOAD SCROLL 19
04C84 0535F800 (00975)	MOVE	R3, R2, S1F800	LOAD SCROLL 20
04C86 0535FA00 (00976)	MOVE	R3, R2, S1FA00	LOAD SCROLL 21
04C88 0535FC00 (00977)	MOVE	R3, R2, S1FC00	LOAD SCROLL 22
04C8A 0535FE00 (00978)	MOVE	R3, R2, S1FE00	LOAD SCROLL 23
(00979) *			
(00980) *			
(00981)	EJECT		

PROGRAM # 840001.01A
 TO PROCESS THE LOAD I/O SCROLL FOR

010100	LOAD I/O-3 SCROLL		
010101			
010102			
010200	LOAD I/O-3 SCROLL		
010201			
010202			
010203			
010204			
010205			
010206			
010207			
010208			
010209			
010300	LOAD I/O-3 SCROLL		
010301			
010302			
010303			
010304			
010305			

ADJUST REPEAT COUNT
 EXECUTE PROPER BLOCK MOVE

LOAD SCROLL 16
 LOAD SCROLL 17
 LOAD SCROLL 18

REMOVE R3, R2, SIF900
 REMOVE R3, R2, SIF900
 REMOVE R3, R2, SIF900

EJECT

04CAB 3C21	(01036)	LDSS02	IRS	R2, 1	ADJUST REPEAT COUNT
04CAJ 2121	(01037)		DECR	R2, HS	
04CAN R2084CAC	(01038)		XCT	LDSSCRL4(R4)	EXECUTE PROPER LPROCL
04CAA 0470	(01039)		RETURN		
04CAN 0M00	(01040)		EVEN		
04CAC 0635F1F	(01041) *				
04CAC 0635F1F	(01042)	LDSSCRL4	LPROCL	R1, R2, STEPF	LOAD SCROLL 16
04CAB 0635F1F	(01043)		LPROCL	R1, R2, STEPF	LOAD SCROLL 17
04CHR 0635F1F	(01044)		LPROCL	R1, R2, STEPF	LOAD SCROLL 18
04CH2 0635F1F	(01045)		LPROCL	R1, R2, STEPF	LOAD SCROLL 19
04CH4 0635F1F	(01046)		LPROCL	R1, R2, STEPF	LOAD SCROLL 20
04CH6 0635F1F	(01047)		LPROCL	R1, R2, STEPF	LOAD SCROLL 21
04CH8 0635F1F	(01048)		LPROCL	R1, R2, STEPF	LOAD SCROLL 22
04CHA 0635F1F	(01049)		LPROCL	R1, R2, STEPF	LOAD SCROLL 23
04CHA 0635F1F	(01050) *				
	(01051)		EJECT		

MODULE TO PROCESS THE RUN I/O SCROLL FOR

FOR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FOR AND FUNCTION LIST FLAG(LSB)	
1	67	0
2	SCROLL IDENT	
3	SCROLL TYPE	
4	SCROLL START ADDRESS	
5	0	0

OBJECT

(01083) * PNSS
 (01084) *
 (01085) *
 (01086) *
 (01087) *
 (01088) *
 (01089) *
 (01090) *
 (01091) *
 (01092) *
 (01093) *
 (01094) *
 (01095) *
 (01096) *
 (01097) *
 (01098) *
 (01099) *
 (01100) *
 (01101) *
 (01102) *
 (01103) *
 (01104) *
 (01105) *
 (01106) *
 (01107) *
 (01108) *
 (01109) *
 (01110) *
 (01111) *
 (01112) *
 (01113) *
 (01114) *
 (01115) *
 (01116) *
 (01117) *
 (01118) *
 (01119) *
 (01120) *
 (01121) *
 (01122) *

PAGE 37: SNAP-11 IOS PACKAGE ---- APP 14, 1980 ---- PROGRAM 8 H40001.01A
 RNS5 MODULE TO PROCESS THE RUN IOS SCROLL FOR

04CF4 F0720001 (01121) RNS5	MOVW R7, 1(R7)	GRT SCROLL ID
04CF6 H60039FA (01124)	CALL R0, WAITSIUS	WAIT TILL SCROLL DEVICE FREE
04CF8 H6104006 (01125)	CALL R1, RNSSTNT	SET UP BUFFERS AND INTERRUPTS
04CFA 9C7F4FFD (01126) *		
04CFB 0C7F4FFD (01127)	ADDW R7, -4(R7)	CONVERT TO SCROLL INDEX
04CFC 0D60 (01128)	CLR R6	SET UP REGISTER FOR START SCROLL
04CFD 0800 (01129)	EVFN	
04CFE C0420002 (01130)	MOVW R4, 2(R4)	H4 = IOS TYPE 3 R5 = START LITERAL
04CF0 92400003 (01131)	CMPTW R4, 3	DISPATCH ON SCROLL TYPE
04CF2 H0104040 (01132)	JMP RNS01, F0	JUMP IF IOS-3
04CF4 92400004 (01133) *		
04CF6 H010404A (01134)	CMPTW R4, 4	TEST FOR IOS-4
04CF8 9010404A (01135)	JMP RNS02, F0	JUMP IF IOS-4
04CFB 9010404A (01136) *		
04CFD 9010404A (01137) *		
04CFE 9010404A (01138) *		
04CF0 3A5H (01139)	LI.S R5, R	MOVE START ADDRESS TO LEFT BYTE
04CF2 F00F403D (01140)	MOVW R5, RNS010S2(R7)	START THE SCROLL
04CF4 3C71 (01141)	LI.S R7, 1	
04CF6 F02F4009 (01142)	MOVW R2, ACQSLIST(R7)	GET ACQUISITION MODE FLAG
04CF8 F02F4009 (01143) *		
04CFA 1810 (01144)	SKP F02	SKIP IF DISCRETE SAMPLING
04CFB 0F70 (01145)	RTURN	FALSE, EXIT
04CFD 0F70 (01146)	EVFN	
04D00 90600009 (01147) *		
04D02 3A71 (01148)	MOVW R6, 0	POINT TO SYNCSTOP REGISTER
04D04 F0A2F403D (01149)	LI.S R7, 1	
04D06 F0A2F403D (01150)	MOVW R2, RNS010S2(R7)	THEN REFERENCE IT TO SET SYNCSTOP
04D08 0F70 (01151)	RTURN	
04D0A 0F70 (01152)	EVFN	
04D0C 0F70 (01153) *		
04D0E 0F70 (01154)	EVFN	

PAGE 34: SNAP-11 I/O PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A
 RNSS MODULE TO PROCESS THE RUN I/O SCROLL PTH

04006 F0704061 (01155)	RNSSFINT	MOVW	R7, RNSSFINT+HS	SPT-UP, THEN
04008 R2004060 (01156)	XCT	XCT	RNSSFINT	ENABLE SCROLL INTERRUPT, LEVEL 1
0400A F0704063 (01157)	MOVW	MOVW	R7, RNSSFINT+3*HS	DITTO FOR LEVEL 2
0400C R2004062 (01158)	XCT	XCT	RNSSFINT+HS	
0400E F0704065 (01159)	MOVW	MOVW	R7, RNSSFINT+5*HS	DITTO FOR LEVEL 3
04010 R2004064 (01160)	XCT	XCT	RNSSFINT+2*HS	
04012 R0200001 (01162)	MOVW	MOVW	R2, 1	
04014 F02F40C1 (01163)	MOVW	MOVW	R2, IUSFLG-16(R7)	FLAG SCROLL BUSY
04016 9C7F1F50 (01165)	ADDW	ADDW	R7, -32(R7)	CONVERT TO INDEX
04018 2671 (01167)	INCR	INCR	R7, 1	
0401A 0800 (01168)	EVEN	EVEN		FORCE INTERRUPT POINTERS AND BUFFER FLAGS
0401C R6704012 (01169)	CALL	CALL	R7, IUSINT	TO CORRECT STATE
0401E 9C700010 (01170)	ADDW	ADDW	R7, 16	POINT TO BUFFERS 1 AND 3
04020 1000001F (01173)	CINT	CINT	IOFVSN3, IELVIS1	
04022 9F700011 (01175)	SUBW	SUBW	R7, 17	CLEAR I/O DONE
04024 406F (01176)	MOVW	MOVW	R6, R7	
04026 3A71 (01177)	LJS	LJS	R7, 1	COMPUTE POINTER TO CSW WORD
04028 4C7C (01178)	ADDW	ADDW	R7, R6	
0402A 906F0120 (01179)	MOVW	MOVW	R6, CSWSIUS-WS(R7)	
0402C CA6F02H6 (01180)	POPWIL	POPWIL	R6, CSWSI161(R7)	
0402E CA6F02H8 (01181)	POPWIL	POPWIL	R6, CSWSI162(R7)	
04030 CA6F02HA (01182)	POPWIL	POPWIL	R6, CSWSI163(R7)	
04032 0F70 (01183)	RTURN	RTURN		
04034 0F70 (01184)	EVEN	EVEN		
04036 0F70 (01185)				
04038 0F70 (01186)				
0403A 0F70 (01187)				

04030 0010E1FF	(01189) *	START ADDRESS TABLE FOR IUS2	
04032 0010E1FF	(01190) RNS\$IOS2	ADDR	\$1FFFC(R6,1)
04034 0010E1FF	(01191)	ADDR	\$1FFFC(R6,1)
04036 0010E1FF	(01192)	ADDR	\$1FFFC(R6,1)
04038 0010E1FF	(01193)	ADDR	\$1FFFC(R6,1)
04040 0010E1FF	(01194)	ADDR	\$1FFFC(R6,1)
04042 0010E1FF	(01195)	ADDR	\$1FFFC(R6,1)
04044 0010E1FF	(01196)	ADDR	\$1FFFC(R6,1)
04046 0010E1FF	(01197)	ADDR	\$1FFFC(R6,1)
04048 0010E1FF	(01198)	ADDR	\$1FFFC(R6,1)
04050 0010E1FF	(01199) *	START IUS-3 TYPE SCROLL	
04052 0010E1FF	(01200) *	MOVEM R5, WNSS\$IOS3(R7)	START THE SCROLL
04054 0010E1FF	(01201)	RETURN	
04056 0010E1FF	(01202)	EVFN	
04058 0010E1FF	(01203)	START ADDRESSES FOR IUS-3	
04060 0010E1FF	(01204)	ADDR	\$1FFFC(R6,1)
04062 0010E1FF	(01205)	ADDR	\$1FFFC(R6,1)
04064 0010E1FF	(01206)	ADDR	\$1FFFC(R6,1)
04066 0010E1FF	(01207)	ADDR	\$1FFFC(R6,1)
04068 0010E1FF	(01208)	ADDR	\$1FFFC(R6,1)
04070 0010E1FF	(01209)	ADDR	\$1FFFC(R6,1)
04072 0010E1FF	(01210)	START IUS-4 TYPE SCROLL	
04074 0010E1FF	(01211)	MOVEM R5, 12	MOVE START ADDR TO LEFT MOST DIGIT
04076 0010E1FF	(01212)	EVFN	
04078 0010E1FF	(01213)	MOVEM R5, WNSS\$IOS4(R7)	START THE SCROLL
04080 0010E1FF	(01214)	RETURN	
04082 0010E1FF	(01215)	EVFN	
04084 0010E1FF	(01216)	START ADDRESSES FOR IUS-4	
04086 0010E1FF	(01217)	ADDR	\$1FFFC(R6,1)
04088 0010E1FF	(01218)	ADDR	\$1FFFC(R6,1)
04090 0010E1FF	(01219)	ADDR	\$1FFFC(R6,1)
04092 0010E1FF	(01220)	ADDR	\$1FFFC(R6,1)
04094 0010E1FF	(01221)	ADDR	\$1FFFC(R6,1)
04096 0010E1FF	(01222)	ADDR	\$1FFFC(R6,1)
04098 0010E1FF	(01223)	ADDR	\$1FFFC(R6,1)
04100 0010E1FF	(01224)	ADDR	\$1FFFC(R6,1)
04102 0010E1FF	(01225)	ADDR	\$1FFFC(R6,1)
04104 0010E1FF	(01226)	ADDR	\$1FFFC(R6,1)
04106 0010E1FF	(01227)	ADDR	\$1FFFC(R6,1)
04108 0010E1FF	(01228)	ADDR	\$1FFFC(R6,1)
04110 0010E1FF	(01229)	ADDR	\$1FFFC(R6,1)
04112 0010E1FF	(01230)	EVFN	

PAGE 40: SNAP-11 IUS PACKAGE ---- APR 18, 1960 ---- PROGRAM # R40001.01A
RNS\$ MODULE TO PROCESS THE RUN IUS SCROLL FOR

04060 12400000	(01231) *	INTERPUPT ENABLE INSTRUCTIONS
04062 12400000	(01232) *	
04064 12400000	(01233) *	
	(01234) *	
	(01235) *	
	(01236) *	
	(01237) *	
	(01238) *	
	(01239) *	

MS\$, ILVLS1
PRINT
MS\$, ILVLS2
PRINT
MS\$, ILVLS3
PRINT
FVFN

HAAS MODULE TO PROCESS THE RUN ADAM AND AUM FCH

(01240) * HAAS
 (01241) *
 (01242) *
 (01243) *
 (01244) *
 (01245) *
 (01246) *
 (01247) *
 (01248) *
 (01249) *
 (01250) *
 (01251) *
 (01252) *
 (01253) *
 (01254) *
 (01255) *
 (01256) *
 (01257) *
 (01258) *
 (01259) *
 (01260) *
 (01261) *
 (01262) *
 (01263) *
 (01264) *
 (01265) *
 (01266) *
 (01267) *
 (01268) *
 (01269) *
 (01270) *
 (01271) *
 (01272) *
 (01273) *
 (01274) *
 (01275) *
 (01276) *
 (01277) *
 (01278) *
 (01279) *

FCH FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCH AND FUNCTION LIST FLAG(LSB)	
1	6R	ADAMID
2	ADAMSA	ADMTD
3	ADMSA	0
4	0	0
5	0	0

SUBJECT

REGISTER USAGE	
(01280) *	R1 POINTS TO FCB#
(01281) *	R2 POINTS TO FCB#
(01282) *	R3 = ADAMID
(01283) *	R4 = ADMISA
(01284) *	R5 = ADMID
(01285) *	R6 = ADMISA
(01286) *	
(01287) *	
(01288) *	
(01289) *	
(01290) *	
(01291) *	
(01292) *	
(01293) *	
(01294) *	
(01295) *	
(01296) *	
(01297) *	
(01298) *	
(01299) *	
(01300) *	
(01301) *	
(01302) *	
(01303) *	
(01304) *	
(01305) *	
(01306) *	
(01307) *	
(01308) *	
(01309) *	
(01310) *	
(01311) *	
(01312) *	
(01313) *	
(01314) *	
(01315) *	
(01316) *	
(01317) *	
(01318) *	
(01319) *	
(01320) *	
(01321) *	
(01322) *	
(01323) *	

04066 4022	MOVW R2, R1	; COPY R1 TO R2
04067 704000FF	MOVW R3, R2, MMSKSHYT	; (R3) <---- ADAMID
04068 2621	INCR R2, 1	; R2 POINTS TO NEXT WORD IN FCB
0406A 7040FF00	MOVW R4, R2, MMSKSLAYT	; (R4) <---- ADAMSA
0406C 705400FF	MOVW R5, R2, MMSKSHYT	; (R5) <---- ADMID
0406E 2621	INCR R2, 1	; R2 POINTS TO NEXT WORD OF FCB
0406F 7064FF00	MOVW R6, R2, MMSKSLAYT	; (R6) <----- ADMISA
04071 4076	MOVW R7, R4	
04072 860049FA	CALL R0, WAITSIUS	WAIT TILL ADAM FREE
04074 86104006	CALL R1, MMSINT	SET UP BUFFERS AND INTERRUPTS
04076 407A	MOVW R7, R5	
04077 860049FA	CALL R0, WAITSIUS	WAIT TILL ADM FREE
04079 86104006	CALL R1, MMSINT	SET UP BUFFERS AND INTERRUPTS
0407B 9C37FF00	ADDW R3, -32(R3)	CONVERT TO SCROLL INDEX
0407D 9C50FF00	ADDW R5, -32(R5)	CONVERT TO SCROLL INDEX
0407E 07F0	SCS 14	; MASTER DISABLE THE INTERRUPTS
04080 F0C64030	MOVW R4, MMSNLSIOS2(R3)	; START ADAM
04082 F0F84030	MOVW R6, MMSNLSIOS2(R5)	; START ADM
04084 03F0	CCS 14	; MASTER ENABLE THE INTERRUPTS
04085 3C31	LPS R3, 1	
04086 F02649D9	MOVW R2, ACQSLIST(R3)	GET ACQUISITION MODE FOR ADAM
04088 8110400F	JMP MPRAAS0, NFZ	
0408A 90800000	MOVW R6, 9	IF = 0,
0408C 3A31	LPS R3, 1	
0408D F0A64030	MOVW R2, MMSNLSIOS2(R3)	SET SYNCSTOP REGISTER

PAGE 43: SNAP-11 IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A
 RAAS MODULE TO PROCESS THE RUN ADAM AND AOM FOR

0409F 3C51	(01324) MPRAAS0	IRS	R5, 1	
04090 F02A4009	(01325)	MOVW	R2, ACUSLIST(R5)	GET ACQUISITION MODE FOR AOM
04092 H1104009	(01326)	JMP	MPRAAS1, NZ	
04094 90600009	(01327) *			
04096 3A51	(01328)	MOVW	R6, 9	IF = 0,
04097 F0AA4030	(01329)	LIS	R5, 1	
	(01330)	MOVW	R7, @RNSST0S2(R5)	SET SYNCSTOP REGISTER
04099 0F70	(01331) *			
	(01332) MPRAAS1	RTURN		
	(01333)	EVEN		

MSKS	MODULE TO PROCESS THE WRITE INTO SCROLL REGISTERS FCH
(01396) *	
(01397) *	
(01398) *	
(01399) *	
(01400) *	
(01401) *	
(01402) *	
(01403) *	
(01404) *	
(01405) *	
(01406) *	
(01407) *	
(01408) *	
(01409) *	
(01410) *	
(01411) *	
(01412) *	
(01413) *	
(01414) *	
(01415) *	
(01416) *	
(01417) *	
(01418) *	
(01419) *	
(01420) *	
(01421) *	
(01422) *	
(01423) *	
(01424) *	
(01425) *	
(01426) *	
(01427) *	
(01428) *	
(01429) *	
(01430) *	
(01431) *	
(01432) *	
(01433) *	
(01434) *	
(01435) *	

FCH FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCH AND FUNCTION LIST FLAG(LSH)	
1	71	INTEGER SCALAR IDENTIFIER
2		SCROLL IDENTIFIER
3		SCROLL TYPE
4		STARTING REGISTER
5		COUNT

OBJECT

AD-A091 663

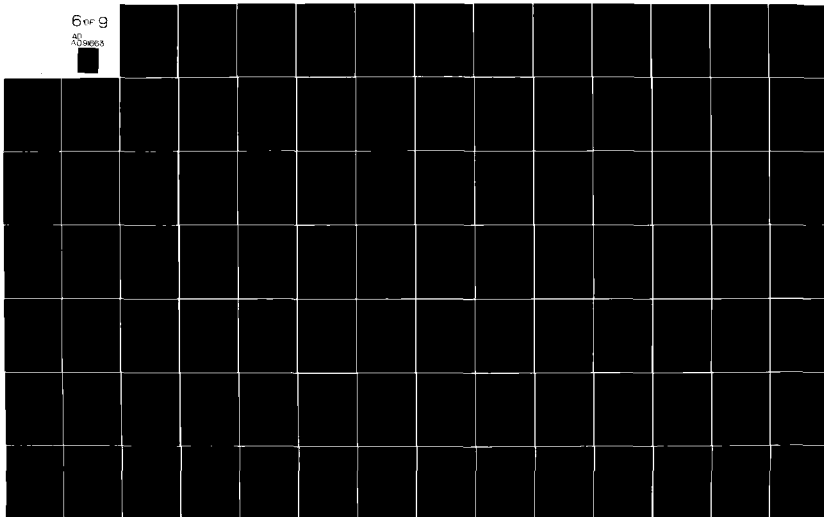
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME SO--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

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NL

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AD
000000



PAGE 47: SNAP-11 I/O PACKAGE ---- APR 14, 1980 ---- PROGRAM # R40001.01A
 WSR5 MODULE TO PROCESS THE WRITE INTO SCROLL REGISTERS FOR

(01436) *					
(01437) *	MODULE ASSUMES I/O-2 ONLY				
(01438) *					
04DM2 F0420001	WSR5	MOVW R4, 1(R1)		GET SCROLL ID	
04DM4 9C49FFFD		ADDR R4, -12(R4)		CONVERT SCROLL ID TO WORD INDEX	
(01441) *					
04DM6 705200FF		MOVW R5, R1, WSR5HVT		GET INTGER TARGE ID	
04DM8 9C500501		ADDR R5, 1SVFS-1		CONVERT TO MAP ADDRESS-1	
04DMA C0620003		MOVW R6, 3(R1)		GET START REG. AND COUNT	
(01445) *					
(01446) *	ENTER HERE FROM I/O-5 WITH REG 4-7 SET UP PROPERLY				
(01447) *					
(01448) *					
(01449) *	R4	SCROLL INDEX			
(01450) *	R5	POINTER - 1 TO DATA TO LOAD SCROLL REGISTER			
(01451) *	R6	1ST SCROLL REGISTER			
(01452) *	R7	# SCROLL REGISTERS TO LOAD			
04DMC FC684031	WSR51	ADDR R6, WSR5IOS2+HS(R4)		CONVERT REG. ID TO PSEUDO-MEM. ADDR.	
04DMF 2662		INCR R6, 2			
04DMF F0604DC7		MOVW R6, WSR5HMOV+HS		STORE IN BLOCK MOVE INSTRUCTION	
04DC1 2771		DFCR R7, 1		CONVERT COUNT TO NO. OF REPEATS	
04DC2 82004DC6		XCT WSR5HMOV		WRITE INTO THE REGISTERS.	
04DC4 0F70		RETURN			
04DC5 0800		EVEN			
(01459) *					
(01460) *					
04DC6 D55F0000	WSR5HMOV	RMV R5, R7, WSR5+10000		BLOCK MOVE INSTRUCTION FOR WRITE	
(01462) *		EVEN			

SNAP-II IOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A
 102SHDR 105 SCROLL PROGRAM BINDING SUBROUTINE

(01464) * 102SHDR 105 SCROLL PROGRAM BINDING SUBROUTINE
 (01465) *
 (01466) * THIS MODULE BINDS THE SPECIFIED ATTRIBUTES OF A LOGICAL BUFFER
 (01467) * TO THE IOS-2 PROGRAM. THE UNBOUND SCROLL PROGRAM MUST HAVE BEEN
 (01468) * LOADED PRIOR TO CALLING THIS PROGRAM.
 (01469) * THE CHAIN INFORMATION IS CONTAINED IN THE RIGHTMOST 15 BITS OF A
 (01470) * 32 BIT IOS-2 INSTRUCTION.

(01471) * ROUTINE ASSUMES WL = 0 => LONG (16 BIT)

(01472) * THE FORMAT OF THE CHAIN DESCRIPTOR IS:

(01473) *
 (01474) *
 (01475) * BITS: 0-7 BACKWARD (WORD) POINTER TO THE NEXT CHAIN ENTRY
 (01476) * BITS: 8-14 ATTRIBUTE SPECIFIER
 (01477) * VALUE: ATTRIBUTE
 (01478) * 0 BASE ADDRESS
 (01479) * 1 BASE ADDRESS - SEPARATION
 (01480) * 2 BUFFER SEPARATION
 (01481) * 3 BUFFER EXTENT
 (01482) * 4 BUFFER SIZE
 (01483) * 5 BUFFER SIZE - OFFSET
 (01484) * 6 OFFSET
 (01485) *

(01486) * THIS MODULE IS CALLED WITH:

(01487) *
 (01488) *
 (01489) * R3 = BASE ADDRESS OF SCROLL PROGRAM ON BUS 1
 (01490) * R4 = SCROLL PROGRAM LOAD ADDRESS (LEAST SIGNIFICANT 16 BITS)
 (01491) * R5 = POINTER TO BINDING CHAIN ANCHOR
 (01492) * R6 = LOGICAL BUFFER ID

(01493) * TORDSOFF CONTAINS INTEGER OFFSET

(01494) *
 (01495) * -----
 (01496) *
 (01497) * RID .IF. 0 OR
 (01498) * BINDING CHAIN ANCHOR .IT. 0
 (01499) *
 (01500) *
 (01501) * => NO BINDING DONE.
 (01502) *
 (01503) * -----

04DCR 405A 102SHDR MOVWP R5, R5 GET BINDING CHAIN ANCHOR
 04DC4 1930 04DC4 1930 SKP GEZ

PAGE 49: SNAP-11 IOS PACKAGE ---- APR 18, 1960 ---- PROGRAM # 840001.01A
 IOS SHDRP IOS SCROLL PROGRAM BINDING SUBROUTINE

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040CA 0670 (01507) * RETURN EXIT IF NULL
040CB 406C (01508) * ADDR R6, R6 CONVERT BUFFER 10 TO WORD INDEX
040CC 1420 (01509) * SKP GTZ EXIT IF NULL
040CD 0670 (01510) * RETURN
040CE 0670 (01511) *
040CF 0670 (01512) *
040D0 0670 (01513) * --- REGISTER USAGE:
040D1 0670 (01514) *
040D2 0670 (01515) * R1 INSTRUCTION TO BE BOUND
040D3 0670 (01516) * P2 ATTRIBUTE & THEN RIGHT HALF OF INSTRUCTION
040D4 0670 (01517) * P7 DISPLACEMENT TO NEXT INSTRUCTION
040D5 0670 (01518) *
040D6 0670 (01519) * R5, 1 ADJUST BINDING POINTER
040D7 0670 (01520) *
040D8 0670 (01521) * MOVEM R1, IONDSPT+HS SET POINTER TO IOS PROGRAM ON BUS 1
040D9 0670 (01522) * MOVEM R4, IONDSPT+HS SET SCROLL ADDRESS
040DA 0670 (01523) *
040DB 0670 (01524) * GET NEXT INSTRUCTION TO BE BOUND
040DC 0670 (01525) * DISPATCH TO PROPER BINDING ROUTINE
040DD 0670 (01526) *
040DE 0670 (01527) * IONSHDRN MOVEM R1, IONDSPT GET INSTRUCTION TO BE BOUND
040DF 0670 (01528) * MOVEM R7, R2, MCKSRHT EXTRACT DISPLACEMENT TO NEXT INSTRUCT.
040E0 0670 (01529) * ILS R7, 1 ADJUST DISPLACEMENT TO HALF WORD
040E1 0670 (01530) * LRS R2, 7 POSITION ATTRIBUTE SPECIFIER AS WORD INDEX
040E2 0670 (01531) * ANDIR R2, SF RESET LEAST SIGNIFICANT BIT
040E3 0670 (01532) * JMP IONSHDRN(R2) DISPATCH TO PROPER BINDING MODULE
040E4 0670 (01533) *
040E5 0670 (01534) * --- BASE ADDRESS BINDING ---
040E6 0670 (01535) *
040E7 0670 (01536) *
040E8 0670 (01537) * IONSHDRN MOVEM R1, IONDSPT GET BASE ADDRESS ATTRIBUTES OF BUFFER
040E9 0670 (01538) * ANDIR R1, SIFF8 CLEAR OUT BUS AND BIT 16
040EA 0670 (01539) * IONKR R1, R3, S7 MERGE BUS AND BIT 16 FROM BUFFER
040EB 0670 (01540) * EVEN
040EC 0670 (01541) *
040ED 0670 (01542) * --- PATH FOR BINDING INTO IOS MEMORY ---
040EE 0670 (01543) * --- AND FOR LOOPING TO NEXT INSTRUCTION
040EF 0670 (01544) *
040F0 0670 (01545) * MOVEM R2, R4 GET LOW ORDER BITS
040F1 0670 (01546) *
040F2 0670 (01547) * MOVEM R1, IONDSPT MOVE BOUND INSTRUCTION TO IOS MEM.
040F3 0670 (01548) * TEST P7 AND OTHER INSTRUCTION IN CHAIN?
040F4 0670 (01549) * SKP NEZ
040F5 0670 (01550) * RETURN NO, EXIT

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(01551) *
0400A 4F5P      SURRR      R5, R7      YES, COMPUTE DISPLACEMENT TO NEXT INST.
0400B 40304C1A  JMR        IUSRR11, LTZ  HINDING CHAIN ERROR, EXIT
0400D 211A      HOP        IUSRDORN      OK, GET NEXT INST TO RE HOUND
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0400F C03C0592  IUSRDOR1 MOVMBL      R3, HCTSHA(R6)  GET HASE ADDRESS ATTRIBUTES OF RUFFER
0400G 9A11FFFH  ANDIR      R1, S1FFFF  CLEAR OUT RUS AND HIT 16
0400H 56160006  IORRR      R1, R3, $6  MERGE RUS ONLY
0400I 444C0604  SUMMR      R4, HCTSD(R6)  SUBTRACT SEPARATION
0400J 0630      DAC        R3
0400K 56160001  IORRR      R1, R3, 1    MERGE HIGH ORDER ADDRESS HIT
0400L 2116      HOP        #0          GO MOVE INST TO SCROLL MEMORY
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(01571) *
0400A F03C0604  IUSRDOR2 MOVMBL      R3, HCTSD(R6)  GET RUFFER SEPARATION
0400B 50267FFF  MOVRR      R2, R3, S7FFF  MERGE 15 BIT SEPARATION
0400C 211A      HOP        #1          GO MOVE INST TO SCROLL MEMORY
0400D 0800      EVEN
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0400D C03C0604  IUSRDOR3 MOVMBL      R3, HCTSD(R6)  GET RUFFER SEPARATION AND SIZE
0400E 4R3R      MULRR      R3, R4      COMPUTE HALF-WORD DISPLACEMENT
0400F FC4C0583  ADDRR      R4, RCTSHA+HS(R6)  ADD RUFFER HASE ADDRESS
0400G 50287FFF  MOVRR      R2, R4, S7FFF  MERGE 15 BIT LLIT FIELD
0400H 2123      HOP        #1          GO MOVE INST. TO SCROLL MEMORY
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0400H F04C0605  IUSRDOR4 MOVMBL      R4, HCTSD+HS(R6)  GET RUFFER SIZE-1
0400I 2641      INCR      R4, 1
0400J 50287FFF  MOVRR      R2, R4, S7FFF  MERGE 15 BIT LLIT FIELD
0400K 2129      HOP        #1          GO MOVE INSTRUCTION TO SCROLL MEMORY
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PAGE	SNAP-11 IUS PACKAGE	----	APR 18, 1980	----	PROGRAM # R40001.01A
	1025000K	105	SCROLL	PROGRAM	WINDING
					SUBROUTINE
0400F	F0304F30	(01595)	1025000K	MOVWR	R3, 1000SOFF
04010	F040C0005	(01596)		MOVWR	R4, ACISAD+HS(R6)
04012	2641	(01597)		INCR	R4, 1
04013	4F46	(01598)		SUBWR	R4, R3
04014	50287FFF	(01599)		MOVWR	R2, R4, 57FFF
04016	2132	(01600)		HDP	B1
04017	0800	(01601)		EVFN	
		(01602)	*		
		(01603)	-----	OFFSET	WINDING
		(01604)	*		
04018	F0304F30	(01605)	1025000K	MOVWR	R3, 1000SOFF
0401A	50267FFF	(01606)		MOVWR	R2, R3, 57FFF
0401C	2138	(01607)		HDP	B1
0401D	0800	(01608)		EVFN	
		(01609)	*		
		(01610)		EJECT	
		(01611)			

PAGE 52: SNAP-11 IUS PACKAGE ---- APR 14, 1940 ---- PROGRAM # 840001.01A
 IUS SHDR IUS SCROLL PROGRAM WINDING SUBROUTINE

04F1F 00000000 (01613)	INDSPPT ADDR	\$10000(R5)	POINTER TO STORE INST IN SCROLL
04F20 00000000 (01612)	INDSPPT ADDR	MS(R5)	POINTER TO GET INST FROM SCROLL PROGRAM
04F22 0000400F (01614) *			
04F24 0000400F (01614)	INDSHDR ADDR	102SHDR0	WINDING DISPATCH TABLE
04F26 0000400F (01615)	ADDR	102SHDR1	
04F28 0000400F (01616)	ADDR	102SHDR2	
04F2A 0000400F (01617)	ADDR	102SHDR3	
04F2C 0000400F (01618)	ADDR	102SHDR4	
04F2E 0000400F (01619)	ADDR	102SHDR5	
04F30 00004018 (01620)	ADDR	102SHDR6	
04F32 00004018 (01621) *			
04F34 00004018 (01622)	INDSHDR DATA	MS	OFFSET VALUE
04F36 00004018 (01623) *			
04F38 00004018 (01624)	EVFN		

ADAMSSSP MODULE TO PROCESS THE ADAM SIMPLIFIED SAMPLING PLAN

FOR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FOR AND FUNCTION LIST FLAG(LSB)	
1	73	ADAM NUMBER
2	LOGICAL BUFFER #1	LOGICAL BUFFER #0
3	CHANNEL #1	CHANNEL #0
4	COUNTER VALUE	
5	ACQUISITION MODE	TRIG. & CLOCK PARAMETERS

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SNAP-11 IOS PACKAGE --- APR 14, 1980 --- PROGRAM # H40001.01A
ADAMSSS1 MODULE TO PROCESS THE ADAM SIMPLIFIED SAMPLING PLAN

04F32 40720002 (016A5)	ADAMSSS1	MOVW	R7, WS(R1)	FIRST SET SINGLE OR DUAL SAMPLING
04F34 9A704F00 (016A6)	ANDIR	R7, MSKSHAYT		
04F36 1A10 (016A7)	SKPL	R0Z		SKIP IF SINGLE CHANNEL
04F37 90700002 (016A8)	MOVW	R7, 2		ELSE, SET DUAL CHANNEL FLAG
04F39 0800 (016A9)				
04F3A 40220002 (01670)	FVFN			
04F3B 40220002 (01671)	MOVW	R2, WS(R1)		GET MIX ADDRESSES
04F3C 9A2000FF (01672)	ANDIR	R2, MSKSHAYT		FIRST MIX ADDRESS TO R3
04F3E 96200000 (01673)	TORIR	R2, SH000		SET ACQUIRE HIT
04F40 F0A44F00 (01674)	MOVW	R6, ADMSHA(R7)		GET POINTER TO FMA IN IOS2 CODE
04F42 F0A44FCC (01675)	MOVW	R2, WADMSPLN(R7)		STORE FIRST MIX ADDRESS IN IOS2 PROG
04F44 0150 (01676)	CLR	R5		SET COUNTER FOR TWO PASSES
04F45 0800 (01677)	FVFN			
04F46 F0220001 (01678)				
04F48 0250 (01680)	ADAMSSS1	MOVW	R2, HS(R1)	GET LOGICAL BUFFER ID
04F49 1A10 (01681)	TEST	R5		
04F4A 3C28 (01682)	SKP	R0Z		
04F4B 4C24 (01683)	LMS	R2, H		
04F4D 4C5F (01684)				
04F4E 4C24 (01685)	ANDIR	R5, R7		CONSTRUCT POINTER FOR BUFFER 0 OR 1 INFO
04F4F 0800 (01686)	ANDIR	R2, MSKSHAYT		
04F50 0800 (01687)	ANDIR	R2, R2		CONVERT TO WORD INDEX
04F52 9A300007 (01688)	FVFN			
04F54 F0A44F04 (01689)	MOVW	R3, HCTSHA(R2)		GET BUFFER START ADDRESS
04F56 90F44FCC (01690)	ANDIR	R3, S7		CLEAR OUT UNWANTED BITS
04F58 763CFF00 (01691)	MOVW	R6, ADMSHA(R5)		CONSTRUCT POINTER TO BASE ADDR LOAD INST
04F5A 443C0000 (01692)	MOVW	R6, WADMSPLN(R7)		GET PNTR TO BASE ADDRESS LOC
04F5C C0340604 (01693)	TORIR	R3, R6, SFF0		CONSTRUCT PROPER BASE ADDRESS INSTRUCTION
04F5E 2663 (01694)	MOVW	R3, 0(R6)		STORE IT IN IOS PROG
04F60 F03C0000 (01695)	MOVW	R3, HCTSHA(R2)		GET BUFFER SEPARATION AND SIZE
04F62 F06A4F08 (01696)	FVFN	R6, WS+HS		STEP TO FIRST SEPARATION LOC
04F64 F08F4FCC (01697)	MOVW	R3, 0(R6)		
04F66 0270 (01698)	MOVW	R6, ADMSSEP(R5)		GET POINTER TO SEPERATION INFO
04F67 1910 (01699)	MOVW	R3, WADMSPLN(R7)		STORE IT
04F68 2003 (01700)	TEST	R7		
04F69 2666 (01701)	SKP	NEZ		SKIP IF DUAL CHANNEL SAMPLING
04F6A F08F4FCC (01702)	MOVW	ADAMSSS2		
04F6B 483H (01703)	INCW	R6, 3WS		POINT TO NEXT LOC FOR SEPERATION
04F6D 3C31 (01704)	MOVW	R3, WADMSPLN(R7)		AND STORE IT
04F6E 483H (01705)				
04F6F 3C31 (01706)	ADAMSSS2			
04F70 483H (01707)	MOVW	R3, R4		COMPUTE DISPLACEMENT TO BUFFER END
04F71 3C31 (01708)	LPS	R3, 1		

PAGE 56: SNAP-11 IOS PACKAGE ---- AFM 18, 1980 ---- PROGRAM # R40001.01A
 ADAMSSNP MODULE TO PROCESS THE ADAM SIMPLIFIED SAMPLING PLAN

04FAS 2161	(01753)	DECR	R6, 1	POINT TO IOS SIZE
04FAS 20F4ECC	(01754)	MOVAP	R6, 2	GET IOS SIZE
04FAS 2662	(01755)	INCR	R6, 2	POINT TO SLOT IN IOS BUFFER FOR ACQ. MODE
04FAS 20F4ECC	(01756)	MOVAP	R3, ADDRESSPIN(R7)	STORE RID 1 AS 'RID 2' FOR IOS
04FAS 2612	(01757)	INCR	R3, 2	
04FAS 20F4ECC	(01758)	MOVAP	R6, 2	POINT TO 1ST ADAM CONTROL REGISTER
04FAS 20F4ECC	(01759)	ADDIR	R2, -12(R2)	CONVERT ADAM DISPLACEMENT TO INDEX
04FAS 20F4ECC	(01760)	POPRT	R1, ADDRESSIN2(R2)	STORE ADAM COUNTER IN SCROLL REGISTER
04FAS 20F4ECC	(01761)	POPRT	R1, R4	GET ACQUIS. MODE: TRIG/CLK PAR
04FAS 20F4ECC	(01762)	MOVAP	R3, R4, MASKSHYT	EXTRACT TRIG/CLK SETTINGS
04FAS 20F4ECC	(01763)	INCR	R6, 2	
04FAS 2662	(01764)	MOVAP	R3, ADDRESSIN2(R2)	STORE IN SCROLL REG 2
04FAS 20F4ECC	(01765)	CLP	R6	SET-UP FOR LATER ADDRESS FETCH
04FAS 20F4ECC	(01766)	EVFN		
04FAS 20F4ECC	(01767)			
04FAS 20F4ECC	(01768)			
04FAS 20F4ECC	(01769)			
04FAS 20F4ECC	(01770)	LES	R4, R	EXTRACT ACQUISITION MODE
04FAS 20F4ECC	(01771)	MOVAP	R1, ADDRESSCH+HS	POINT TO START OF DUMMY FOR
04FAS 20F4ECC	(01772)	MOVAP	R3, ADDRESSPIN(R7)	POINT TO START OF SCROLL PROGRAM
04FAS 20F4ECC	(01773)	SUBIR	R3, WS	LOAD SCROLL WANTS POINTER TO SIZE
04FAS 20F4ECC	(01774)	MOVAP	R2, R3	
04FAS 20F4ECC	(01775)	ADDIR	R2, R3	PUT ACQUISITION MODE
04FAS 20F4ECC	(01776)	MOVAP	R4, 3(R2)	IN TRAILER (RA+R+3)
04FAS 20F4ECC	(01777)	CALL	R0, LSSADAM	LOAD THE ADAM SCROLL
04FAS 20F4ECC	(01778)	CALL	R0, RNSS	EXECUTE SCROLL PROGRAM
04FAS 20F4ECC	(01779)	POPXOL	R0, R1	RESTORE SAVED FOR POINTER
04FAS 20F4ECC	(01780)	RETURN		AND EXIT
04FAS 20F4ECC	(01781)			
04FAS 20F4ECC	(01782)			
04FAS 20F4ECC	(01783)			

TABLES FOR ADAM CODE SET-UP

TABLES FOR ADAM CODE SET-UP

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ADAMSSCS(P6,1) POINTER TO SINGLE CHANNEL PLAN
ADAMSSCS(R6,1) POINTER TO DUAL CHANNEL PLAN

ADCSFMA*WS + HS, 0

ADCSFMA*WS + HS, 0

ADCSH1AD*WS

ADCSH2AD*WS

ADCSH1AD*WS

ADCSH2AD*WS

ADCSH1SP*WS + HS

ADCSH2SP*WS + HS

ADCSH1SP*WS + HS

ADCSH2SP*WS + HS

ADCSH1IND*WS + HS

ADCSH2IND*WS + HS

ADCSH1IND*WS + HS

ADCSH2IND*WS + HS

ADCSMA1*WS + HS

ADCSMA2*WS + HS

DUMMY FCH FOR LOAD/RUN SCROLL CALLS

DUMMY FCH NUMBER

SCROLL NUMBER

SCROLL TYPE = IOS2

LITERAL START VALUE

0
0
0
2
0
0

```

PAGE 58:          CNAP-11 I/O PACKAGE      ---- APR 18, 1980 ----  PROGRAM # H40001.01A
                ADAM PROGRAM TO SUPPORT SINGLE CHANNEL SAMPLING

                                ADAM PROGRAM TO SUPPORT SINGLE CHANNEL SAMPLING
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011829) ADAMSNS REG IN IUS2(ADAMSNS)
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011835) ASCSFWA
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011839) ASCSMIAD
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PAGE 502      SNAP-11 IUS PACKAGE  ---- APR 14, 1980 ----  PROGRAM # H40001.01A
              ADAM PROGRAM TO SUPPORT SINGLE CHANNEL SAMPLING

A19 04F1C 00000000      (01861)      .JUMP(ASCSTART)
A1A 04F1F 04300000      (01862) *
A1B 04F20 1C300180      (01863) ASCSTOP CF1
A1C 04F22 1D306400      (01864)      STOP
              00000010      (01865) *
              (01866) ASCSIZE = 8A
              (01867) *
04F24      (01868)      .END
              (01869) *
              (01870) * SFT UP TRAILER INFORMATION
              (01871) *
              (01872)      DATA 2
04F25 0000      (01873)      DATA 0
04F26 0000      (01874)      DATA 0
04F27 0000      (01875)      DATA 0
04F28 0000      (01876)      DATA 0
04F29 FFFF      (01877)      DATA -1
04F2A FFFF      (01878)      DATA -1,-1,-1,-1
04F2B FFFF
04F2C FFFF
04F2D FFFF
              (01879)      .EVEN

              SET TRANSFER DIRECTION TO WRITE
              # CHANNELS : ACQUISITION MODE
              RID 3 : 2
              # SCROLL REGISTERS TO LOAD
              1ST SCROLL REGISTER TO LOAD
              INTEGER SCALAR ID OF OFFSET = NULL
              BINDING CHAIN ANCHORS = NULL

```

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PAGE 602 SNAP-11 IUS PACKAGE ---- APP 1R, 1980 ---- PROGRAM # 840001.01A
ADAM PROGRAM TO SUPPORT DUAL CHANNEL SAMPLING

(01880) * ADAM PROGRAM TO SUPPORT DUAL CHANNEL SAMPLING
(01881) *
(01882) *
(01883) * DATA ADCSSIZE*WS, 0 MODULE SIZE IN HW : LOGICAL RID 1:0
(01884) *
(01885) * ADAMSDCS REGIN IUS7(ADAMSDCS) ADAM PROGRAM FOR DUAL CHANNEL SAMPLING
(01886) *
(01887) * PR
(01888) *
(01889) * ADCSFWA LOAD(R1, MSS) SFT TO READ FROM PERIPHERAL
(01890) * ADCSFWA LOAD(R1, 0, TP) R1 = FIRST MUX ADDRESS
(01891) * SFT SEND INITIAL MUX ADDRESS
(01892) * START ACQUISITION
(01893) *
(01894) * ADCSH1AD LOAD(R0, MSS) R0 = BUFFER ONE START ADDRESS-SEP
(01895) * SUB(R0, MSS)
(01896) *
(01897) * ADCSSMA1 ADD(R1, MSS, TP) SEND SECOND CHANNEL ADDRESS
(01898) * ADCSRISP ADD(R0, MSS, TP) ACQUIRE SAMPLE
(01899) * ADD(R1, MSS, TP) SEND FIRST MUX ADDR : RELEASE S/H
(01900) * SUB(R1, S10)
(01901) * ADD(R0, MSS, TP) ACQUIRE SAMPLE
(01902) * ADCSH1ND JNE(R1, R0, MSS) IF END OF BLOCK
(01903) *
(01904) * INT1 INTERRUPT THE CSPI
(01905) * SKIPC(SYNCTOP) STOP PROGRAM AT CSPI REQUEST
(01906) *
(01907) * JUMP(ADCSSTIP)
(01908) *
(01909) * ADCSH2AD LOAD(R0, MSS) R0 = BUFFER TWO START ADDRESS-SEP
(01910) * SUB(R0, MSS)
(01911) *
(01912) * ADCSSMA2 ADD(R1, MSS, TP) SEND SECOND CHANNEL ADDRESS
(01913) * ADCSH2SP ADD(R0, MSS, TP) ACQUIRE SAMPLE
(01914) * ADD(R1, MSS, TP) SEND FIRST MUX ADDR : RELEASE S/H
(01915) * SUB(R1, S10)
(01916) * ADD(R0, MSS, TP) ACQUIRE SAMPLE
(01917) * ADCSH2ND JNE(R2, R0, MSS) IF END OF BLOCK
(01918) *
(01919) * INT1 INTERRUPT CSPI
(01920) * SKIPC(SYNCTOP) STOP PROGRAM AT CSPI REQUEST

```


PAGE 81: SNAP-11 IUS PACKAGE ---- APR 18, 1980 ---- PROGRAM # H40001.01A
ADAM PROGRAM TO SUPPORT DUAL CHANNEL SAMPLING

```

A1D 04F6A 00000000
A1F 04F6C 04300000 (01921) * JUMP(ADCSHAD)
A1F 04F6E 20300180 (01922) *
A20 04F70 21306400 (01923) ADCSTOP CFI
A20 04F70 21306400 (01924) STOP
000000021 (01925) ADCSSIZE = BA
04F72 (01926) *
(01927) * END
(01928) * SET UP TRAILER INFORMATION
(01929) *
(01930) *
(01931) * DATA 2
(01932) * DATA 0
(01933) * DATA 0
(01934) * DATA 0
(01935) * DATA 0
(01936) * DATA -1
(01937) * DATA -1,-1,-1,-1
(01938) * EVEN

```

ELSE, CONTINUE SAMPLING

FIRST TURN OFF ACQUISITION
THEN STOP THE SCROLL.

SET TRANSFER DIRECTION TO WRITE
CHANNELS : ACQUISITION MODE
MID 1 : 2
SCROLL REGISTERS TO LOAD
1ST SCROLL REGISTER TO LOAD
INTEGER SCALAR TO OF OFFSET = NULL
BINDING CHAIN ANCHORS = NULL

PAGE 67: SNAP-11 LOS PACKAGE ---- APR 18, 1980 ---- PROGRAM # M40001.01A
 ADAMS165 - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. SAMP. FCR

FCR FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	POINTER TO NEXT FCR AND FUNCTION LIST FLAG (LSB)	
1	74	ADAM NUMBER
2	LOGICAL BUFFER #1	LOGICAL BUFFER #0
3	0	0
4	COUNTER VALUE	
5	ACQUISITION MODE	TRIG. & CLOCK PARAMETER

END

(01939) * ADAMS165 - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. SAMP. FCR
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PAGE 61: SNAP-IT IOS PACKAGE ---- APR 18, 1960 ---- PROGRAM # R40001.01A
ADAMSINS - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. CAMP. FCH

04F7C F0220001	(01977)	ADAMSINS	MOVW	R2, HSRH1)	GET BUFFER IOS
04F7E 9A20000F	(01978)		ANDIR	R2, MASKSHVT	GET BUFFER IOS
04F80 4C2A	(01979)		ADDR	R2, R2	CONVERT TO RCT INDEX
04F81 0800	(01980)		FVEN		
04F82 C0340582	(01981)		MOVW	R3, RCTSHA(R2)	GET BUFFER BUS NUMBER AND BASE ADDRESS
04F84 9A300006	(01982)		ANDIR	R3, S6	CLEAR OUT UNWANTED BITS
04F86 906004FF	(01983)		MOVW	R6, ATASH1D	GET LOC. OF PTR TO BASE ADDR INST.
04F88 763CFEF0	(01984)		MOVW	R3, R6, SFF0	CONSTRUCT PROPER FORMAT TO BASE ADDR INST.
04F8A 843C0000	(01985)		MOVW	R3, 0(R6)	STORE IN IOS PROG.
04F8C C0340604	(01986)		MOVW	R3, RCTSHAD(R2)	GET BUFFER SEPERATION AND SIZE
04F8E 2663	(01987)		INCR	R6, S3	STEP TO SEPERATION LOC IN IOS PROG.
04F8F 0800	(01988)		FVEN		
04F90 F03C0000	(01989)		MOVW	R3, 0(R6)	STORE IN IOS PROG.
04F92 4838	(01990)		MULW	R3, R4	COMPUTE DISPLACEMENT TO BUFFER END LOC.
04F93 3C31	(01991)		LPS	R3, S1	GET BASE ADDR. IN PROPER IOS FMT.
04F94 FC340583	(01992)		ADDR	R3, RCTSHA+HS(R2)	ADD IN BASE ADDRESS TO YIELD BUFFER END
04F96 F030503F	(01993)		MOVW	R3, ATASH1F+HS	STORE IN IOS PROG.
04F98 F0220001	(01994)		MOVW	R2, HSRH1)	GET BUFFER IOS
04F9A 9A2000F0	(01995)		ANDIR	R2, MASKSHVT	GET BUFFER ID #1
04F9C 0220	(01997)		TEST	P2	SEE IF BUFFER IS USED
04F9D 80104F84	(01998)		JMP	ADMSCU, F0Z	
04F9F 3C28	(01999)		LPS	R2, S8	GET BUFFER ID IN PROPER FORMAT
04FA0 4C2A	(02000)		ADDR	R2, R2	IT IS, SO CONVERT TO RCT INDEX
04FA1 0800	(02001)		FVEN		
04FA2 C0340582	(02002)		MOVW	R3, RCTSHA(R2)	GET BUFFER BUS NUMBER AND BASE ADDR.
04FA4 9A300006	(02003)		ANDIR	R3, S6	CLEAR OUT UNWANTED BITS
04FA6 90600504C	(02004)		MOVW	R6, ATASH2D	GET LOC. OF POINTER TO BASE ADDR. INST.
04FA8 763CFEF0	(02005)		MOVW	R3, R6, SFF0	CONSTRUCT PROPER FORMAT FOR BASE ADDR.
04FAA 843C0000	(02006)		MOVW	R3, 0(R6)	STORE IN IOS PROG.
04FAC C0340604	(02007)		MOVW	R3, RCTSHAD(R2)	GET BUFFER SEPERATION AND SIZE
04FAE 2663	(02008)		INCR	R6, S3	STEP TO SEPERATION LOC IN IOS PROG.
04FAF 0800	(02009)		FVEN		
04FB0 F03C0000	(02010)		MOVW	R3, 0(R6)	STORE IN IOS PROG.
04FB2 4838	(02011)		MULW	R3, R4	COMPUTE DISPLACEMENT TO BUFFER END.
04FB3 3C31	(02012)		LPS	R3, S1	PUT BASE ADDR. IN PROPER IOS FMT.
04FB4 FC340583	(02013)		ADDR	R3, RCTSHA+HS(R2)	ADD IN BASE ADDRESS TO YIELD BUFFER END
04FB6 F030503F	(02014)		MOVW	R3, ATASH2F+HS	STORE IN IOS PROG.
04FB8 F0220001	(02015)		MOVW	R2, HSRH1)	
04FBA 9A2000F0	(02016)		ANDIR	R2, MASKSHVT	
04FBC 0220	(02017)		TEST	P2	
04FBD 80104F84	(02018)		JMP	ADMSCU, F0Z	
04FBE 3C28	(02019)		LPS	R2, S8	
04FBF 4C2A	(02020)		ADDR	R2, R2	
04FC0 0800	(02021)		FVEN		
04FC1 C0340582	(02022)		MOVW	R3, RCTSHA(R2)	
04FC3 9A300006	(02023)		ANDIR	R3, S6	
04FC5 90600504C	(02024)		MOVW	R6, ATASH2D	
04FC7 763CFEF0	(02025)		MOVW	R3, R6, SFF0	
04FC9 843C0000	(02026)		MOVW	R3, 0(R6)	
04FCA C0340604	(02027)		MOVW	R3, RCTSHAD(R2)	
04FCE 2663	(02028)		INCR	R6, S3	
04FCF 0800	(02029)		FVEN		
04FD0 F03C0000	(02030)		MOVW	R3, 0(R6)	
04FD2 4838	(02031)		MULW	R3, R4	
04FD3 3C31	(02032)		LPS	R3, S1	
04FD4 FC340583	(02033)		ADDR	R3, RCTSHA+HS(R2)	
04FD6 F030503F	(02034)		MOVW	R3, ATASH2F+HS	

DISTRIBUTE FCH PARAMETERS

PAGE 64: SNAP-11 I/Os PACKAGE ---- APR 18, 1980 ---- PROGRAM 1 040001.01A
ADAMS16S - MODULE TO PROCESS THE 16 CHANNEL ADAM SIMP. SAMP. FCH

```

04F08 3602 (02021) ADMS16C      PUSH11 R0, R1      SAVE FCH POINTER
04F09 2711 (02022)              DECR R1, R5      PREPARE FOR POP OPERATIONS
04F0A 3014 (02023)              POPX1 R1, R2      GET ADAM NUMBER
04F0B 0800 (02024)              EVEN              EXTRACT SCROLL IDENTIFIER
04F0C 9A2000FF (02025)          ADDIP R2, MSKSHRYT  STORE IN DUMMY FCH
04F0D F0204FF4 (02026)          MOVW R2, ADMSDFCH+MS
04F0E F020271 (02027)          MOVW R3, 1(R1)      GET RID 0
04F0F 503600FF (02028)          MOVW R4, R3, MSKSHRYT
04F10 F0404FFD (02029)          MOVW R4, ADMS16C+HS
04F11 F020311 (02031)          LPS R3, R          GET RID 1
04F12 3C38 (02032)              MOVW R4, ADMS16C  COMPUTE POINTER TO 'RID 2'
04F13 90404FF4 (02033)          ADDW R4, ADMS16C-2 IN SCROLL BUFFER AREA
04F14 F0404FFC (02034)          MOVW R3, 2(R4)
04F15 F0380002 (02035)          INCR R1, 2        POINT TO 1ST ADAM CONTROL REGISTER
04F16 2612 (02037)              MOVW R6, 2        CONVERT ADAM DISPLACEMENT TO INDEX
04F17 90600002 (02038)          ADDW R2, -32(R2)  STORE ADAM COUNTER IN SCROLL REGISTER 1
04F18 0C25FF40 (02039)          POPM1 R1, WNSS10S2(R2)
04F19 F0944030 (02040)          POPX1 R1, R4      GET ACQUIS. MODE: TRIG/CLK PARA.
04F1A 3018 (02042)              MOVW R3, R4, MSKSHRYT
04F1B 503800FF (02043)          INCR R6, 2        EXTRACT TRIG. & CLOCK SETTINGS
04F1C 2662 (02044)              MOVW R3, WNSS10S2(R2)
04F1D F0844030 (02045)          EVEN              STORE IN SCROLL REG 2
04F1E F0844030 (02046)          LPS R4, R          EXTRACT ACQUISITION MODE
04F1F 90104FF3 (02047)          MOVW R1, ADMSDFCH+HS
04F20 F0304FF4 (02048)          MOVW R3, ADMS16C  POINT TO START OF DUMMY FCH
04F21 9F300002 (02049)          SUBW R3, W5        POINT TO START OF SCROLL PROG.
04F22 6026 (02051)          MOVW R2, R3        LOAD SCROLL (R3 = START OF SCROLL BUFFER)
04F23 F0440003 (02052)          ADDW R2, R3        PUT ACQUISITION MODE IN SCROLL BUFFER
04F24 F0440003 (02053)          MOVW R4, 3(R2)    ( R4=R2+3 )
04F25 86004A42 (02054)          CALL R0, LSSADAM  LOAD THE ADAM WITH THE SCROLL PROGRAM
04F26 4C26 (02055)          CALL R0, KNSS      EXECUTE SCROLL PROGRAM
04F27 86004C14 (02057)          POPX1L R0, R1     RESTORE SAVED FCH POINTER
04F28 3302 (02058)          RETURN            AND EXIT
04F29 0F70 (02059)          EVEN
04F2A 0800 (02060)
04F2B 0800 (02061)

```

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PAGE 65:  SNB1-11 105 PACKAGE  ---- APR 18, 1980 ----  PROGRAM 8 H40001.01A
          ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING

          ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING
(02062) *
(02063) *
(02064) *
(02065) *
          DATA  A16SIZAMS, 0  MODULE SIZE IN HALF-WORDS : LOGICAL BID'S

          DATA  A16SIZAMS, 0  MODULE SIZE IN HALF-WORDS : LOGICAL BID'S
(02066) *
(02067) A16S16C 16S16C 16S16C 16S16C 16S16C 16S16C 16S16C 16S16C 16S16C 16S16C
(02068) *
(02069) *
(02070) *
          OPADD  SYNCSTOP, (6 .LS. 10) + (26 .LS. 5) + 4
          SET TO READ FROM ADAM

          SET TO READ FROM ADAM
(02071) *
          A16S16C=1
          LOAD  (R1, S8000)
          ADD  (R1, 0, TP)
          SET 1
          SET 2
          SET 3
          SET 4
          SET 5
          SET 6
          SET 7
          SET 8
          SET 9
          SET 10
          SET 11
          SET 12
          SET 13
          SET 14
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PAGE 44: SNAP-11 I/O PACKAGE ---- APR 14, 1960 ---- PROGRAM # R40001.01A
      ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING

A10 05024 14590001 (02105)      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #12
A11 0502A 141A0001 (02106)      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #11 INTO MAP MEMORY
A12 0502C 20590001 (02107)      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #13
A13 0502E 211A0001 (02108)      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #12 INTO MAP MEMORY
A14 05030 22590001 (02109)      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #14
A15 05032 231A0001 (02110)      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #13 INTO MAP MEMORY
A16 05034 24590001 (02111)      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #15
A17 05036 251A0001 (02112)      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #14 INTO MAP MEMORY
A18 05038 26590021 (02113)      ADD      (R1, S21, TP)    SET CHANNEL # TO 00 AND ACTIVATE
A19 0503A 271A0001 (02114)      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #15 INTO MAP MEMORY
A20 0503C 28590030 (02115) *      SUB      (R1, S10)      RESET S/H
A21 0503E 29590030 (02116) *      ATAKSH1P=BL
A22 05040 30005030 (02117) *      JMP      (R1, R0, MSS)    LOOP AGAIN IF BUFFER NOT FILLED
A23 05042 30005030 (02118) *
A24 05044 2C302000 (02119) *      INT1
A25 05046 2F341000 (02120) *      SKIPC      (SYNCSSTOP)    OTHERWISE, INTERRUPT THE CSPU
A26 05048 00000000 (02121) *      JUMP      (A16SSTOP)    AND STOP THE PROGRAM AT THE CSPU'S REQUEST
A27 0504A 54300400 (02122) *      (02125) *      OTHERWISE CONTINUE SAMPLING INTO THE SECOND BUFFER
A28 0504C 3000504C (02123) *      LOAD      (R0, MSS)
A29 0504E 31100000 (02124) *      SUB      (R0, MSS)
A30 05050 32590001 (02125) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #01
A31 05052 331A0001 (02126) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #00 INTO MAP MEMORY
A32 05054 34590001 (02127) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #02
A33 05056 351A0001 (02128) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #01 INTO MAP MEMORY
A34 05058 36590001 (02129) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #03
A35 0505A 371A0001 (02130) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #02 INTO MAP MEMORY
A36 0505C 38590001 (02131) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #04
A37 0505E 391A0001 (02132) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #03 INTO MAP MEMORY
A38 05060 3A590001 (02133) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #05
A39 05062 3B1A0001 (02134) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #04 INTO MAP MEMORY
A40 05064 3C590001 (02135) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #06
A41 05066 3D1A0001 (02136) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #05 INTO MAP MEMORY
A42 05068 3E590001 (02137) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #07
A43 0506A 3F1A0001 (02138) *      ADD      (R0, 1, TM)      PUT DATA FROM CHANNEL #06 INTO MAP MEMORY
A44 0506C 40590001 (02139) *      ADD      (R1, 1, TP)      ACTIVATE CHANNEL #08

```


PAGE 68: SNAP-11 LOS PACKAGE --- APR 14, 1960 --- PROGRAM # R40001.01A
 ADAM PROGRAM TO SUPPORT 16 CHANNEL SAMPLING

 0500M FFFF (02189) DATA -1 INTEGER SCALAR TO OF OFFSET = NULL
 0500C FFFF (02190) DATA -1,-1,-1,-1 WINDING CHAIN ANCHORS = NULL,
 0500D FFFF
 0500E FFFF
 0500F FFFF

ADMBR MODULE TO PROCESS ADAM ROTATE BUFFER FOR
 000000003
 ADMBR MODULE TO PROCESS ADAM ROTATE BUFFER FOR
 000000003
 SET TO MAP-300 ASSEMBLY
 OPADD TABS.(1,LS,14) + 19 DEFINE TAB PSEUDO OP

FCB FORMAT (16 BIT WORD FORMAT SHOWN)

WORD	LEFT BYTE	RIGHT BYTE
0	0	0
1	229	0
2	VALUE	ISA
3	UNDEF	0
4	0	0
5	0	0
6	0	0
7	0	0
8	0	0
9	0	0
10	0	0
11	0	0
12	0	0
13	0	0
14	0	0
15	0	0
16	0	0
17	0	0
18	0	0
19	0	0
20	0	0
21	0	0
22	0	0
23	0	0
24	0	0
25	0	0
26	0	0
27	0	0
28	0	0
29	0	0

SNAP-11 IOS PACKAGE --- APR 14, 1980 --- PROGRAM # H40001.01A
SPECIAL BINDING MODULE FOR ROTATE ADAM BUFFER

```

(02230) * SPECIAL BINDING MODULE FOR ROTATE ADAM BUFFER
(02231) *
(02232) * THE INPUT BUFFER 'U' IS SEPARATED INTO TWO 'BUFFERS'
(02233) * A AND B, WHERE
(02234) *
(02235) * A = 'BUFFER' STARTING WITH THE TRIGGER ADDRESS + 1
(02236) * AND EXTENDING TO THE LAST SAMPLE OF THE U BUFFER
(02237) *
(02238) * B = 'BUFFER' STARTING WITH THE FIRST SAMPLE OF THE
(02239) * U BUFFER AND EXTENDING TO THE SAMPLE INCLUDING THE
(02240) * TRIGGER ADDRESS
(02241) *
(02242) * THIS SPECIAL SUPPORT MODULE EXTRACTS THE TRIGGER ADDRESS
(02243) * FROM A PAIR OF CONSECUTIVE INTEGER SCALARS AND BINDS
(02244) * THE PERTINENT INFORMATION FOR 'BUFFERS' A AND B INTO THE
(02245) * APS MODULE
(02246) *
(02247) *
(02248) * CALL R1, APSNDW PERFORM STANDARD BINDING FIRST
(02249) *
(02250) * REG. 3 = HIGH ORDER BIT OF TRIGGER ADDRESS
(02251) * REG. 4 = LOW ORDER 16 BITS OF TRIGGER ADDRESS
(02252) *
(02253) * MOVBR R7, 1(R1) INTEGER SCALAR 'A' ID
(02254) * ANDIR R7, WSKSRBYT
(02255) *
(02256) * MOVBR R3, ISVTS(R7) GET TRIGGER VALUE
(02257) *
(02258) * INCR R4, 1 ADJUST TO 'ARASE'
(02259) * JMP ADHRSD0, NCHY IF CARRY,
(02260) * SMH 0, R3 SET BIT 17 IN ADDRESS
(02261) *
(02262) * ADHRSD0 MOVBR R4, RHARAS+1 PUT TRIGGER ADDRESS IN APS
(02263) * MOVBR R5, RHARAS PICK UP 'ARASE' INSTRUCTION
(02264) * TNRAR R5, R3, 1 GET HIGH ORDER BIT
(02265) * MOVBR R5, RHARAS STORE IN CONSTRUCTED BLOCK
(02266) *
(02267) * SMH 3, RHARAS SET SHORT BIT IN LOAD INSTRUCTIONS
(02268) * SMH 3, RHARAS-2
(02269) * SMH 3, RHARAS-R
(02270) *
(02271) * MOV R3, 1 MOVE HIGH ORDER ADDRESS BIT TO POS 17
(02272) * TNRAR R3, R4, SFFFF CONCATENATE BIT 17 WITH ADDRESS
(02273) *

```

PAGE 71: SNAP-IT I/O PACKAGE ---- APR 18, 1980 ---- PROGRAM # R40001.01A
SPECIAL WINDING MODULE FOR ROTATE ADAM BUFFER

```

05006 F0220002 (0227A) MOVW R2, 7(R1) GET UNDEF
05008 0A200000 (0227S) ABLEF R2, MSFSLVHT GET UNDEF ID FROM LEFT MYTF
05002 3C27 (02276) LFS R2, 7 CONVERT TO RCT INDEX
05004 F0740542 (02277) MOVW R7, RCTSAH(R2) GET HIGH ORDER ADDRESS BIT
05005 50A00001 (02278) MOVW R6, R7, 1
05007 4F61 (02279) POP R6, 1 MOVE TO BIT POS 17
05008 F0540543 (02280) MOVW R5, RCTSAH+HS(R2) LOW ORDER 16 BITS
0500A 56A0F0F0 (02281) TURNP R6, R5, SEFFF CONCATENATE BIT 17 TO ADDRESS
0500C F0740604 (02283) MOVW R7, RCTSAH(R2) GET SEPARATION
0500E 405C (02284) R5, R6 R5 = URASE
0500F F0540605 (02285) ADDRK R5, RCTSAH+HS(R2) COMPUTE BUFFER EXTENT
05011 4256 (02286) CMPPR R5, R3 TRIG BEYOND BUFFER RANGE?
05012 R13050FD (02287) JMP ADDRESS1, GFZ JUMP IF NOT
05014 403C (02288) MOVW R3, R6 TRIG = URASE
05015 F0305159 (02290) MOVW R3, RRAHAS+HS STORE IN APS PROGRAM
05017 0140 (02291) CLW R4
05018 56470000 (02292) TURNP R4, R3, $10000 UPPER BIT SET?
0501A 1810 (02293) SKP R0Z SKIP IF NOT
0501B 0200515B (02294) SMR 0, RRAHAS ELSE, SET UPPER BIT
0501D 4F66 (02296) ADDRNS1 SUMW R6, R3 URASE = TRIG
0501F 1820 (02297) SKP GTZ
05020 0800 (02298) NEG R6
05021 F02007B4 (02299) MOVW R2, TEMSO OFFSET MUST BE NON - NEGATIVE
05022 86001850 (02300) CALL R0, IDIVS SAVE R2 = RCT INDEX
05024 F02007B4 (02302) MOVW R2, TEMSO HSIZE := AHS(URASE-TRIG )/SEP
05026 F0540605 (02303) SUMW R5, RCTSAH + HS(R2) GET BUFFER SIZE - 1
05028 4F5F (02304) SUMW R5, R7 ASIZE - 1 := USIZE - RSIZE - 1
05029 F0505125 (02305) MOVW R5, RINDRL0K+3 ASIZE - 1
0502B 2771 (02307) DECR R7, 1
0502C F0705126 (02308) MOVW R7, RINDRL0K+9 RSIZE - 1
0502E 0F70 (02310) RETURN

```

```

(02411) * APU - ROTATE ADAM BUFFER
(02412) *
(02413) * THIS ROUTINE ASSUMES THAT DATA IS IN 16 BIT FORMAT -
(02414) * EITHER LONG & FIXED OR SHORT & FLOATING PT.
(02415) *
(02416) * THERE IS ONE ADDRESSOR FOR BOTH DATA TYPES
(02417) *
(02418) * THE SPECIAL BINDING MODULE FORCES THE DATA MODE
(02419) * TO BE SHORT, SINCE THERE IS NO ARITHMETIC OPERATION
(02420) * PERFORMED ON THE DATA.
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05004 0000 EVEN
05100 0000 DATA ADDRESS
05101 000A DATA ADDRESSZ

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ADAMRAPU BEGIN APU(ADAMR)

```

A00 05102 00FC0000 ADDRESSA MOV( IGA, 00 ) \ NOP
A01 05104 001C0000 JUMPC( ADDRESSA, FO )
A02 05106 20422042 CLEAR( RA )
A03 05108 00000000 NOP
A04 0510A 10000000 JUMPC( 0 )
A05 0510C 00000000 NOP
A06 0510E 00000000 NOP
A07 05110 00000000 NOP
A08 05112 00000000 NOP
A09 05114 00000000 NOP
0000000A ADDRESSZ = RA - ADDRESSA
05116
END

```

INSERT DUMMY NOP'S FOR 200/300
OBJECT COMPATIBILITY
(# NOP'S = # REAL INSTR'S ABOVE)

PAGE 73: SNAP-11 I/O PACKAGE ---- APP 14, 1980 ---- PROGRAM # 840001.01A
 APS - ROTATE ADAM BUFFER

```

(02146) * APS - ROTATE ADAM BUFFER
(02147) *
(02148) * SPECIAL BINDING MODULE MODIFIES BASE ADDRESSES FOR ALL
(02149) * THREE BUFFERS 'A', 'W', AND 'V' IN THE CONSTRUCTED
(02150) * INSTRUCTION BLOCK
(02151) * 1) SETS UP 'ARASE'
(02152) * 2) FORCES THE SHORT BIT FOR THE LOAD INSTRUCTIONS
(02153) *
(02154) *
(02155) * INPUT STREAM - HK: FI
(02156) * OUTPUT STREAM - YK: FO
(02157) *
(02158) * EVEN
(02159) *
(02160) * ADDR RMAPSSI
(02161) * ADDR ADDRMAPS
(02162) * DATA 0
(02163) * DATA RMAPSSZ
(02164) * ADDR RMAPSSA
(02165) * EVEN
(02166) *
(02167) * ADDRMAPS BEGIN APS(ROTUFAPS)
(02168) *
(02169) *
(02170) *
(02171) * JSN(RR0UT, P2)
(02172) * SET(R0) ENABLE OUTPUT
(02173) * INPUT PROGRAM
(02174) *
(02175) * RINDRLOK = #1.
(02176) *
(02177) * LOAD(RR0, I1, S)
(02178) * LOAD(RR1, MSS)
(02179) * SHR(RR0, MSS)
(02180) *
(02181) * LOAD(RR2, I1, S)
(02182) * LOAD(RR3, MSS)
(02183) * SHR(RR2, MSS)
(02184) *
(02185) * ADDR(RR0, I9), IF)
(02186) * SHR(RR1, I), JUMPP(ATRANS)
(02187) *
(02188) * ADDL(RR3, 0), JUMPP(OTRANS)
(02189) * JUMPP(PRENDIN)
  
```

POINTER TO CONSTR. INST. BLOCK
 POINTER TO SCALAR BLOCK (NOT USED)
 MODULE SIZE
 CHAIN ANCHOR

SET OUTPUT PC
 [ARASE] (TRIGGER)
 [ASIZE] - 1
 ARASE - SEPARATION
 [RRASE] (= URASE)
 [RSIZE] - 1
 RRASE - SEPARATION

```

(02300) *
A0C 05136 18AAG00R (02301) WTRANS ADD(R2, [9], TF)
A0D 05138 1A300CR1 (02302) * SUBL(RW1, 1), JUMPP(OTTRANS)
(02303) *
A0E 0513A 1C200031 (02304) RNEADIN CLEAR(R1)
A0F 0513C 1F000020 (02305) * RUP(U)
(02306) *
(02307) *
A10 0513E 20300032 (02308) RNDUT SFT(RA)
(02309) *
A11 05140 2240000A (02400) * LOAD(RW0, [0], S)
A12 05142 24500000 (02401) * LOAD(RW1, MSS)
A13 05144 26020000 (02402) * SUB(RW0, MSS)
(02403) *
A14 05146 288A0006 (02404) OUTTRANS ADD(RW0, [H], TF)
A15 05148 2A111401 (02405) * SUBL(RW1, 1), JUMPP(OUTTRANS)
(02406) *
A16 0514A 2C200030 (02407) * CLEAR(R0)
A17 0514C 2F000020 (02408) * NOP(0)
(02409) *
00005146 (02410) RHAPSSA = PC
(02411) *
(02412) *
(02413) *
(02414) *
(02415) *
(02416) *
0514F 00000000 (02417) RHAPSS1 DATA 6F'0.0'
...
(02418) *
00005158 (02419) RHAPSS = BL - 2
(02420) *
0000003C (02421) RHAPSSZ = BL - ADMPHAPS
(02422) *

```

[YBASE]
 [YSIZE] - 1
 BASE := BASE - SEPERATION

STORAGE FOR CONSTRUCTED INSTRUCTIONS

POINT TO 'ARASE' SUBSTITUTION
 MODULE SIZE

PAGE 74: SNAP-11 I/O PACKAGE ----- APR 18, 1980 ----- PROGRAM # H40001.01A

CROSS REFERENCE

(02423) * CROSS REFERENCE

(02424) *

(02425) *

(02426) * END OF MODULE

(02427) *

(02428) *

0000515A (02429) *

(02430) *

(02431) *

(02432) *

TOP SCUR=81.

CALCULATE END ADDRESS OF MODULE

0515A

END

A16SH1A:	00005 (02079) (02172)	
A16SH1D:	04F48 (01981) (02078)	
A16SH1F:	05036 (01993) (02188)	
A16SH2D:	0504C (02005) (02127)	
A16SH2F:	05092 (02015) (02167)	
A16SFMA:	04F40 (02072)	
A16SS1Z:	0005C (02065) (02178)	
A16SS1UP:	00059 (02123) (02174)	
ACQSL1ST:	04909 (00214) (00767)	(01142) (01317) (01325)
ADAMS16S:	04F7C (00091) (01977)	
ADAMSDCS:	04F30 (01791) (01885)	
ADAMSSCS:	04F4A (01790) (01829)	
ADAMSSS1:	04F46 (01679) (01735)	
ADAMSSS2:	04F6C (01702) (01707)	
ADAMSSS3:	04F92 (01719) (01733)	
ADAMSSSP:	04F32 (00090) (01665)	
ADCSH1AD:	00004 (01798) (01893)	(01921)
ADCSH1ND:	0000P (01808) (01902)	
ADCSH1SP:	00007 (01803) (01898)	
ADCSH2AD:	00011 (01799) (01908)	
ADCSH2ND:	00018 (01809) (01917)	
ADCSH2SP:	00014 (01804) (01913)	
ADCSFMA:	00001 (01794) (01889)	
ADCS17F:	00021 (01883) (01925)	
ADCSMA1:	00006 (01811) (01897)	
ADCSMA2:	00013 (01812) (01917)	
ADCSSTUP:	0001F (01906) (01923)	
ADMS16C:	04F5F (02030) (02033)	(02034) (02049) (02067)
ADMSHA:	04FD4 (01690) (01796)	
ADMSHEND:	04FDC (01710) (01806)	
ADMSCU:	04F48 (01998) (02021)	
ADMSD1CH:	04F22 (01746) (01770)	(01814) (02026) (02048)
ADMSFMA:	04FD0 (01674) (01793)	
ADMSFP:	04FD4 (01698) (01801)	
ADMSMA:	04F40 (01721) (01811)	
ADMSFLIN:	04FCC (01675) (01691)	(01699) (01705) (01711) (01714) (01716) (01727) (01731) (01750)
	(01754) (01756) (01771) (01790)	
ADMPHS:	050H0 (00101) (02248)	
ADMPHS0:	050H0 (02259) (02262)	
ADMPHS1:	050FD (02287) (02296)	
ADMPHSA:	00000 (02325) (02331)	(02332) (02343)
ADMPHSSZ:	0000A (02326) (02343)	
ADMPHAFS:	0511F (00100) (02360)	(02367) (02421)
ADMPHAPH:	05102 (00099) (02328)	

ADMPREFCH:	00065 (00019) (00097)		
AFUTSORG:	00066 (00020) (00097)		
APSHDR2:	00068 (00021) (02248)		
ASCMIAD:	00004 (01796) (01819) (01861)		
ASCMIIND:	00009 (01806) (01845)		
ASCMIISP:	00008 (01801) (01844)		
ASCSTRAD:	00007 (01797) (01851)		
ASCSTRND:	00014 (01807) (01857)		
ASCSTRSP:	00013 (01802) (01856)		
ASCSPMA:	00001 (01793) (01835)		
ASCSSIZE:	00010 (01826) (01866)		
ASCSTUP:	00018 (01849) (01863)		
ATRANS:	00008 (02385) (02386)		
ACTSAD:	00004 (00023) (00847) (00869) (01581) (01578) (01596) (01694) (01986)		
	(02008) (02283) (02285) (02303)		
ACTSAT:	00086 (00024) (00600) (00601) (00602) (00603) (01008)		
ACTSHA:	00582 (00025) (07511) (01537) (01560) (01580) (01588) (01709) (01715) (01981) (01992)		
	(02003) (02014) (02277) (02280)		
ACTSSIZE:	00040 (00026) (01004)		
ADPSCHK:	04040 (00790) (00795) (00821) (00998)		
ADPSPT:	04040 (01000) (01013)		
RINDPLK:	05122 (02306) (02308) (02375)		
ADPSAD:	00008 (00027) (01008)		
ADPSIND:	00006 (00028) (00600) (00601)		
ADPSIOWT:	00009 (00029) (00602) (00603)		
ADTRANS:	00000 (02388) (02391) (02397)		
CSWS1161:	00206 (00031) (00110) (01180)		
CSWS1162:	00208 (00114) (01181)		
CSWS1163:	00208 (00115) (01182)		
CSWS1171:	00208 (00116)		
CSWS1172:	00208 (00117)		
CSWS1173:	00208 (00118)		
CSWS1181:	00202 (00119)		
CSWS1182:	00204 (00120)		
CSWS1183:	00206 (00121)		
CSWS1191:	00208 (00122)		
CSWS1192:	00208 (00123)		
CSWS1193:	00208 (00124)		
CSWS1201:	00208 (00125)		
CSWS1202:	00208 (00126)		
CSWS1203:	00202 (00127)		
CSWS1211:	00204 (00128)		
CSWS1212:	00206 (00129)		
CSWS1213:	00208 (00130)		

PROGRAM # H40001.01A

[illegible]

PAGE	79:	SNAP-IT LOS PACKAGE CROSS REFERENCE	-----	APR 18, 1980	-----	PROGRAM # R40001.01A
		(01993) (01995) (02014) (02015) (02022) (02030) (02048) (02280) (02285) (02290)				
		(02303)				
10V243:	0003F	(00042) (00237) (00558) (00592) (01173)				
10V5:	01050	(00043) (02300)				
10V51:	00001	(00044) (00237) (00558) (00592) (01173) (01215)				
10V52:	00002	(00045) (01236)				
10V53:	00003	(00046) (01237)				
10SWTHL:	04044	(00552) (00556) (00575) (00579) (00583) (00587) (00600)				
10SKR1:	0404C	(00545) (00565) (00612) (00781) (00782)				
10SL1:	0406C	(00550) (00554) (00570) (00571) (00637) (00803) (00804) (00806) (00807) (00817)				
		(00818)				
102SMOR:	0406H	(00914) (00950) (00955) (00981) (01505)				
102SMOR0:	0400F	(01537) (01614)				
102SMOR1:	0400F	(01560) (01615)				
102SMOR2:	0400F	(01571) (01616)				
102SMOR3:	04000	(01574) (01617)				
102SMOR4:	0400H	(01587) (01618)				
102SMOR5:	0400F	(01595) (01619)				
102SMOR6:	0401H	(01605) (01620)				
102SMOR7:	04022	(01532) (01614)				
102SMOR8:	04004	(01527) (01554)				
10MSLPT:	0401F	(01522) (01547) (01611)				
10MSLPT:	04010	(00932) (01595) (01605) (01622)				
10MSLPT:	04020	(01521) (01527) (01612)				
10MSLPT:	0406C	(00718) (01055)				
10SFF10:	0407	(00929) (01073)				
10SFF11:	0401A	(01075) (01553)				
10SFF12:	0406F	(00792) (01057)				
10SFF13:	04002	(00797) (01059)				
10SFF14:	04005	(00823) (01061)				
10SFF15:	0400H	(00924) (01063)				
10SFF16:	0400H	(00857) (01065)				
10SFF17:	0400H	(00973) (01067)				
10SFF18:	04001	(00900) (00902) (01009)				
10SFF19:	04014	(00922) (00906) (01071)				
10SFF19:	0400B	(01056) (01084) (01060) (01062) (01064) (01066) (01068) (01070) (01072) (01074)				
		(01076) (01078)				
10SFF19:	04001	(00213) (00221) (00234) (00590) (01163)				
10SFF19:	04001	(00181) (00220)				
10SFF19:	04012	(00249) (00253) (00263) (00267) (00286) (00290) (00300) (00304) (00323) (00327)				
		(00337) (00341) (00360) (00364) (00374) (00378) (00397) (00401) (00411) (00415)				
		(00434) (00438) (00448) (00452) (00471) (00475) (00485) (00489) (00508) (00512)				
		(00522) (00526) (00545) (01169) (01171)				
10SFF19:	04024	(00277) (00314) (00351) (00388) (00425) (00462) (00499) (00516) (00564)				

CROSS REFERENCE

ISV15:	0502 (00047) (00931) (01378) (01441) (02256)
ISVTS17:	0000 (00048) (00928)
IPSSC12:	047C (00917) (00947) (00971)
IPSSC13:	047A (01922) (01040)
IPSSC14:	047A (01048) (01042)
IPSS:	040C (00079) (00748)
IPSS01:	047A (00091) (00915)
IPSS02:	047A (00077) (01020)
IPSS03:	047A (00079) (01046)
IPSS04:	047A (00047) (00852) (00861)
IPSS05:	047A (00753) (01777) (02056)
IPSS06:	0477 (00864) (00875)
IPSS07:	0477 (00775) (00780)
IPSS08:	0477 (00927) (00932)
IPSS09:	0000 (00052) (01235) (01236) (01237) (01394) (01461) (01612) (01835) (01839)
IPSS10:	0184 (01844) (01851) (01852) (01856) (01857) (01889) (01893) (01894)
IPSS11:	01847 (01888) (01899) (01901) (01902) (01908) (01909) (01912) (01913) (01914)
IPSS12:	01916 (01917) (02079) (02080) (02119) (02128) (02129) (02168) (02378) (02379)
IPSS13:	02382 (02383) (02401) (02402)
MPRAAS:	0406 (00081) (01292)
MPRAAS0:	0406 (01318) (01324)
MPRAAS1:	0409 (01326) (01332)
MSKSHYT:	0000 (00050) (00947) (00958) (01295) (01298) (01666) (01724) (01996) (02275)
MSKSHYT:	0004 (00051) (00784) (00766) (00785) (00813) (00839) (00862) (00867) (00940) (00953)
MSKSHYT:	01293 (01294) (01296) (01377) (01442) (01528) (01672) (01685) (01723) (01745) (01749)
MSKSHYT:	01763 (01978) (02025) (02029) (02043) (02254)
OUTRAMS:	0004 (02404) (02405)
PRAMS:	05158 (02262) (02283) (02265) (02267) (02268) (02269) (02290) (02294) (02419)
PRATSSA:	05146 (02363) (02410)
PRATSS1:	05146 (02359) (02417)
PRATSS2:	0003C (02362) (02421)
PRATSS3:	0000 (02389) (02394)
PRATSS4:	0001 (02370) (02398)
PRATSS5:	0474 (00080) (01123) (01778) (02057)
PRATSS6:	0404 (01132) (01201)
PRATSS7:	0404 (01135) (01213)
PRATSS8:	0406 (01155) (01156) (01157) (01158) (01159) (01160) (01235)
PRATSS9:	0406 (01125) (01155) (01302) (01306)
PRATSS10:	0406 (01140) (01150) (01190) (01312) (01313) (01322) (01330) (01384) (01451) (01761)
PRATSS11:	01765 (02040) (02045)
PRATSS12:	0404 (01201) (01207)
PRATSS13:	0405 (01215) (01221)
PRATSS14:	0405A (00085) (01377)
PRATSS15:	0406 (01379) (01390) (01394)

PAGE 01: SNAP-11 I/O PACKAGE ---- APR 14, 1980 ---- PROGRAM # 840001.01A
CROSS REFERENCE

SFTSSMR:	01C12 (00054) (00226)	
SNAPSLML:	00R6R (00055) (00179) (00225)	
STANT:	04900 (00067) (00185)	
TPMS0:	00704 (00057) (00760) (00934) (02299) (02302)	
TPMS1:	00705 (00058)	
TPMS2:	00706 (00059) (00844) (00872)	
TUPSCUP:	0515A (00072) (02429)	
TUPSPTR:	00298 (00060) (00070)	
WS:	00002 (00062) (00552) (00575) (00579) (00583) (00587) (01004) (01158) (01160) (01179)	
	(01665) (01671) (01695) (01704) (01722) (01728) (01746) (01772) (01793) (01794)	
	(01796) (01797) (01798) (01799) (01801) (01802) (01803) (01804) (01806) (01807)	
	(01808) (01809) (01811) (01812) (01826) (01883) (02026) (02050) (02065)	
WATTS0:	049FD (00237)	
WATTS105:	049FA (00234) (00239) (00755) (01124) (01301) (01305)	
WSPS:	040H2 (00086) (01439)	
WSPS1:	030MC (00911) (01453)	
WSPSHMIV:	040C6 (01455) (01457) (01461)	

LINES WITH ERRORS: 0 (MAP VERSION 800101.10) F- 0

T A B L E O F C O N T E N T S

SYSTEM DEPENDENT VARIABLES (SNAP-100 REF. 3.5)	PAGE 2
DISPATCH TABLE ENTRIES FOR F42R, F42RR	PAGE 3
SPECIAL BINDING MODULE FOR FFT SETUP	PAGE 4
SPECIAL BINDING FOR WBASE, YBASE, AND UBASE	PAGE 9
SET PRINTING SLOTS TO PROPER VALUES	PAGE 11
FFT - AP PROGRAMS	PAGE 12
ADP PROGRAMS	PAGE 12
SCRAMBLE AND FIRST RADIX-2 STAGE, FORWARD	PAGE 13
SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD	PAGE 14
SUCCESSIVE RADIX-4 STAGES, FORWARD	PAGE 16
EVEN-ODD SEPARATE	PAGE 20
APS PROGRAMS	PAGE 22
CSM - COMPLEX FFT SCRAMBLE (P0 - INPUT)	PAGE 23
CSM-SCRAMBLE SUBROUTINE (P1 - OUTPUT)	PAGE 25
CSM-SCRAMBLE SUBROUTINE (OUTPUT - P2)	PAGE 27
SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)	PAGE 28
GENERATE OUTPUT ADDRESSES FOR EVEN-ODD SEPARATE	PAGE 30
SUCCESSIVE RADIX-4 STAGES (INPUT - P0)	PAGE 31
GENERATE INPUT ADDRESSES FOR EVEN-ODD SEPARATE	PAGE 33
ANGULAR SEPARATION SUBROUTINE (INPUT - P1)	PAGE 34
SYMBOL TABLE	PAGE 35

```

(00001) :FAST FOURIER TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980
(00002) :
(00003) : MODIFIED FOR GTE SYLVANIA BY S. TERRACE
(00004) :
(00005) :MODIFICATIONS MADE TO CORRECT BUFFER PROBLEMS.....31 JANUARY 1979
(00006) :
(00007) : TWO REAL TRANSFORMS, Y=2.0*SAFE(X+17); X, Z REAL
(00008) : PERFORMS TWO REAL TRANSFORMS OF SIZE N ON
(00009) : THE U BUFFER, ONE REAL FUNCTION, X, IS STORED IN
(00010) : THE REAL PART OF U AND THE OTHER, Z, IS STORED IN
(00011) : THE IMAGINARY PART OF U. RESULTS ARE LEFT IN THE
(00012) : Y BUFFER WITH THE TRANSFORM OF X IN THE FIRST HALF
(00013) : AND THE TRANSFORM OF Z IN THE SECOND HALF. SINCE
(00014) : THE RESULT SAMPLES 0 AND N/2 ARE KNOWN TO HAVE
(00015) : ONLY REAL PARTS, THEIR RESULTS ARE STORED IN RN
(00016) : ZERO WITH RX(0)=RX(0) AND IX(0)=RX(N/2) AND
(00017) : RZ(0)=RZ(0) AND IZ(0)=RZ(N/2).
(00018) : THE ALGORITHM PRODUCES TWICE THE CORRECT VALUE FOR THE SPECTRAL
(00019) : OUTPUTS BEFORE THEY ARE WEIGHTED BY SCALAR A.
(00020) :
(00021) : THIS ROUTINE CANNOT BE DONE IN PLACE. I.E. THE
(00022) : Y CANNOT BE THE SAME AS W AND U CANNOT BE THE SAME
(00023) : AS W. BUT Y CAN BE THE SAME AS U.
(00024) :
(00025) :
(00026) : THE BUFFER DESCRIPTIONS ARE:
(00027) : Y BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(00028) : U BUFFER (10-39) COMPLEX 32-BIT FLOATING POINT
(00029) : V BUFFER (10-39) REAL 32-BIT FLOATING POINT
(00030) : W BUFFER (10-39) COMPACT, COMPLEX 32-BIT FLOATING POINT
(00031) :
(00032) : THE COSINE TABLE ENTRIES ARE:
(00033) : CT(R)=COS(2*PI*R/Csize)
(00034) : WHERE Csize IS A MULTIPLE OF N
(00035) :
(00036) :

```

```
(00031) *SYSTEM DEPENDENT VARIABLES (SNAP-300 REL. 3.5)
(00036) ?
(00039) ?
(00040) ?
(00041) ?
(00042) ?
(00043) ?
(00044) ?
(00045) ?
(00046) ?
(00047) ?
(00048) ?
(00049) ?
(00050) ?
(00051) ?
(00052) ?
(00053) ?
(00054) ?
(00055) ?
(00056) ?
(00057) ?
(00058) ?
(00059) ?
(00060) ?
(00061) ?
(00062) ?
(00063) ?
(00064) ?
(00065) ?
(00066) ?
(00067) ?
(00068) ?
(00069) ?
(00070) ?
(00071) ?
(00072) ?
(00073) ?
(00074) ?

AFDTS = SNFH
AFDTSORG = AFDTS
TUSPTR = $2RH
MSKSHRHT = $FF00
MSKSHRHT = $00FF
MS = 1
WS = 2*MS
ACTSHA = $582
ACTSAD = $604
ACTSAT = $686
MS = 0
FLGSSPT = $20
FLGSGO = $4
FLGSG1 = $5
SVTS = $382
APSKSL = $240
ZPRD = $78A
SVSSTGCS = $1FFCF
APSHNDMO = $0F63

BL = TUSPTR
ADDR TUSPTR(,1)
NM = 3
START = $4690

ARRAY FUNCTION DISPATCH
DISPATCH TABLE ORIGIN
POINTER TO TOP OF EXEC
MASK LEFT BYTE
MASK RIGHT BYTE
1 HALFWORD = 1 HALFWORD
1 FULLWORD = 2 HALFWORDS
BASE ADDRESS TABLE
ARRAY DEFINITION TABLE
BUFFER ATTRIBUTE TABLE
DUMMY ARGUMENT
SET FLAG HIT
FLAG GO
FLAG G1
SCALAR VALUE TABLE
ADDR OF ADDR OF HINDING SUPPORT LIST
ADDR OF ZERO

UPDATE TOP OF EXEC POINTER

OFFINE START LOCATION FOR MODULE
```



```

(00093) SPECIAL BINDING MODULE FOR FFT SETUP
(00095) ;
(00096) ; DOES ALL BINDING NOT NEEDED EVERY TIME
(00097) ; AM FFT IS DONE.
(00098) ;
(00099) ; ENTER WITH R1 POINTING TO FCH NUMBER
(00100) ; R2 POINTING TO DISPATCH TABLE.
(00101) ;
(00102) ;
(00103) ; GET 0 BUFFER TO
(00104) ; MASK OUT RIGHT HALF
(00105) ; SET FOR FULL WORD INDEX
(00106) ; SKIP TO EVEN BOUNDARY
(00107) ; R5= 0 BUFFER ADDRESS
(00108) ; STORE FOR LATER REFERENCE
(00109) ;
(00110) ; BIND ALL USIZE-1'S AND USIZE-2
(00111) ;
(00112) ; MOVWPL R5, R2SAD(R4)
(00113) ; MOVWPL R6, CSMS*W*CR4A03+HS
(00114) ; MOVWPL R6, CSMS*W*CSML05+HS
(00115) ; MOVWPL R6, CSMS*W*CR4A05+HS
(00116) ; MOVWPL R6, CSMS*W*CR4A05+HS
(00117) ; MOVWPL R6, CSMS*W*CR4A13+HS
(00118) ; DECH R6, 1
(00119) ; EVEN
(00120) ; MOVWPL R6, CSMS*W*F0S03+HS
(00121) ;
(00122) ; INCR R6, 2
(00123) ; MOVWPL R3, R5
(00124) ; MOVWPL R5, R6
(00125) ; LRS R5, 2
(00126) ;
(00127) ; BIND ALL R0'S
(00128) ;
(00129) ; MOVWPL R4, R0STHL-1
(00130) ; MOVWPL R4, R7
(00131) ; TEST R7
(00132) ; JMP R0STHL, R7
(00133) ; CMW 0.0(R7)
(00134) ; MOVWPL R5, 1(R7)
(00135) ; SKPL R4
(00136) ; SMW 0.0(R7)
(00137) ; HUP
(00138) ;
(00139) ; POINT TO TABLE OF HU BINDING LOC'S
(00140) ; GET NEXT BINDING ADDR
(00141) ; CHECK FOR END OF TABLE
(00142) ; ZERO MARKS END OF TABLE
(00143) ; RESET MSB OF DELTA FIELD
(00144) ; STORE LOW 16 BITS
(00145) ; IF NOT 0, DELTA IS OK
(00146) ; PRODUCT WAS 2*1R BEFORE CORRECTION
(00147) ; LOOP FOR ALL HU BINDING

```

```

(00138) ?
0460C 0250      TEST  R5      ; CHECK PRODUCT
0460D 1010      SKPL  R5      ; IF NOT 0, DELTA IS OK
0460E 4C510000  MOVAP  R5,S10000 ; SET UP CORRECT DELTA FOR 2**18

0460F 3C51      LRS  R5, 1
04610 0800      EVEN
(00145) ?
(00146) ?
(00147) ?
(00148) ?
04612 40504007  MOVPM  R5, CSMS*WS*CSM13S+HS
04613 4F56      SUMPR  R5, R3
04614 0800      EVEN
04615 405040CF  MOVPM  R5, CSMS*WS*CSM13S+HS
04616 405C      MOVPM  R5, R6
04617 0800      EVEN
04618 40504003  MULTR  R5, 3
(00155) ?
(00156) ?
(00157) ?
(00158) ?
04620 405040F5  MOVPM  R5, CSMS*WS*SCM0+HS
04621 405040F9  MOVPM  R5, CSMS*WS*SCM2S+HS
04622 405040FD  MOVPM  R5, CSMS*WS*SCM4S+HS
04623 405040FF  MOVPM  R5, CSMS*WS*SCM6S+HS
04624 3C51      LRS  R5, 1
04625 0800      EVEN
04626 405040F7  MOVPM  R5, CSMS*WS*SCM1S+HS
04627 405040FF  MOVPM  R5, CSMS*WS*SCM5S+HS
04628 3C51      LRS  R5, 1
04629 0800      EVEN
04630 405040F8  MOVPM  R5, CSMS*WS*SCM3S+HS
04631 3C51      LRS  R5, 1
04632 0800      EVEN
04633 405040F5  MOVPM  R5, CSMS*WS*SCM7S+HS
04634 3C51      LRS  R5, 1
04635 0800      EVEN
04636 405040F4  MOVPM  R5, CSMS*WS*SCMRS+HS
04637 3C51      LRS  R5, 1
04638 0800      EVEN
04639 405040F1  MOVPM  R5, CSMS*WS*SCM9S+HS
04640 3C51      LRS  R5, 1
04641 0800      EVEN
04642 405040F7  MOVPM  R5, CSMS*WS*SCM10S+HS
04643 3C51      LRS  R5, 1

```

; CHECK PRODUCT
 ; IF NOT 0, DELTA IS OK
 ; SET UP CORRECT DELTA FOR 2**18

R5<=00/2 (00)
 TO TO EVEN BOUNDARY

STORE 00'S
 R5<=00-USEP

STORE 00-USEP
 R5<=N

R5<=4*(3N/2)

STORE ALL 00'S
 ...

...
 R5<=00/2 (01)

STORE ALL 01'S
 ...

R5<=01/2 (02)
 STORE 02

R5<=02/2 (03)
 STORE 03

R5<=03/2 (04)
 STORE 04

R5<=04/2 (05)
 STORE 05

R5<=05/2 (06)
 STORE 06

R5<=06/2 (07)


```

04721 485C (00226) MOVW R5, R6 R5<2*(CSFZP)(CSIZE)
04722 3C5A (00227) LRS R5, 1 R5<0.25*(CSFZP)(CSIZE) (HPI)
04723 0800 (00228) EVN
(00229) ?
(00230) ?HIND ALL HPI'S
(00231) ?
(00232) ?
04724 F0504971 (00233) MOVW R5, CSMS+WS*CR4A5S+HS STORE ALL HPI'S
04725 F0504975 (00234) MOVW R5, CSMS+WS*CR4A6S+HS ...
04726 F0504979 (00235) MOVW R5, CSMS+WS*CR4A7S+HS ...
(00236) ?
(00237) ?SIANG=HPI/SSI, RIGHT NOW ONLY RADIX 2 AND RADIX 4
(00238) ?HAVE BEEN IMPLEMENTED. SO ALL THAT IS NECESSARY
(00239) ?TO CALCULATE SSI IS TO KNOW IF SIZE IS AN EVEN OR
(00240) ?ODD POWER OF TWO. THIS IS DONE BY CHECKING THE
(00241) ?POWER OF TWO ENTRY FOR THE 0 BUFFER IN THE HCTSAT
(00242) ?TABLE. IF BIT 0 IS ON, THEN IT'S AN ODD POWER.
(00243) ?
0472A F030024D (00244) MOVW R3, APSHSL ? GET HINDING SUPPORT LIST POINTER
0472C D000478R (00245) SMRC 0, PWR2S SKIP IF EVEN POWER OF TWO
0472E 2006 (00246) HOP SHMS1 ... ODD POWER
0472F F0004927 (00247) MOVW 4, CSMS+WS*CR4A01+HS SET AND HIND SSI = 4
04731 F00A47H1 (00248) MOVW FLSG1, GSFLGS+HS RADIX 4 FIRST STEP
04733 3C52 (00249) LRS R5, 2 SIANG=HPI/4
(00250) ?
04734 2005 (00251) HOP SHMS2
04735 F00A4927 (00252) MOVW 2, CSMS+WS*CR4A01+HS SET AND HIND SSI = 2
04737 F00A47H1 (00253) MOVW FLSG1+FLGSSET, GSFLGS+HS RADIX 2 FIRST STEP
04739 3C51 (00254) LRS R5, 1 SIANG=HPI/2
(00255) ?
(00256) ?
(00257) ?
0473A F05049H7 (00258) MOVW R5, CSMS+WS*ANGL1S+HS STORE SIANG
0473C 3C52 (00259) LRS R5, 2
0473D 0800 (00260) EVN
0473E F05049H9 (00261) MOVW R5, CSMS+WS*ANGL2S+HS STORE S2ANG
04740 3C52 (00262) LRS R5, 2
04741 0800 (00263) EVN
04742 F05049H1 (00264) MOVW R5, CSMS+WS*ANGL3S+HS STORE S3ANG
04744 3C52 (00265) LRS R5, 2
04745 0800 (00266) EVN
04746 F05049H1 (00267) MOVW R5, CSMS+WS*ANGL4S+HS STORE S4ANG
04748 3C52 (00268) LRS R5, 2
04749 0800 (00269) EVN

```

PAGE 02 MC FAST FOURIER TRANSFORM ALGORITHM - FFTM, FFTM MAY 7, 1980
 SPECIAL BINDING MODULE FOR FFT SETUP

0474A F05049C1 (00270)	MOVPM P5, (CMS+S*ANGL)S+HS	STORE SSANG
0474C 3C52 (00271)	IRS P5, 2	
0474D 0M00 (00272)	EVFN	
0474E F05049C1 (00273)	MOVPM P5, (CMS+S*ANGL)S+HS	STORE SAANG
04750 3C52 (00274)	IRS P5, 2	
04751 0M00 (00275)	EVFN	
04752 F05049C1 (00276)	MOVPM P5, (CMS+S*ANGL)S+HS	STORE STANG

```

(00277) ; SPECIAL BINDING FOR WBASE, YBASE, AND UBASE
(00278) ;
(00279) ; THIS SECTION DOES THE FAST BINDING FOR
(00280) ; CHANGES IN WBASE, YBASE, AND UBASE ONLY.
(00281) ;
(00282) ;
(00283) ;
(00284) ;
(00285) ;
(00286) ;
(00287) ;
(00288) ;
(00289) ;
(00290) ;
(00291) ;
(00292) ;
(00293) ;
(00294) ;
(00295) ;
(00296) ;
(00297) ;
(00298) ;
(00299) ;
(00300) ;
(00301) ;
(00302) ;
(00303) ;
(00304) ;
(00305) ;
(00306) ;
(00307) ;
(00308) ;
(00309) ;
(00310) ;
(00311) ;
(00312) ;
(00313) ;
(00314) ;
(00315) ;
(00316) ;
(00317) ;
(00318) ;
(00319) ;
(00320) ;

START ON EVEN BOUNDARY
GET W BUFFER ID
MASK THE LEFT HALF
CREATE FULL WORD INDEX

LOAD BASE ADDRESS IN R6, R7
POINT TO LOAD INSTRUCTION
ONLY LOW FOUR BITS
'OR'ED INTO LOAD INST.
STORE ALL WBASE'S
MASK OUT OLD LOAD INST.
POINT TO NEXT LOAD
OR INTO INSTRUCTION
HIND WBASE
MASK OUT OLD LOAD INSTR
POINT TO NEXT LOAD
PLACE MSR INTO SEP REG
CLR MSR TOF WBASE ADDR
WBASE-4
CARRY INTO MSR
PLACE PROPER MSR INTO RESULT
'OR' INTO LOAD INSTR
LOAD WBASE-4 INTO APS
MASK OUT OLD LOAD INSTR

LOAD U BUFFER ID
MASK THE LEFT HALF
CREATE FULL WORD INDEX

LOAD WBASE ADDRESS IN R6, R7
POINT TO LOAD WBASE INST.
ONLY LOW ORDER FOUR BITS
'OR' INTO INST.
STORE WBASE

```

04784	10420001	(00321) 2	
04790	2A40FF00	(00322) THIRD YBASE	
04792	3C47	(00323) 2	
04794	0800	(00324)	MOVW R4, HSP1
04796	0080582	(00325)	ANDR R4, MRSIMY
04798	20504450	(00326)	LES R4, J
0479A	2A000004	(00327)	LEW
0479C	7668FF0	(00328)	MOVW R5, HCTSHA(R4)
		(00329)	MOVW R5, CSMS*WS*FUS01
		(00330)	ANDR R6, SF
		(00331)	TOPK R6, R5, SFF0
		(00332)	MOVW R6, 0(R5)
			LOAD Y BUFFER ID
			MASK LEFT HALF
			CREATE FULL WORD INDEX
			LOAD YBASE ADDRESS IN R6, R7
			POINT TO LOAD INST.
			MASK LOW FOUR BITS
			OR INTO INST.
			STORE YBASE

SET PENDING SLOTS TO PROPER VALUES

```

(00333) ? SET PENDING SLOTS TO PROPER VALUES
(00334) ?
0470E C6330RER PUSHMTL R3, APTSORG(W2) STORE APS MODULE HUS ORIGIN
(00336)
047AD 90714FFF MOVIR R7, -WS
047AD C6H74FFF PUSHMTL R3, W-WS(R3) STORE APS MODULE START AND SIZE
047AD C6H74FFF PUSHMTL R3, APTSORG+WS(R2) STORE APS MODULE HUS ORIGIN
(00340) **
047AD C6340H4A PUSHMTL R3, APTSORG + 2*WS(R2) ; STORE CSPO SUPPORT ADDR
047AB 2637 INCR R3, 2
047A7 7771 DECR R7, 1
(00343)
047AB C4B74FFC PUSHMTL R3, W-2*WS(R3) STORE APS MODULE SIZE
(00345) ** ; NO SPECIAL SUPPORT
(00346) ** MOVIR R5, R1
(00347) ** INCR W5, HS POINT TO SCALAR A ID
(00348) ** MOVIR R4, R5, MMSRHYT STORE SCALAR A IDENTIFIER
(00349) ** PUSHMTL R3, R4
(00350) INCR R3, 4
047AA 2634 PUSHMTL R3, GSFICS+HS STORE FLAG G1
047AB C33047H1
(00351)
(00352) *
(00353) ** INCR R3, 2
(00354) ** PUSHMTL R3, ZERO SET FOR NO PRF
(00355) * AND EXIT
047AD 80004F63 JMP APSRHDRO ; GO & G1 FLAG CONTROL STORAGE
047AF 0800 EVFN
047AD 0004 GSFICS DATA S4, S25 ; ADDRESS TABLE FOR HU BINDING
047A1 0025
(00360) ?
(00361) HUSTRL DATA CSMS + WS*CSML
(00362) DATA CSMS + WS*CSME
(00363) DATA CSMS + WS*CSML2S
047H4 48D4 DATA CSMS + WS*CSML4S
047H5 48D8 DATA CSMS + WS*CSML5S
047H6 48D6 DATA 0
047H7 0000 DATA 0
(00367) ?
047RR 0000 DATA 0 ; STORAGE FOR POWER OF TWO

```

PAGE 12: LAST FOURTH TRANSFORM ALGORITHM - FF2H, FF2H MAY 7, 1980
 FFT - AP PROGRAMS

(00369) FFT - AP PROGRAMS
 (00370) RM=3
 (00371) OPAND F=1, (1 .LS, 10)+(12 .LS, 5)+X'16'
 (00372) : AP PROGRAMS
 (00373) :
 (00374) : BINDING SECTION FOR FPC
 (00375) :
 (00376) : FVN
 (00377) : DATA CSM/SSA
 (00378) : DATA CM4SSZ
 (00379) : FVN
 (00380) :
 (00381) :

047H9 0000
 047HA 0000
 047HH 0000

```

(00382) ; SCRAMBLE AND FIRST RADIX-2 STAGE, FORWARD
(00383) ; 03/08/78
(00384) ; SCRAMBLE AND FIRST STAGE OF FFT,GI SET
(00385) ;
(00386) ; FUNCTION
(00387) ; LEFTING EIGHT SUCCESSIVE INPUTS BEING DESIGNATED BY
(00388) ; R00,R01,100,102,103,R03,R02
(00389) ; THE EIGHT OUTPUTS ARE PROVIDED
(00390) ; R00+R01,R00-R01,100+101,100-101
(00391) ; 102+103,102-103,R02+R03,R02-R03
(00392) ; FOR RELATIONSHIP OF INPUTS TO U(K), AND OUTPUTS TO Y(K),
(00393) ; SEE SUPPORTING APS PROGRAM, CSM
(00394) ;
(00395) CSM2S BEGIN APH(CSM2)
(00396)   BA=00
(00397) ;
(00398) CSM2SSA JUMP(CSM4F, G1)
(00399)   JUMP(CSM2F)
(00400) ;
(00401) CSM2L MOV(R,00)
(00402) ;
(00403) CSM2F MOV(1QA,A0)
(00404)   MOV(1QA,A1)
(00405)   ADD(A0,A1)\SUR(A0,A1)
(00406) ;
(00407)   MOV(1QA,A3)
(00408)   MOV(1QA,A2)
(00409)   MOV(00),ADD(A2,A3)\MOV(00),SUR(A2,A3)
(00410) ;
(00411)   JUMP(CSM2L,FWI)
(00412) ;
(00413)   MOV(R,00)
(00414) ;
(00415)   NOP
(00416)   JUMP(CR4FS)
(00417) ;
00000000
00000000
A00 0476C 90050000
A01 0478E 10000003
A02 047C0 089C089C
A03 047C2 08F008F0
A04 047C4 08F108F1
A05 047C6 41004900
A06 047C8 08F308F3
A07 047CA 08F208F2
A08 047CC 435C485C
A09 047CE 90160002
A0A 047D0 089C089C
A0B 047D2 00000000
A0C 047D4 1000002H

```

00=1Y0\1Y1
A0=R00
A1=R01
A3=101
A2=100
0Q=RY0\RY1
0Q=1Y0\1Y1

```

(00419) ; SCRAMBLE AND FIRST RADIX-4 STAGE, FORWARD
(00419) ; SCRAMBLE AND FORWARD RADIX 4 STAGE OF FFT.G1 CLEAR
(00420) ;
(00421) ; FUNCTION
(00422) ; THE EIGHT SUCCESSIVE INPUTS ARE PROVIDED TO LOOP
(00423) ; R00,R01,I01,I02,I03,R03,R02
(00424) ; THE INTERMEDIATE RADIX TWO RESULTS ARE CALCULATED
(00425) ; S0=0+0I,S1=0+0I,S2=02+03,S3=02-03
(00426) ; THE OUTPUTS THEN BEING GIVEN BY
(00427) ; Y0=S0+S2,Y1=S1+S3,Y2=S0-S2,Y3=S1+S3
(00428) ; THE ACTUAL OUTPUT SEQUENCE BEING
(00429) ; Y0,Y1,IY0,IY1,IY2,IY3,IY2,IY3
(00430) ;
(00431) ; APU INITIALIZATION
(00432) ;
(00433) ;
(00434) ; CSM4F MOV(10A,A0) A0=R00\R00
(00435) ; MOV(10A,A1) A1=R01\R01
(00436) ; MOV(10A,A3) A3=I01\I01
(00437) ; ADD(A0,A1)\SUR(A0,A1) A2=I00\I00
(00438) ; MOV(10A,A2)
(00439) ; JUMP(CSM4FS)
(00440) ;
(00441) ;
(00442) ; CSM4F, APU INNER LOOP
(00443) ;
(00444) ; MOV(00),ADD(A5,A6)\MOV(00),SUR(A5,A7) Q0=RY0\RY1
(00445) ; MOV(10A,A0) A0=R00
(00446) ; MOV(00),SUR(A5,A6)\MOV(00),ADD(A5,A7) Q0=IY0\IY1
(00447) ; MOV(10A,A1) A1=R01
(00448) ; MOV(00),SUR(A4,A7)\MOV(00),SUR(A4,A6) Q0=IY2\IY3
(00449) ; MOV(10A,A3) A3=I01
(00450) ; MOV(00),ADD(A0,A1)\MOV(00),SUR(A0,A1) Q0=RY2\RY3
(00451) ; MOV(10A,A2) A2=I00
(00452) ;
(00453) ; CSM4FS
(00454) ; MOV(A4),ADD(A2,A3)\MOV(A4),SUR(A2,A3) A4=RS0\RS1
(00455) ; MOV(10A,A0) A0=I02
(00456) ; MOV(10A,A1) A1=I03
(00457) ; MOV(A5),ADD(A0,A1)\MOV(A5),SUR(A0,A1) A5=IS0\IS1
(00458) ; MOV(10A,A3) A3=R03
(00459) ;
(00460) ;
(00461) ;

```

A20 047FC 08F208F2 (00462)	MOV(10A,A2)	A2=RU2
A21 047F4 43563856 (00463)	MOV(A6),ADD(A2,A3)\MOV(A6),SUB(A2,A3)	A6=TS2\IS3
A22 04800 47974697 (00466)	MOV(A7),ADD(A4,A7)\MOV(A7),ADD(A4,A6)	A7=RS2\RS3
A23 04802 90160013 (00469)	JUMPC(01,FW1)	
A24 04804 468C4F8C (00470)	MOV(00),ADD(A5,A6)\MOV(00),SUB(A5,A7)	00=RY0\RY1
A25 04806 4F8C478C (00471)	MOV(00),SUB(A5,A6)\MOV(00),ADD(A5,A7)	00=IY0\IY1
A26 04808 4F9C4F9C (00472)	MOV(00),SUB(A4,A7)\MOV(00),SUB(A4,A6)	00=IY2\IY3
A27 0480A 089C089C (00473)	MOV(P,00)	00=RY2\RY3

```

(00474) ;      SUCCESSIVE RADIX-4 STAGES, FORWARD
(00475) ;
(00476) ;USES APS PROGRAM CP3A
(00477) ;
(00478) ;MATHEMATICS
(00479) ;
(00480) ; W=A+H*CF2+DEF3=P+Q
(00481) ; X=A-H*CF2+DEF3=R-S
(00482) ; Y=A-H*CF2-DEF3=P-Q
(00483) ; Z=A+H*CF2-DEF3=R+S
(00484) ;
(00485) ; P=A*CF2,P=A-CV2
(00486) ; Q=E(H+H*2),S=JF(H-H*2)
(00487) ;
(00488) ; PR=AR+WCOS2X+CSIN2X
(00489) ; PI=AI+CI COS2X-CRSIN2X
(00490) ; WR=AR-RCOS2X-CISIN2X
(00491) ; RI=AI-CI COS2X+CRSIN2X
(00492) ; OR=RCOSX+RISINX+DR COS3X+DISIN3X
(00493) ; OI=RI COSX-RRSINX+DI COS3X-DRSIN3X
(00494) ; SR=RSINX-RI COSX+DI COS3X-DRSIN3X
(00495) ; SI=RCOSX+RISINX-DR COS3X-DISIN3X
(00496) ;
(00497) ; SINX STORED IN M1M5
(00498) ; SIN2X STORED IN M2M6
(00499) ; SIN3X STORED IN M7M3
(00500) ; COSX STORED IN M5M1
(00501) ; COS2X STORED IN M2M6
(00502) ; COS3X STORED IN M3M7
(00503) ;
(00504) ;EJECT

```

SUCCESSIVE RADIX-4 STAGES, FORWARD

```

(005051) ?CR4F=PUTLINE STARTUP
(005061)
(005071)
A28 0480C 20202028 CR4FS CLEAR(A11)
A29 0480E 202A202A CR4FS CLEAR(A12)
A2A 04810 20292029 CR4FS CLEAR(A11)
A2B 04812 20282028 CR4FS CLEAR(A10)
A2C 04814 16801680 PUT
A2D 04816 0809080F MOV(ZERO,M1)\MOV(10A,A0)
A2E 04818 08F0808F MOV(10A,A0)\MOV(ZERO,M5)
A2F 0481A 08400840 MOV(R,M5)\MOV(R,M1)
A30 0481C 088A088F MOV(R,M2)\MOV(R,M6)
A31 0481E 08400840 MOV(R,M3)\MOV(R,M7)
A32 04820 08F1080A MOV(10A,A1)\MOV(ZERO,M2)
A33 04822 080F081F MOV(ZERO,M6)\MOV(10A,A1)
A34 04824 080F080F MOV(ZERO,M7)\MOV(ZERO,M3)
A35 04826 08F808F8 MOV(10A,M0)
A36 04828 84308430 MUL(M0,M6)
A37 0482A 0A200220 NEG(A1)\R(A1)
A38 0482C 10000054 JUMP(CR4FE)
(005311)
(005321) ?CR4F=COSINE ENTRY
(005331) ?
A39 0482E 08F008F0 MOV(10A,M5)\MOV(10A,M1)
A3A 04830 08F908F9 MOV(10A,M1)\MOV(10A,M5)
A3B 04832 08A008A0 MOV(P,NULL)
A3C 04834 08FF08FF MOV(10A,M6)\MOV(10A,M2)
A3D 04836 08FA08FA MOV(10A,M2)\MOV(10A,M6)
A3E 04838 08F808F8 MOV(10A,M3)\MOV(10A,M7)
A3F 0483A 08FF08FF MOV(10A,M7)\MOV(10A,M3)
(005411) ?
(005421) ?CR4F=BUTTERFLY
(005431)
A40 0483C 08F808F8 MOV(10A,M0)
A41 0483E 847A847A MUL(A4)\MUL(M0,M7)
A42 04840 8A958A95 MOV(A5)\SHR(A6,A2)\MOV(A5),ADD(A2,A4)
A43 04842 08F080F0 MOV(10A,M4)
A44 04844 8A728A72 MOV(A2)\SHR(A3,A2)
A0=01\DR
SINX=0=M1\M5
COSX=1=M5\M1
COS2X=1=M2\M6
COS3X=1=M3\M7
A1=R1\RR
SIN2X=0=M6\M2
SIN3X=0=M7\M3
M0=CR
P=CRSIN2X\CHCOS2X
R=-R1\RR
COSX TO M5\M1
SINX TO M1\M5
SIN2X TO M6\M2
COS2X TO M2\M6
COS3X TO M3\M7
SIN3X TO M7\M3
DR TO M0
A4=CTCOS2X\CTISIN2X
A5=01\DR
D1 TO M4
A2=CTCOS2X-CRSIN2X

```

SUCCESSIVE RADIX-4 STAGES, FORWARD

```

(00589) ;
(00590)
(00591)
VCISIN2X+CHCOS2X

A45 04836 8590A590
A46 04834 42744274
A47 04844 08560A56
A48 04840 08560A56
A49 04844 47044704
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FAST FOURIER TRANSFORM ALGORITHM - FFTH, FFTHR

FAST FOURIER TRANSFORM ALGORITHM - FFTH, FFTHR

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```

M0=A
A0=RW(N/2)
A1=RW(N/2)
A2=RW(0)
A3=RW(0)
M4=2RW(0)\21W(0)
A4=RW(N-K)
A5=RW(K)
A6=RW(N-K)
A7=RW(K)
M5=RW(K)-RW(N-K)
RW(N-K)-RW(K)
NQ=RX(K)\RZ(K)
M4=RW(K)+RW(N-K)
RW(K)+RW(N-K)
NQ=IX(K)\IZ(K)

```

```

MOV(10A, M0)
MOV(10A, A0)
MOV(10A, A1)
MOV(10A, A2)
MOV(10A, A3)
ADD(A2, A3)
MOV(M4), ADD(A0, A0)\MOV(M4), ADD(A1, A1)
MUL(M0, M4)
MUL(M0, M4)
MOV(10A, A4)
MOV(10A, A5)
MOV(10A, A6)
MOV(10A, A7)
MOV(M5), ADD(A5, A6)\MOV(M5), ADD(A4, A7)
MOV(M0), MUL(M0, M5)
MOV(M4), SUB(A7, A4)\MOV(M4), SUB(A6, A5)
MOV(M0), MUL(M0, M4)
JUMP(C1, END)
CLEAN(RA)
NOP
JUMP(CSM2SSA)

```

```

CP4SSZ = RA-CSM2SSA
END

```

PAGE 21: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2H, FF2MH MAY 7, 1980
FF2K-000 SEPARATE

(00656)
-(00657)
(00658)
(00659) ;

```

APS PROGRAMS
(00660) ; APS PROGRAMS
(00661) ;
(00662) ;
(00663) ; START OF HEADER BLOCK
(00664) ;
(00665) ;
(00666) ;
(00667) ;
(00668) ;
(00669) ;
(00670) ;
(00671) ;
(00672) ;
(00673) ;

04H0C 000040C4
04H0E 000040C4
04H06 0000
04H01 0100
04H02 00000000

EVEN
ADDR CSM51
ADDR CSM5+2*CSM55
DATA 0
DATA CR4ASZ
ADDR CR4ASA
EVEN

START ON WORD BOUNDARY
PTR TO CONSTR INSTR BLOCK(NONE)
PTR TO SCALAR BLOCK (NONE)
NO SCALARS
MODULE SIZE
PTR TO CHAIN ANCHOR
END OF BOUNDARY

```

```

CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)

(00674) ? CSM - COMPLEX FFT SCRAMBLE (PO - INPUT)
(00675) ? APS PROGRAM PROVIDING SCRAMBLE FOR COMPLEX FFT
(00676) ? MAY BE USED WITH THE MAD-300 API PROGRAMS
(00677) ? CSM2(Y,U), CSM4F(Y,U), CSM4I(Y,U)
(00678) ?
(00679) ? RESTRICTIONS
(00680) ? IN PLACE OPERATION NOT PERMITTED
(00681) ? BUFFER SIZES MUST BE 1024 OR LESS
(00682) ? Y BUFFER MUST BE COMPACT 32 BIT FLOATING
(00683) ?
(00684) ?
(00685) ? BINDING PARAMETERS
(00686) ?
(00687) ? N# OF POINTS IN FFT (USIZE)
(00688) ? MU=USEP*N/2
(00689) ? QU=QU/2
(00690) ? APS-INPUT ADDRESS SEQUENCE
(00691) ? RU(K), RU(K+N/2), IU(K+N/2), IU(K), IU(K+N/4)
(00692) ? IU(K+N/4), RU(K+N/4), RU(K+N/4), RU(K+N/4), FOR 0<K<N/4
(00693) ? THUS PROVIDING REVERSAL OF TWO BITS
(00694) ?
(00695) ? APS-OUTPUT ADDRESS SEQUENCE
(00696) ? RW(J), RW(J+1), IW(J), IW(J+1), RW(J+2),
(00697) ? RW(J+3), IW(J+2), IW(J+3)
(00698) ? WHERE J= HIT REVERSAL OF K
(00699) ? THE DIFFERENCE, D(K) = J(K+1)-J(K) IS
(00700) ? PROVIDED BY THE P3 SUBROUTINE
(00701) ?
(00702) ?
(00703) CSM$ BEGIN APS(CSM)
(00704) ?
(00705) CSM$S JSN(CSMO,P2)
(00706) ?
(00707) CSM$AS LOAD(HR0,MSS,L,TF)
(00708) CSM$AS LOAD(HR1,MSS)
(00709) ? JUMP(CSMF,RA),SET
(00710) ?
(00711) CSM$ SUB(HR0,MSS,TF)
(00712) ?
(00713) CSM$IS SUB(HR0,MSS,TF)
(00714) CSM$F ADD(HR0,MSS,TF)
(00715) ? ADD(HR0,2,TF)
(00716) CSM$DS SUB(HR0,MSS,TF)
(00717) ?

INPT RU(0) (URASE,ROUND)
(USIZE-1,ROUND)
TURN ON API

INPT RU(K+N/4) (HU,ROUND)

INPT RU(K) (OU=USEP,ROUND)
INPT RU(K+N/2) (HU,ROUND)
INPT IU(K+N/2)
INPT IU(K) (HU,ROUND)

```

PAGE 24: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980
 (SM - COMPLEX FFT SCRAMBLE (PO - INPUT)

A09 04ND6 12M0000 (00718) CSML35	ADD(MR0,KCS,TF)	INPT 10(K+N/4) [OH ROUND]
A0A 04ND6 14M0000 (00719) CSML45	ADD(MR0,MSS,TF)	INPT 10(K+3N/4) [HU ROUND]
A0B 04ND6 16M20002 (00720)	SUB(MR0,2,TF)	INPT R0(K+3N/4)
A0C 04ND6 181904W4 (00721) ;		
A0D 04ND6 181904W4 (00722)	SUBL(MR1,4),JUMPP(CSMU)	
A0E 04ND6 181904W4 (00723) ;		
A0F 04ND6 181904W4 (00724) CSML55	SUB(MR0,MSS,TF)	INPT R0(LAST) [HU ROUND]
A0G 04ND6 1C104F77 (00725)	JUMP(CP4AF,M1),SPT	
A0H 04ND6 1C104F77 (00726) ;		

```

(00721) ? CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)
(00722) ?
(00723) ? DATA POINT ADDRESS AT ENTRY 4*(J(K)-N)+PHASE
(00724) ? DATA OUTPUT ADDRESS GENERATED (4*(K+1))+PHASE
(00725) ?
(00726) ? ITERATION LOCATION, J=J(K)-N, HIT REVERSED
(00727) ? 4*(K+1)=4*(J(K)-N)+DM
(00728) ? WHERE M=NUMBER OF TRAILING 1'S IN (K)
(00729) ?
(00730) ? BINDING CONSTANTS
(00731) ?
(00732) ? D0=4*(4*N/2)
(00733) ? D1=00/2
(00734) ? D2=01/2
(00735) ? D3=02/2
(00736) ? D4=03/2
(00737) ? D5=04/2
(00738) ? D6=05/2
(00739) ? D7=06/2
(00740) ?
(00741) ?
(00742) ?
(00743) ?
(00744) ?
(00745) ?
(00746) ?
(00747) ?
(00748) ? JSN(ANGLE,P1)
(00749) ? SCRM0 ADD(MW0,MSS,TF,C)
(00750) ? SCRM1 ADD(MW0,MSS,TF,C)
(00751) ? SCRM2 ADD(MW0,MSS,TF,C)
(00752) ? SCRM3 ADD(MW0,MSS,TF,C)
(00753) ? SCRM4 ADD(MW0,MSS,TF,C)
(00754) ? SCRM5 ADD(MW0,MSS,TF,C)
(00755) ? SCRM6 ADD(MW0,MSS,TF,C)
(00756) ? SCRM7 JUMPS(SCRM4,AF0),CLEAR
(00757) ? ADD(MW0,MSS,TF,C)
(00758) ? JUMP(SCRM0,AF0),SFT
(00759) ?
(00760) ? SCRM4 JUMPS(SCRM5,AF1),CLEAR
(00761) ? SCRM5 ADD(MW0,MSS,TF,C)
(00762) ? SCRM6 JUMP(SCRM0,AF1),SFT
(00763) ?
(00764) ? SCRM5 JUMPS(SCRM6,AF2),CLEAR
(00765) ? SCRM6 ADD(MW0,MSS,TF,C)
(00766) ? SCRM7 JUMP(SCRM0,AF2),SFT
(00767) ?
(00768) ? SCRM5 JUMPS(SCRM6,AF2),CLEAR
(00769) ? SCRM6 ADD(MW0,MSS,TF,C)
(00770) ? SCRM7 JUMP(SCRM0,AF2),SFT
(00771) ?

```

LOADS P1 FOR CR4A

[D0 ROUND]

[D1 ROUND]

[D2 ROUND]

[D3 ROUND]

[D4 ROUND]

[D5 ROUND]

[D3 ROUND]

[D4 ROUND]

[D5 ROUND]

PAGE 26: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FFT, FFTH, FFTH MAY 7, 1980
CSM-SCRAMBLE SUBROUTINE (P3 - OUTPUT)

A20 04904 402021H (00771) SCRAM	JUMP(SCRAM),AF3,CLEAR	
A21 04906 4240000 (00772) SCRAM10\$	ADD(HW0,MSS,TF,C)	(D6 ROUND)
A22 04908 443010H (00773)	JUMP(SCRAM,AF3),SET	
A23 0490A 4640000 (00774) ?		
A24 0490C 480010A (00775) SCRAM7	ADD(HW0,MSS,TF,C)	(D7 ROUND)
	JUMP(SCRAM)	
	(00777) ?	


```

(00778) ; CSM-SCRAMBLE SUBROUTINE (OUTPUT = P2)
(00779) ;
(00780) ;
A25 04906 4A306A40 (00781) CSM01 JSN(CSM0,P3)
A26 04910 4C000000 (00782) CSM01S LOAD(RW0,MSS,I,TF)
A27 04912 4F112953 (00783) SUBR(RW1,RP1),JUMP(CSM0E)
(00784) ;
A28 04914 50060000 (00785) CSM01L SUB(RW0,MSS,C)
(00786) ;
(00787) ; PRIOR TO JUMP TO P3,RW0=4J(K)-4N
(00788) ; SCRAMBLE SUBROUTINE OUTPUTS RW(J)
(00789) ; LEAV19C RW0=4J(K+1)
(00790) ;
A29 04916 520A0004 (00791) CSM01F ADD(RW0,4,TF)
A2A 04918 54020002 (00792) SUB(RW0,2,TF)
A2B 0491A 560A0004 (00793) ADD(RW0,4,TF)
(00794) ;
A2C 0491C 580A0004 (00795) ADD(RW0,4,TF)
A2D 0491F 5A0A0004 (00796) ADD(RW0,4,TF)
A2E 04920 5C020006 (00797) SUB(RW0,6,TF)
A2F 04922 5E0A0004 (00798) ADD(RW0,4,TF)
(00799) ;
A30 04924 60112884 (00800) SUB1(RW1,4),JUMPP(CSM0E)
(00801) ;
OTPT RW(0) (PHASE ROUND)
RW1=MSSIZE-1
[4N+12 ROUND]
OTPT RW(J+1)
OTPT LW(J)
OTPT LW(J+1)
OTPT LW(J+2)
OTPT LW(J+3)
OTPT RW(J+2)
OTPT RW(J+3)

```

```

(00802) ; SUCCESSIVE RADIX-4 STAGES (OUTPUT - P2)
(00803) ; PROGRAM ASSUMES SCRAMBLE COMPLETED
(00804) ;
(00805) ; 12/14/77
(00806) ; SCRAMBLE LEAVES APS AS FOLLOWS
(00807) ; INPUT - LAST COMMAND, JUMP(CR4AP, W1), SET
(00808) ; INPUT-P1 LOCATED AT ANGLE
(00809) ; OUTPUT - ACTIVE, IN P2
(00810) ;
(00811) ;
(00812) ; SS=STAGE SEPARATION, DATA SEPARATION A H C D
(00813) ; STARTING VALUES(4)ARE
(00814) ; SS1=1, SCRAMBLE ONLY PRECEDING N=4*M
(00815) ; SS1=2, SCRAMBLE PLUS RADIX2 PRECEDING N=2*4*M
(00816) ; SS1=3, SCRAMBLE PLUS RADIX3 PRECEDING N=3*4*M
(00817) ; SS1=4, SCRAMBLE PLUS RADIX4 PRECEDING N=4*4*M
(00818) ; SS1=6, SCRAMBLE PLUS RADIX6 PRECEDING N=6*4*M
(00819) ;
(00820) ;
(00821) ; COSINE TABLE IS A REAL BUFFER WITH CONTENTS:
(00822) ; CT(K)=COS(2*PI*K/CSIZE)
(00823) ; RESTRICTION.
(00824) ; CSIZE MUST BE MULTIPLE OF N, FFT SIZE
(00825) ;
(00826) ; COSINE TABLE BINDING PARAMETERS
(00827) ;
(00828) ; HPI=CSIZE*(CSIZE/4)
(00829) ; 90 DEG SEPARATION
(00830) ; REGISTER USAGE
(00831) ; R0/RW0
(00832) ; R1/RW1
(00833) ; R2
(00834) ; R3
(00835) ; R4
(00836) ; R5
(00837) ;

```

DATA ADDRESSES
OF USES OF A COSINE, COUNTER
OF COSINES USED, COUNTER
STAGE SEPARATION
SEP BETWEEN COSINES WITHIN A SET
SEP BETWEEN SETS OF COSINES

PICT

```

A31 04926 62700000 (00840) CR4A01 LOAD(RW1,MSS) (FFT1 ROUND)
      (00840) ;
      (00841) ; CR4A-ALS OUTPUT PROGRAM-STAGE INITIALIZATION
      (00842) ;
      (00843) ;
      (00844) CR4A02 ADDH(RW1,RW1)
      (00845) ADDH(RW1,RW1)
      (00846) CR4A03 LOAD(RW0,MSS)
      (00847) MOVH(RW2,RW1)
      (00848) MOVH(RW0,RW0)
      (00849) CLEAR(RW1)
      (00850)
      (00851) CR4A03 LOAD(RW1,MSS)
      (00852) CR4A03 ADDH(RW0,MSS)
      (00853)
      (00854)
      (00855) CR4A04 SUBH(RW0,RW1,TF)
      (00856) ADDH(RW0,2,TF)
      (00857) SUBH(RW0,RW1,TF)
      (00858) SUBH(RW0,2,TF)
      (00859) SUBH(RW0,RW1,TF)
      (00860) ADDH(RW0,2,TF)
      (00861) SUBH(RW0,RW1,TF)
      (00862) SUBH(RW0,2,TF)
      (00863) ;TEST FOR NEW COSINES, ELSE OUTPUT NEXT 4 POINTS
      (00864) ;
      (00865) SUBH(RW1,RW1),JUMPP(CR4A04)
      (00866) ;TEST STAGE DONE, ELSE GET NEW COSINES
      (00867) ;
      (00868) JUMPP(CR4A03,AF2),CLEAR
      (00869) ;
      (00870) ;STOPPING SHORT CHECK IS HERE
      (00871) ;
      (00872) CR4A03 LOAD(RW1, MSS)
      (00873) SUBH(RW1, RW1), JUMPP(CR4A02)

```

SET SEPARATION FOR STAGE
SET RW0=RW-4 (WHAKE ROUND)
SET UP INPUT BASE
START UP INPUT
SET BUTTERFLY COUNT (USIZE-1 ROUND)
SET STARTING ADDRESS (4N+4 ROUND)

ZR
ZI
YI
YH
XH
XI
WI
WP

```

(00073) ; GENERATE OUTPUT ADDRESSES FOR EVEN-ODD SEPARATE
(00075) ;
(00076) ;
(00077) ; W BUFFER CONTAINS FFT OF X(0)
(00078) ; TWO REAL FFT'S ARE EXTRACTED AS FOLLOWS
(00079) ;  $X(K) = A + iW(K) + W'(N-K)$ 
(00080) ;  $Y(K) = A + iW(K) - W'(N-K)$ 
(00081) ; WHERE,  $W = \text{CONJUGATE OF } w$ 
(00082) ;
(00083) ;  $FX(0) = PW(0)$ 
(00084) ;  $IX(0) = W(N/2)$ 
(00085) ;  $RZ(0) = IW(0)$ 
(00086) ;  $IZ(0) = IW(N/2)$ 
(00087) ;
(00088) ;  $X(K)$  IS STORED IN  $Y(K)$ 
(00089) ;  $Z(K)$  IS STORED IN  $Y(K+N/2)$ 
(00090) ;
(00091) ;  $RX(K) = PW(K) + PW(N-K)$ ,  $IX(K) = IW(K) - IW(N-K)$ 
(00092) ;  $RZ(K) = W(K) - W(N-K)$ ,  $IZ(K) = IW(K) + IW(N-K)$ 
(00093) ;
(00094) ; THE OUTPUT ADDRESS SEQUENCE IS:
(00095) ;  $RX(0)$ ,  $RZ(0)$ ,  $IX(0)$ ,  $IZ(0)$ ,
(00096) ;  $RX(K)$ ,  $RZ(K)$ ,  $IX(K)$ ,  $IZ(K)$ 
(00097) ;
(00098) ;
(00099) ;  $FDS01$  LOAD(RW0, MSS, TF)
(00100) ;  $FDS02$  MOVH(RW1, RW0)
(00101) ;  $FDS03$  ADD(RW1, MSS, TF)
(00102) ;  $FDS04$  LOAD(RW2, MSS)
(00103) ;  $FDS05$  ADD(RW0, 2, TF)
(00104) ;  $FDS06$  ADD(RW1, 2, TF)
(00105) ;  $FDS07$  SUBL(RW2, 1), JUMPP(FDS04)
(00106) ;  $FDS08$  JUMP(CN4A1S, RW), CLEAR
(00107) ;
(00108) ;

```

<RX(0) [PHASE ROUND]
 <RZ(0) [2N ROUND]
 [USIZE-2 ROUND]
 <X(K)
 <Z(K)
 HALT APS OUTPUT

```

(00009) ;      SUCCESSIVE RADIX-4 STAGES (INPUT - P0)
(00010)
A48 04960 9C300037 (00011) C4A41 SET(WI)
(00012) ; STAGE INITIALIZATION
(00013) ;
A49 04962 9F600000 (00014) C4A44 LOAD(RH2,0)      SET COSINE VALUE=0
A50 04964 A0300015 (00015) W0VB(RH3,RH2,C)
(00016) ;
(00017) ; ANGLE SUBROUTINE, P1, INSERTS COSINE SEPARATION
(00018) ; FOR STAGE INTO RH4
(00019)
(00020)
A51 04966 A2215071 (00021) S0RL(RH2,1),JUMP(C4A43)  RH2=80F COSINES-1
(00022)
A52 04968 A4M90032 (00023) C4A42 S0RL(RH0,2,TF)  AH
(00024) ; COSINE ENTRY
(00025) ;
A53 0496A A6500000 (00026) C4A45 LOAD(RH1,MSS,L)  [CHASE ROUND]
A54 0496C A829002F (00027) C4A46 ADDR(RH2,RH1)  RH2=NEXT ANGLE
(00028)
A55 0496E AAY90020 (00029) ADDR(RH1,RH2,TF)  COSX
A56 04970 ACY90000 (00030) ADD(RH1,MSS,TF)  SINX [HPT RC MD]
A57 04972 AF990020 (00031) ADDR(RH1,RH2,TF)  SIN2X
A58 04974 H0200000 (00032) C4A46S S0RL(RH1,MSS,TF)  COS2X [HPT ROUND]
A59 04976 H2990020 (00033) ADDR(RH1,RH2,TF)  COS3X
A5A 04978 H49A0000 (00034) C4A47S ADD(RH1,MSS,TF)  SIN3X [HPT ROUND]
A5B 0497A H60A0000 (00035) C4A43 ADD(RH0,MSS)  SET RH0 TO 4N+COS# [4N+4 ROUND]
A5C 0497C H8500000 (00036) C4A48S LOAD(RH1,MSS)  SET COSINE USE COUNTER [USIZE-1 ROUND]
(00037)
(00038) ; C4A4A-AFS BUTTERFLY INPUT
(00039) ;
(00040)
A5D 0497F HAH90026 (00041) C4A44 S0RL(RH0,RH3,TF)  DR
A5E 04980 HC99003A (00042) ADDL(RH0,2,TF)  DI
A5F 04982 HF990026 (00043) S0RL(RH0,RH3,TF)  AI
A60 04984 H0990032 (00044) S0RL(RH0,2,TF)  HR
A61 04986 C2990026 (00045) S0RL(RH0,RH3,TF)  CR
A62 04988 C499003A (00046) ADDL(RH0,2,TF)  CI
A63 0498A C6990026 (00047) S0RL(RH0,RH3,TF)  AI
(00048)
(00049) ; TEST IF NEW COSINE NEEDED, ELSE INPUT NEXT 4 POINTS
(00050) ;
A64 0498C C81976A6 (00051) S0RL(RH1,RH3),JUMP(C4A45)
A65 0498E CA300028 (00052) SET(AE0)

```

TELL APU COSINES COMING

PAGE 32: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RB MAY 7, 1980
 SUCCESSIVE RADIX-4 STAGES (INPUT - P0)

```

(00954) ; TEST IF STAGE DONE, ELSE GET NEW CUSINES
(00954)
A06 04000 00215204      SUBL(0W2,4),JUMPP(CR4A2)
A07 04002 0E400020      SET(AP1)
(00955)
(00956) ;***** CHANGE VALUE HERE TO STOP SHORT *****
(00957) ;*****
(00958) ;TO STOP SHORT 1 STAGE, KIND UNIZE/4-1 HERE
(00959) CR4A9S  LOAD(HR1,MSS)
A08 04004 00500000      SUBL(HR0,2,TF)
A09 04006 02800032      (00961)
(00962) ; TEST IF FFT DONE, ELSE START NEXT STAGE
(00963)
(00964)      SUBR(HR1,RW3),JUMPP(CR4A1)
A0A 04008 03104FA6      GO BACK
(00965) ;

```

```

(00966) : GENERATE INPUT ADDRESSES FOR EVEN-ODD SEPARATE
(00967) :
(00968) :
(00969) : INPUT SEQUENCE:
(00970) : SCALAR A,
(00971) : PW(N/2), LW(N/2), PW(0), LW(0),
(00972) : LW(N-K), PW(K), LW(N-K), LW(K)
(00973) :
(00974) :
(00975) : SET(AE)
(00976) : LOAD(PW, MSS(1), TF)
(00977) : LOAD(LW, MSS)
(00978) : MOVW(HR1, HR0)
(00979) : ADD(PW, MSS, TF)
(00980) : ADD(HR0, 2, TF)
(00981) : MOVW(HR0, HR1, TF)
(00982) : ADD(HR1, MSS)
(00983) : ADD(HR0, 2, TF)
(00984) : SUB(HR1, 2, TF)
(00985) : JUMP(FUS5)
(00986) :
(00987) : CR45
(00988) : SHL(HR0, 2, TF)
(00989) : JUMP(CR4A4)
  
```

SIGNAL, APH TO DO FDS
 <SA ISCALE A ROUND
 RM(0) [WRASE, ROUND]

 <RW(N/2) [2N ROUND]
 <LW(N/2)
 <RW(0)
 RW(N) [4N ROUND]
 <WK
 <W(N-K)

(00990) ; ANGULAR SEPARATION SUBROUTINE (INPUT = P1)

(00991) ; STANG=PI/SSI

(00992) ; S2ANG=STANG/4

(00993) ; S3ANG=S2ANG/4

(00994) ; S4ANG=S3ANG/4

(00995) ; S5ANG=S4ANG/4

(00996) ; S6ANG=S5ANG/4

(00997) ; S7ANG=S6ANG/4

(00998) ; S8ANG=S7ANG/4

(00999) ; S9ANG=S8ANG/4

(00999) ; S10ANG=S9ANG/4

(00999) ; S11ANG=S10ANG/4

(00999) ; S12ANG=S11ANG/4

(00999) ; S13ANG=S12ANG/4

(00999) ; S14ANG=S13ANG/4

(00999) ; S15ANG=S14ANG/4

(00999) ; S16ANG=S15ANG/4

(00999) ; S17ANG=S16ANG/4

(00999) ; S18ANG=S17ANG/4

(00999) ; S19ANG=S18ANG/4

(00999) ; S20ANG=S19ANG/4

(00999) ; S21ANG=S20ANG/4

(00999) ; S22ANG=S21ANG/4

(00999) ; S23ANG=S22ANG/4

(00999) ; S24ANG=S23ANG/4

(00999) ; S25ANG=S24ANG/4

(00999) ; S26ANG=S25ANG/4

(00999) ; S27ANG=S26ANG/4

(00999) ; S28ANG=S27ANG/4

(00999) ; S29ANG=S28ANG/4

(00999) ; S30ANG=S29ANG/4

(00999) ; S31ANG=S30ANG/4

(00999) ; S32ANG=S31ANG/4

(00999) ; S33ANG=S32ANG/4

(00999) ; S34ANG=S33ANG/4

(00999) ; S35ANG=S34ANG/4

(00999) ; S36ANG=S35ANG/4

(00999) ; S37ANG=S36ANG/4

(00999) ; S38ANG=S37ANG/4

(00999) ; S39ANG=S38ANG/4

(00999) ; S40ANG=S39ANG/4

(00999) ; S41ANG=S40ANG/4

(00999) ; S42ANG=S41ANG/4

(00999) ; S43ANG=S42ANG/4

(00999) ; S44ANG=S43ANG/4

(00999) ; S45ANG=S44ANG/4

(00999) ; S46ANG=S45ANG/4

(00999) ; S47ANG=S46ANG/4

(00999) ; S48ANG=S47ANG/4

(00999) ; S49ANG=S48ANG/4

(00999) ; S50ANG=S49ANG/4

(00999) ; S51ANG=S50ANG/4

(00999) ; S52ANG=S51ANG/4

(00999) ; S53ANG=S52ANG/4

(00999) ; S54ANG=S53ANG/4

(00999) ; S55ANG=S54ANG/4

(00999) ; S56ANG=S55ANG/4

(00999) ; S57ANG=S56ANG/4

(00999) ; S58ANG=S57ANG/4

(00999) ; S59ANG=S58ANG/4

(00999) ; S60ANG=S59ANG/4

(00999) ; S61ANG=S60ANG/4

(00999) ; S62ANG=S61ANG/4

(00999) ; S63ANG=S62ANG/4

(00999) ; S64ANG=S63ANG/4

(STANG ROUND)
 (SZANG ROUND)
 (S3ANG ROUND)
 (S4ANG ROUND)
 (S5ANG ROUND)
 (S6ANG ROUND)
 (S7ANG ROUND)
 (S8ANG ROUND)

ASSIGN VALUE TO CHAIN
 #A-1 END OF MODULE

CHANGA=4C

END

049C4

000049C4 (01013) CSMS1 #1=#1*2*0

00000100 (01015) CHANGA7=#1-CSMS

(01016) ;

(01017) * OFFINE TOP OF MODULE

(01018) *

000049C4 (01019) TOPSCUR = #1.

(01020) *

PAGE 35: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980
SYMMOL, TABLE

049C4 (01021) SYMMOL, TABLE
(01022) END

AFD1S:	0000H (00046) (00047) (00082) (00087)
AFDSURG:	0000H (00047) (00335) (00339)
ANG1S:	00079 (00758) (01001)
ANG2S:	0007A (00261) (01003)
ANG3S:	0007H (00263) (01005)
ANG4S:	0007C (00267) (01004)
ANG5S:	0007D (00270) (01005)
ANG6S:	0007E (00273) (01006)
ANG7S:	0007F (00276) (01007)
ANG8S:	0007H (00748) (01000)
APSHNR0:	00F63 (00064) (00356)
APSH1:	0024D (00061) (00243)
HCFSAD:	00604 (00054) (00112) (00224)
ACTSAT:	00606 (00055) (00107)
HCFSHA:	00582 (00053) (00219) (00290) (00316) (00328)
CR4S7:	0008D (00378) (00650)
CR4A2:	00000 (00670) (01010)
CR4AS7:	00100 (00669) (01015)
CR4A1:	0004F (00911) (00964)
CR4A1S:	00034 (00300) (00846) (00906)
CR4A13S:	00040 (00117) (00872)
CR4A2:	00052 (00923) (00455)
CR4A2S:	00039 (00200) (00852)
CR4A3:	0005H (00201) (00921) (00935)
CR4A4:	0005D (00941) (00988)
CR4A4S:	00053 (00220) (00926)
CR4A5:	00076 (00951) (00987)
CR4A5S:	00056 (00232) (00930)
CR4A6S:	0005H (00233) (00932)
CR4A7S:	0005A (00244) (00934)
CR4A8S:	0005C (00115) (00936)
CR4A9S:	0006H (00116) (00959)
CR4A9:	0004F (00725) (00914)
CR4A01:	00031 (00246) (00250) (00439)
CR4A02:	00032 (00844) (00873)
CR4A03:	0003H (00113) (00851) (0086H)
CR4A04:	0003A (00855) (00865)
CR4F1:	00040 (00544) (00583)
CR4F2:	00039 (00534) (00584)
CR4F3:	00054 (00530) (00575)
CR4F4:	0002H (00416) (00508)
CR4F5:	0002A (00510) (00609)
CR4F5A:	04RC4 (00084) (00089) (00113) (00114) (00115) (00116) (00117) (00120) (00148) (00151)
CSMS:	(00158) (00159) (00160) (00161) (00164) (00165) (00171) (00174) (00177)

CSW51:	030C3 (00666) (01013)	(00180) (00143) (00190) (00191) (00194) (00200) (00201) (00204) (00211) (00220)
CSW55:	00000 (00667) (00705)	(00232) (00233) (00234) (00246) (00250) (00258) (00261) (00264) (00267) (00270)
CSW56:	047AC (00083) (00088)	(00273) (00276) (00291) (00296) (00300) (00317) (00329) (00361) (00362) (00363)
CSW255A:	00000 (00377) (00398)	(00364) (00365) (00667) (00703) (01015)
CSW2F:	00003 (00392) (00403)	
CSW2L:	00002 (00401) (00411)	
CSW4F:	00000 (00398) (00434)	
CSW4F5:	00014 (00439) (00456)	
CSW4F:	00006 (00362) (00709)	(00714)
CSW1:	00004 (00361) (00711)	(00722)
CSW105:	00002 (00114) (00708)	
CSW115:	00005 (00151) (00713)	
CSW125:	00004 (00363) (00716)	
CSW135:	00009 (00148) (00718)	
CSW145:	0000A (00364) (00719)	
CSW155:	00000 (00365) (00724)	
CSW165:	00001 (00317) (00707)	
CSW0:	00025 (00705) (00781)	
CSW015:	00026 (00291) (00782)	
CSW0F:	00029 (00783) (00791)	
CSW01:	00028 (00204) (00785)	(00800)
F0S1:	0006C (00211) (00976)	
F0S2:	0006D (00296) (00977)	
F0S3:	0006F (00190) (00979)	
F0S4:	00072 (00194) (00982)	
F0S5:	00073 (00983) (00985)	
F0S01:	00046 (00329) (00989)	
F0S02:	00048 (00191) (00901)	
F0S03:	00049 (00120) (00902)	
F0S04:	0004A (00903) (00905)	
FF2R5:	00006 (00079) (00082)	
FF2RHS:	00017 (00080) (00087)	
FFTSST:	04690 (00085) (00103)	
FLGSC0:	00004 (00058)	
FLGSC1:	00005 (00059) (00247)	(00251)
FLGSSFT:	00020 (00057) (00251)	
GSFLGS:	04740 (00247) (00251)	(00351) (00359)
HS:	00001 (00051) (00052)	(00103) (00107) (00113) (00114) (00115) (00116) (00117) (00120)
	(00148) (00151) (00158)	(00159) (00160) (00161) (00164) (00165) (00168) (00171)
	(00174) (00177) (00180)	(00183) (00190) (00191) (00194) (00200) (00201) (00204)

PAGE 19: FAST FOURIER TRANSFORM TRANSFORM ALGORITHM - FF2R, FF2RH MAY 7, 1980
SYMBOL TABLE

ZEND: 007NA (00062)

LINES WITH ERRORS: 0 (MAP VERSION R00101.10) 1- 0

APSDONE CP PROCESS INTERRUPT HANDLER MAY 29, 1980

T A B L E O F C O N T E N T S

DEFINING NECESSARY SYMBOLS FROM SNAP-II EXECUTIVE

PAGE 2

MAY 28, 1980

APS0004 AP PROCESS INTERRUPT HANDLER

(00001) *

(00002) *

(00003) *

(00004) *

(00005) *

(00006) *

(00007) *

(00008) *

(00009) *

(00010) *

(00011) *

(00012) *

(00013) *

(00014) *

(00015) *

(00016) *

THE AP DUMP INTERRUPT ROUTINE HAS BEEN MODIFIED
FOR GTE SYLVANIA TO SPEED UP PROCESSING.
IN ADDITION, SPECIAL CODE HAS BEEN ADDED TO CONVERSE W/ TH
ATC DISPATCH PROGRAM "CSPIER.TXT".

THE LIMITATIONS ARE DESCRIBED IN THE DOCUMENTATION.

000007F6 (00013) BL = S02F6

SFT PC TO CSW FOR AP DONE INTERRUPT

002F6 GFH2

DATA APS0004

SFT CSW TO NEW START ADDRESS

```

(00017) *
(00018) *
(00019) *
00001390 (00020)
00000245 (00021)
00000244 (00022)
00000006 (00023)
00000008 (00024)
00000000 (00025)
01000244 (00026)
00000010 (00027)
00000004 (00028)
00011400 (00029)
00000006 (00030)
00000000 (00031)
00011400 (00032)
00011400 (00033)
00000003 (00034)
00000002 (00035)
(00036) *
(00037) *
00000011 (00038)
00000020 (00039)
(00040) *
00000001 (00041)
00000502 (00042)
00000578 (00043)
00000540 (00044)
00000020 (00045)
00000038 (00046)
0000003F (00047)
00000001 (00048)
00000002 (00049)
(00050) *
00005000 (00051)
(00052) *
00000002 (00053)
00001012 (00054)
00011400 (00055)
(00056) *
00000002 (00057)
(00058) *
(00059) *
(00060)
  
```

ANOMIS = \$1300
 APSASSS = \$245
 APSCSU = \$244
 APSCSPIU = \$6
 APSFCM = \$H
 APSGI = \$D
 APSGAP = \$244
 APSHFF = \$6
 APSHPO = \$4
 APSHIA = \$1FFC0
 APSSTZE = \$H
 APSHPO = \$G
 APSHIA = \$1FFCA
 APSHPC = \$1FFCH
 APSSTZE = \$4
 PUSSTRT = \$2

 FIGSRT = \$11
 FIGSST = \$20

 NS = 1
 ISVTS=\$502
 ISAS125=ISVTS+D'125'
 ISAS126=ISVTS+D'126'
 IIVS12 = \$20
 IIVS62 = \$3F
 IIVS63 = \$3F
 IIVLS1 = \$1
 IIVLS2 = \$2

 OFFSET = \$5000

 SCUSPENTRY = \$E02
 SFTSSNH = \$1C12
 SVSFLGS = \$1FFCH

 WS = \$2

 EJECT


```

(00061) *
(00062) BL = S0F02      SET PC TO OLD APSDNE + HS (FVEN)
(00063) *
(00064) *
(00065) * THIS MODULE IS ENTERED WHENEVER RA MARKS THE
(00066) * TRANSITION FROM ON TO OFF OF THE EXECUTIVE HAS
(00067) * GENERATED THE INTERRUPT BECAUSE THE AP IS IDLE AND
(00068) * A TASK IS PENDING
(00069) *
(00070) * THE MODULE RELEASES THE CURRENT ACTIVE TASK
(00071) * AND ACTIVATES ANY PENDING TASK.
(00072) *
(00073) * THE CONTENTS OF THE AP-PROCESSOR CONTROL BLOCK
(00074) * ARE USED TO CONTROL THE FUNCTIONS OF THIS MODULE
(00075) *
(00076) * THE MODULE ASSUMES THAT REGISTERS R1-R7 ARE
(00077) * PRESERVED BY THE INTERRUPT.
(00078) *
(00079) APSDNE      MOVWPL R6,APSCSL      GET POINTERS TO SUPPORT LISTS
(00080) **          ANDIR R6, SFFF          MASK ADDRESS FOR SIGN EXTENSION
(00081) **          ANDIR R7, SFFF          IF ABOVE 16X HALFWORDS
(00082) **          TEST R6
(00083) **          JNP APSDNE10,E0Z      JUMP IF NULL.
(00084) *
(00085) **          EVEN
(00086) **          MOVW R1,APSCFR(R6)      GET CURRENT AP-PCB POINTER
(00087) **          DECM APSASS          IF SPECIAL SUPPORT SEMAPHORE NON-ZERO
(00088) **          JNP APSCSPL(R6),GEZ    GOTO DESIGNATED MODULE
(00089) *
(00090) *
(00091) * RETURN HERE IF JUMP WAS EXECUTED WHEN NO SUPPORT WAS REQUESTED
(00092) *
(00093) *
(00094) APSDNE10 APSDNE10, ILVLS2      FLAG AP DONE TO WAITING PROGRAM
(00095) **          R4,R1,,NOT. MRSKATO    GET POINTER TO PCB JUST PROCESSED
(00096) **          R5, HS(R4)            AND PCB NUMBER WITH CONTROL BITS
(00097) **          MOVWPL R4,APSCFR      SAVE LAST PCB PROCESSED
(00098) **          MOVW R3,APSCFR(R6)    IF PERM. ROUND FUNCTION
(00099) **          TORM R3, FLSNDAT      OR BUFFER TESTING INHIBITED
(00100) **          JNP APSDNE05,NEZ      BYPASS BUFFER IN USE CLEARUP
(00101) **
(00102) **          MOVW R3, FCSMMR-1      ELSE RESET BUFFER IN-USE FLAGS
(00103) **          MOVW R5,,NOT. MRSKCURH  POINT TO LAST POSSIBLE LOGICAL BUFFER ID
(00104) **          ANDIR R4, WS(R3)

```


PAGE 52 ADDRESS AP ADDRESS INTERRUPT HANDLER MAY 29, 1980
DEFINING MACROASSEMBLY SYMBOLS FROM SNAP-11 EXECUTIVE

```

000000 00000004 (00150) APSDEF30      MOVAP      R3, APSDEF30(R6)
000000 00000005 (00150)      LDR      R3, R3, WS
000000 00000006 (00151)      LDR      R3, R3, WS
000000 00000007 (00152) *R2      SUBR      R3, OFFSET      SUBTRACT OFFSET ON RUS 2
000000 00000008 (00153)      MOVAP      R2, APSDEF30(R6)
000000 00000009 (00154)      LRS      R2, 1      COUNT = # WORDS TO BE MOVED
000000 00000010 (00155)      LDR      R2, R2, HS
000000 00000011 (00156)      LDR      R3, R2, APSDEF30
000000 00000012 (00157)      CCS      3
000000 00000013 (00158) *R2      SCS      2
000000 00000014 (00159) *      UPGRADE      PENDING AP-FUNCTION TO CURRENT
000000 00000015 (00160) **      MOVAP      R4, APSDEF30(R6)      PICK UP G0, G1 FLAG STATES
000000 00000016 (00161)      MOVAP      R4, APSDEF30(R6)
000000 00000017 (00162) **      MOVAP      R4, SYSSEFLGS      SET OR CLEAR G1
000000 00000018 (00163) **      MOVAP      R5, SYSSEFLGS      SET OR CLEAR G1
000000 00000019 (00164) **      MOVAP      APSDEF30      SET SPECIAL SUPP. SEMAPHORE
000000 00000020 (00165) **      MOVAP      R4, APSDEF30(R6)      SET SPECIAL SUPPORT SEMAPHORE
000000 00000021 (00166) **      MOVAP      R4, APSDEF30
000000 00000022 (00167) **      MOVAP      R3, APSDEF30(R6)      SET SPECIAL SCALAR
000000 00000023 (00168) **      MOVAP      R3, APSDEF30
000000 00000024 (00169) **      MOVAP      SYSSEFLGS+R3, SYSSEFLGS      START APS
000000 00000025 (00170) **      MOVAP      SYSSEFLGS+R3, SYSSEFLGS
000000 00000026 (00171) **      CHECK FOR PERMANENTLY ROUND FUNCTION OR BUFFER CHECK INHIBIT
000000 00000027 (00172) **      MOVAP      R3, APSDEF30(R6)      GET PERMANENTLY ROUND FUNCTION FLAG
000000 00000028 (00173) **      LDR      R3, FLAGSMNT      COMPARE PRE FLAG WITH BUFFER INHIBIT
000000 00000029 (00174) **      JMP      APSDEF30, NEZ      BYPASS BUFFER USAGE UPGRADE IF EITHER SET
000000 00000030 (00175) **      BUFFER USAGE UPGRADE - PENDING TO CURRENT
000000 00000031 (00176) **      MOVAP      R1, APSDEF30(R6)
000000 00000032 (00177) **      MOVAP      R3, FLAGSMNT+1
000000 00000033 (00178) **      MOVAP      R5, .NOT. MASKSMNT
000000 00000034 (00179) **      MOVAP      R4, R1, .NOT. MASKSMNT
000000 00000035 (00180) **      ADDR      R4, R3, R3
000000 00000036 (00181) **      LDR      TO TRANSFER BUFFER USAGE BITS
000000 00000037 (00182) **
000000 00000038 (00183) **
000000 00000039 (00184) **
000000 00000040 (00185) **
000000 00000041 (00186) **
000000 00000042 (00187) **
000000 00000043 (00188) **
000000 00000044 (00189) **
000000 00000045 (00190) **
000000 00000046 (00191) **
000000 00000047 (00192) **

```

```

(00193) 00021 MOVW R2,R4,UNKNSTAT
(00194) 00000 RTS
(00195) 00000 EVFN
(00196) 00000 MOVW R1,UNKNSTAT
(00197) 00000 ANDW R1,ACTSAT(R2)
(00198) 00000 LRS R1,1
(00199) 00000 EVFN
(00200) 00000 ANDW R5,ACTSAT(R2)
(00201) 00000 LRRW R1,ACTSAT(R2)
(00202) 00000 DECR R3,1
(00203) 00000 EVFN
(00204) 00000 LOP
(00205) 00000
(00206) 00000 APSHOW15 MOVW R6,APCSL
(00207) 00000 MOVW R5,1
(00208) 00000 MOVW R5,APSPAF
(00209) 00000 WAITC DEVS32,LEVEL1
(00210) 00000 ***** WAIT TILL FOR RELEASE DONE *****
(00211) 00000 * ADD INTERFACE CODE FOR "CSPUK"
(00212) 00000 MOVW R5,ISAS126
(00213) 00000 INCM ISAS125
(00214) 00000
(00215) 00000 *****
(00216) 00000 RET
(00217) 00000 EVFN
(00218) 00000 JMP APSHOW
(00219) 00000
(00220) 00000
(00221) 00000
(00222) 00000 * PATH FOR MULT. PENDING FOR
(00223) 00000
(00224) 00000
(00225) 00000 APSHOW20 MOVW R6,APCSL
(00226) 00000 MOVW R5,APSPAF
(00227) 00000 WAITC DEVS32,LEVEL1
(00228) 00000 AINT DEVS32,LEVEL1
(00229) 00000 ***** TELL WAIT ROUTINE AP IS IDLE *****
(00230) 00000 * ADD INTERFACE CODE FOR "CSPUK"
(00231) 00000 MOVW R5,ISAS126
(00232) 00000 INCM ISAS125
(00233) 00000
(00234) 00000 *****
(00235) 00000 RET
(00236) 00000 EVFN
  
```

SET "APDONE" SCALAR
 FOR CALLING CARD. = 0 A T
 OP OF MPWHL,

RETURN, BACK TO CSPUK?

RESTART ON NEXT INTERRUPT

T

SET CURRENT FOR NULL
 SET PROCESSOR AVAILABLE
 WAIT TILL FOR RELEASE DONE
 TELL WAIT ROUTINE AP IS IDLE

SET "APDONE" SCALAR
 FOR CALLING CARD. = 0 A T
 OP OF MPWHL,

RETURN, BACK TO CSPUK?

LINE	INSTRUMENT
1	1
2	2
3	3
4	4
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93	93
94	94
95	95
96	96
97	97
98	98
99	99
100	100

RESTART IN NEXT INTERPRIT

ADHITS:	01390 (00020)	
APSSASS:	00215 (00021)	
APSSSL:	00248 (00022)	(00079) (00206) (00225)
APSSSP2:	00006 (00023)	
APSSMPO:	00140 (00112)	
APSSMPO:	00140 (00083)	(00113) (00119)
APSSMPO:	00140 (00206)	
APSSMPO:	00140 (00121)	(00225)
APSSMPO:	00140 (00149)	
APSSMPO:	00140 (00143)	
APSSMPO:	00140 (00115)	(00079) (00214) (00247)
APSSMPO:	00140 (00094)	
APSSMPO:	00006 (00024)	(00086)
APSSMPO:	00000 (00025)	(00165)
APSSMPO:	00244 (00026)	(00208) (00226)
APSSMPO:	00010 (00027)	
APSSMPO:	00004 (00028)	(00146)
APSSMPO:	11140 (00029)	(00157)
APSSMPO:	00008 (00030)	(00154)
APSSMPO:	00000 (00031)	(00125)
APSSMPO:	11140 (00032)	(00145)
APSSMPO:	11140 (00033)	(00132)
APSSMPO:	00003 (00034)	(00133)
APSSMPO:	00002 (00035)	
APSSMPO:	00011 (00038)	(00173)
APSSMPO:	00020 (00039)	(00173)
APSSMPO:	00001 (00041)	(00134) (00156) (00212) (00231)
APSSMPO:	00020 (00045)	(00115) (00209) (00227)
APSSMPO:	00036 (00046)	(00228)
APSSMPO:	00016 (00047)	(00094)
APSSMPO:	00001 (00048)	(00115) (00209) (00227) (00228)
APSSMPO:	00002 (00049)	(00094)
APSSMPO:	00576 (00043)	(00213) (00232)
APSSMPO:	00580 (00044)	(00212) (00231)
APSSMPO:	00502 (00042)	(00043) (00044)
APSSMPO:	05000 (00051)	
APSSMPO:	00102 (00053)	
APSSMPO:	01112 (00054)	
APSSMPO:	11140 (00055)	(00166) (00173)
APSSMPO:	00002 (00057)	(00143) (00150)

[illegible]

SET TO INDICATE FIRST HC
VR FRAME AFTER SYNC

[illegible]

(000224)	104675	001A	$10^3 20461^3, 10^3 1^3$
(000225)	?		
(000226)		1-ATA	$10^3 4440^3, 10^3 1^3$
(000227)		1-ATA	$10^3 6860^3, 10^3 2^3$
(000228)		1-ATA	$10^3 1320^3, 10^3 4^3$
(000229)		1-ATA	$10^3 40472^3, 10^3 7^3$
(000230)		1-ATA	$10^3 7452^3, 10^3 1^3$
(000231)		1-ATA	$10^3 15504^3, 10^3 2^3$
(000232)		1-ATA	$10^3 41000^3, 10^3 4^3$
(000233)		1-ATA	$10^3 536^3, 10^3 7^3$
(000234)		1-ATA	$10^3 10731^3, 10^3 6^3$
(000235)		1-ATA	$10^3 2147^3, 10^3 4^3$
(000236)		1-ATA	$10^3 4208^3, 10^3 0^3$
(000237)		1-ATA	$10^3 8500^3, 10^3 0^3$
(000238)		1-ATA	$10^3 17380^3, 10^3 0^3$
(000239)		1-ATA	$10^3 30408^3, 10^3 7^3$
(000240)		1-ATA	$10^3 7422^3, 10^3 1^3$
(000241)		1-ATA	$10^3 14643^3, 10^3 2^3$
(000242)		1-ATA	$10^3 20284^3, 10^3 4^3$

MILITARY AND NAVAL
ATTACHES ONLY

() A MILITARY AND NAVAL ATTACHES ONLY

0400 02 0000, COUNCIL, 1117

09000 0000	(002000)	DATA 0'200000', 0'0000'
09001 0000	(002000)	DATA 0'200000', 0'0000'
09002 0000	(002000)	DATA 0'200000', 0'0000'
09003 0000	(002000)	DATA 0'200000', 0'0000'
09004 0000	(002000)	DATA 0'200000', 0'0000'
09005 0000	(002000)	DATA 0'200000', 0'0000'
09006 0000	(002000)	DATA 0'200000', 0'0000'
09007 0000	(002000)	DATA 0'200000', 0'0000'
09008 0000	(002000)	DATA 0'200000', 0'0000'
09009 0000	(002000)	DATA 0'200000', 0'0000'
09010 0000	(002000)	DATA 0'200000', 0'0000'
09011 0000	(002000)	DATA 0'200000', 0'0000'
09012 0000	(002000)	DATA 0'200000', 0'0000'
09013 0000	(002000)	DATA 0'200000', 0'0000'
09014 0000	(002000)	DATA 0'200000', 0'0000'
09015 0000	(002000)	DATA 0'200000', 0'0000'
09016 0000	(002000)	DATA 0'200000', 0'0000'
09017 0000	(002000)	DATA 0'200000', 0'0000'
09018 0000	(002000)	DATA 0'200000', 0'0000'
09019 0000	(002000)	DATA 0'200000', 0'0000'
09020 0000	(002000)	DATA 0'200000', 0'0000'

RESIDUE OUT TO FIRST INF
FORMATION BIT
POWER SUM TABLE IN REVER
SE ORDER

PAGE 112 FROM: CROUSP.FAT

09034 0005		
09039 0024	(00200)	DATA 0'17',0'40',0'16'
09044 0007		
09049 0024	(00200)	DATA 0'13',0'45',0'24'
09054 0010		
09059 0008		
09064 0004	(00400)	DATA 0'20',0'53',0'20'
09069 0018		
09074 0010	(00400)	DATA 0'56',0'17',0'30'
09079 0013		
09084 0034	(00402)	DATA 0'53',0'14',0'17'
09089 0011		
09094 0034	(00404)	DATA 0'47',0'53',0'56'
09099 0025		
09104 0034	(00403)	DATA 0'49',0'18',0'46'
09109 0009		
09114 0012	(00405)	DATA 0'18',0'22',0'54'
09119 0024		
09124 0016	(00406)	DATA 0'46',0'54',0'45'
09129 0036		
09134 0024	(00407)	DATA 0'11',0'58',0'26'
09139 0036		
09144 0016	(00408)	DATA 0'22',0'28',0'23'
09149 0017		
09154 0020	(00409)	DATA 0'44',0'33',0'61'
09159 0006		
09164 0034	(00410)	DATA 0'27',0'43',0'22'
09169 0019		
09174 0006		
09179 0030	(00411)	DATA 0'53',0'23',0'43'
09184 0004		

PACK 172 PAGE CODES, EX1

00073 0074	(00412)	DATA 01471,01621,01451
00075 0075		
00076 0076	(00413)	DATA 01299,01571,01401
00077 0077		
00078 0078	(00414)	DATA 01500,01411,01591
00079 0079		
00080 0080	(00415)	DATA 01588,01681,0171
00081 0081		
00082 0082	(00416)	DATA 01451,01311,01371
00083 0083		
00084 0084	(00417)	DATA 01251,01241,01221
00085 0085		
00086 0086	(00418)	DATA 01501,01511,01291
00087 0087		
00088 0088	(00419)	DATA 01391,01301,01501
00089 0089		
00090 0090	(00420)	DATA 01141,01111,01431
00091 0091		
00092 0092	(00421)	DATA 01261,01591,01411
00093 0093		
00094 0094	(00422)	DATA 01521,01171,01491
00095 0095		
00096 0096	(00423)	DATA 01431,01141,0161
00097 0097		
00098 0098	(00424)	DATA 01211,01511,01121
00099 0099		
00100 0100	(00425)	DATA 01421,01181,01101
00101 0101		
00102 0102	(00426)	DATA 01231,01221,01151
00103 0103		
00104 0104		
00105 0105		

AD-A691 663

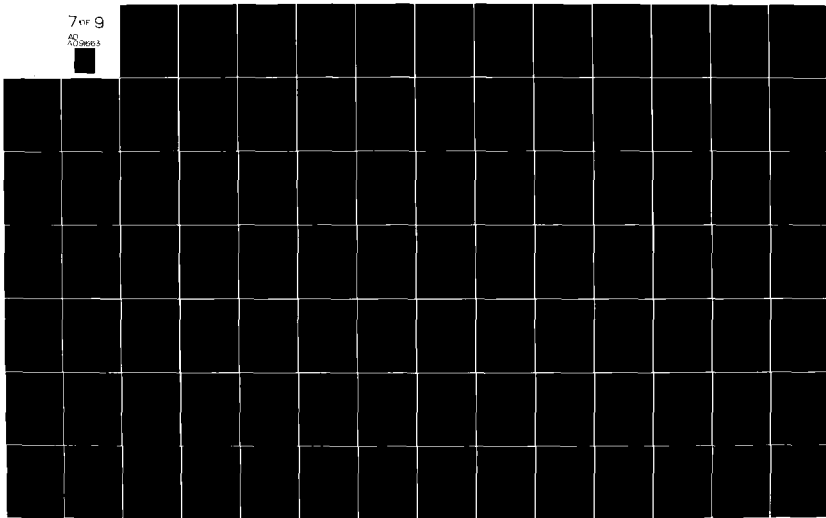
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME S0--ETC (U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

7 of 9

AD
A000000



00000 0000	(00321)	DATA 0'30',0'54',0'41'
00001 0024		
00002 0046		
00003 0029	(00324)	DATA 0'31',0'58',0'28'
00004 0014		
00005 0046		
00006 0016	(00329)	DATA 0'32',0'28',0'18'
00007 0046		
00008 0019		
00009 0012	(00330)	DATA 0'33',0'13',0'27'
00010 0034		
00011 0000		
00012 0010	(00331)	DATA 0'31',0'43',0'54'
00013 0030		
00014 0037	(00332)	DATA 0'37',0'23',0'13'
00015 0039		
00016 0017		
00017 0000	(00333)	DATA 0'40',0'62',0'42'
00018 0031		
00019 0046		
00020 0028	(00334)	DATA 0'33',0'57',0'63'
00021 0000		
00022 0039		
00023 0014		
00024 0001	(00335) ;	
00025 0001	(00336) ;	DATA 0'11'
00026 0002	(00337) ;	
00027 0004	(00338)	DATA 0'20'
00028 0000	(00339)	DATA 0'40'
00029 0000	(00340)	DATA 0'50'
00030 0010	(00341)	DATA 0'16'
00031 0020	(00342)	DATA 0'42'
00032 0001	(00343)	DATA 0'30'
00033 0006	(00344)	DATA 0'60'
00034 0000	(00345)	DATA 0'12'
00035 0014	(00346)	DATA 0'24'
00036 0030	(00347)	DATA 0'38'
00037 0024	(00348)	DATA 0'45'
00038 0005	(00349)	DATA 0'50'
00039 0000	(00350)	DATA 0'10'
00040 0014	(00351)	DATA 0'20'
00041 0024	(00352)	DATA 0'40'
00042 0014	(00353)	DATA 0'10'

POWER SUPPLY S1,S3,S5 DUE

TO FIRST INFORMATION HIT
POWER TO RESIDUE CONVERS
ION TABLE
FOR A (63,45) RCH CODE

0900A 0028	(004504)	DATA 0'340
0900B 000F	(004505)	DATA 0'150
0900C 0018	(004506)	DATA 0'400
0900D 004C	(004507)	DATA 0'800
0900E 0045	(004508)	DATA 0'500
0900F 0035	(004509)	DATA 0'530
09010 0029	(004510)	DATA 0'410
09011 0011	(004511)	DATA 0'170
09012 0022	(004512)	DATA 0'330
09013 0007	(004513)	DATA 0'700
09014 000F	(004514)	DATA 0'140
09015 001C	(004515)	DATA 0'280
09016 0038	(004516)	DATA 0'560
09017 0033	(004517)	DATA 0'510
09018 0025	(004518)	DATA 0'470
09019 0009	(004519)	DATA 0'000
0901A 0012	(004520)	DATA 0'180
0901B 0024	(004521)	DATA 0'360
0901C 0006	(004522)	DATA 0'110
0901D 0016	(004523)	DATA 0'220
0901E 002C	(004524)	DATA 0'440
0901F 0016	(004525)	DATA 0'270
09020 0046	(004526)	DATA 0'540
09021 002F	(004527)	DATA 0'470
09022 0010	(004528)	DATA 0'290
09023 003A	(004529)	DATA 0'580
09024 0037	(004530)	DATA 0'550
09025 0020	(004531)	DATA 0'450
09026 0039	(004532)	DATA 0'500
09027 0027	(004533)	DATA 0'390
09028 0000	(004534)	DATA 0'130
09029 001A	(004535)	DATA 0'260
0902A 0034	(004536)	DATA 0'520
0902B 0020	(004537)	DATA 0'430
0902C 0015	(004538)	DATA 0'210
0902D 002A	(004539)	DATA 0'420
0902E 0017	(004540)	DATA 0'230
0902F 002E	(004541)	DATA 0'460
09030 0016	(004542)	DATA 0'310
09031 0036	(004543)	DATA 0'620
09032 0048	(004544)	DATA 0'640
09033 0030	(004545)	DATA 0'610
09034 0049	(004546)	DATA 0'570

00021 0017	(000421)	DATA 0124
00022 0045	(000433)	DATA 0154
00023 0043	(000433)	DATA 0151
00024 0025	(000455)	DATA 0137
00025 0021	(000436)	DATA 0144
00026 0037	(000447)	DATA 0155
00027 0028	(000414)	DATA 0140
00028 0006	(000439)	DATA 0110
00029 0030	(000450)	DATA 0161
00030 0026	(000451)	DATA 0146
00031 0014	(000452)	DATA 0130
00032 0032	(000453)	DATA 0150
00033 0016	(000454)	DATA 0122
00034 0027	(000455)	DATA 0159
00035 0020	(000456)	DATA 0144
00036 0010	(000457)	DATA 0129
00037 0040	(000458)	DATA 0160
00038 0028	(000459)	DATA 0132
00039 0015	(000460)	DATA 0121
00040 0014	(000461)	DATA 0120
00041 0030	(000462)	DATA 0150
00042 0039	(000463)	DATA 0157
00043 0038	(000464)	DATA 0158
00044 0000	(000465)	DATA 0101
00045 0002	(000466)	DATA 0120
00046 0004	(000467)	DATA 0140
00047 0006	(000468)	DATA 0160
00048 0008	(000470)	DATA 0180
00049 0000	(000471)	DATA 0110
00050 0000	(000472)	DATA 0112
00051 0000	(000473)	DATA 0114
00052 0010	(000474)	DATA 0116
00053 0012	(000475)	DATA 0118
00054 0014	(000476)	DATA 0120
00055 0016	(000477)	DATA 0122
00056 0018	(000478)	DATA 0124
00057 0019	(000479)	DATA 0126
00058 0010	(000480)	DATA 0128
00059 0014	(000481)	DATA 0140
00060 0020	(000482)	DATA 0142
00061 0022	(000483)	DATA 0144
00062 0024	(000484)	DATA 0146
00063 0026	(000485)	DATA 0148

MULT. TABLE OF ALPHA FOR
A (63,45)RCH CODE

0044C 0028	(004463)	DATA 0040*
0044D 0029	(004467)	DATA 0041*
0044E 002C	(004464)	DATA 0042*
0044F 002B	(004465)	DATA 0043*
00450 0030	(004466)	DATA 0044*
00451 0032	(004467)	DATA 0045*
00452 0034	(004468)	DATA 0046*
00453 0036	(004469)	DATA 0047*
00454 0038	(004470)	DATA 0048*
00455 003A	(004471)	DATA 0049*
00456 003C	(004472)	DATA 0050*
00457 003E	(004473)	DATA 0051*
00458 0040	(004474)	DATA 0052*
00459 0043	(004475)	DATA 0053*
0045A 0046	(004476)	DATA 0054*
0045B 0049	(004477)	DATA 0055*
0045C 004B	(004478)	DATA 0056*
0045D 004D	(004479)	DATA 0057*
0045E 004F	(004480)	DATA 0058*
0045F 0051	(004481)	DATA 0059*
00460 0053	(004482)	DATA 0060*
00461 0055	(004483)	DATA 0061*
00462 0057	(004484)	DATA 0062*
00463 0059	(004485)	DATA 0063*
00464 005B	(004486)	DATA 0064*
00465 005D	(004487)	DATA 0065*
00466 005F	(004488)	DATA 0066*
00467 0061	(004489)	DATA 0067*
00468 0063	(004490)	DATA 0068*
00469 0065	(004491)	DATA 0069*
0046A 0067	(004492)	DATA 0070*
0046B 0069	(004493)	DATA 0071*
0046C 006B	(004494)	DATA 0072*
0046D 006D	(004495)	DATA 0073*
0046E 006F	(004496)	DATA 0074*
0046F 0071	(004497)	DATA 0075*
00470 0073	(004498)	DATA 0076*
00471 0075	(004499)	DATA 0077*
00472 0077	(004500)	DATA 0078*
00473 0079	(004501)	DATA 0079*
00474 007B	(004502)	DATA 0080*
00475 007D	(004503)	DATA 0081*
00476 007F	(004504)	DATA 0082*
00477 0081	(004505)	DATA 0083*
00478 0083	(004506)	DATA 0084*
00479 0085	(004507)	DATA 0085*
0047A 0087	(004508)	DATA 0086*
0047B 0089	(004509)	DATA 0087*
0047C 008B	(004510)	DATA 0088*
0047D 008D	(004511)	DATA 0089*
0047E 008F	(004512)	DATA 0090*
0047F 0091	(004513)	DATA 0091*
00480 0093	(004514)	DATA 0092*
00481 0095	(004515)	DATA 0093*
00482 0097	(004516)	DATA 0094*
00483 0099	(004517)	DATA 0095*
00484 009B	(004518)	DATA 0096*
00485 009D	(004519)	DATA 0097*
00486 009F	(004520)	DATA 0098*
00487 00A1	(004521)	DATA 0099*
00488 00A3	(004522)	DATA 0100*
00489 00A5	(004523)	DATA 0101*
0048A 00A7	(004524)	DATA 0102*
0048B 00A9	(004525)	DATA 0103*
0048C 00AB	(004526)	DATA 0104*
0048D 00AD	(004527)	DATA 0105*
0048E 00AF	(004528)	DATA 0106*
0048F 00B1	(004529)	DATA 0107*

MULT. TABLE OF ALPAA*2

0047A 0000	(00540)	DATA 0'01
0047B 0004	(00541)	DATA 0'11
0047C 0008	(00542)	DATA 0'00
0047D 000C	(00543)	DATA 0'12
0047E 0010	(00544)	DATA 0'16
0047F 0014	(00545)	DATA 0'20
00480 0018	(00546)	DATA 0'24
00481 001C	(00547)	DATA 0'28
00482 0020	(00548)	DATA 0'32
00483 0024	(00549)	DATA 0'36
00484 0028	(0054A)	DATA 0'40
00485 002C	(0054B)	DATA 0'44
00486 0030	(0054C)	DATA 0'48
00487 0034	(0054D)	DATA 0'52
00488 0038	(0054E)	DATA 0'56
00489 003C	(0054F)	DATA 0'60
0048A 0040	(00550)	DATA 0'64
0048B 0044	(00551)	DATA 0'68
0048C 0048	(00552)	DATA 0'72
0048D 004C	(00553)	DATA 0'76
0048E 0050	(00554)	DATA 0'80
0048F 0054	(00555)	DATA 0'84
00490 0058	(00556)	DATA 0'88
00491 005C	(00557)	DATA 0'92
00492 0060	(00558)	DATA 0'96
00493 0064	(00559)	DATA 1'00
00494 0068	(0055A)	DATA 1'04
00495 006C	(0055B)	DATA 1'08
00496 0070	(0055C)	DATA 1'12
00497 0074	(0055D)	DATA 1'16
00498 0078	(0055E)	DATA 1'20
00499 007C	(0055F)	DATA 1'24
0049A 0080	(00560)	DATA 1'28
0049B 0084	(00561)	DATA 1'32
0049C 0088	(00562)	DATA 1'36
0049D 008C	(00563)	DATA 1'40
0049E 0090	(00564)	DATA 1'44
0049F 0094	(00565)	DATA 1'48
004A0 0098	(00566)	DATA 1'52
004A1 009C	(00567)	DATA 1'56
004A2 00A0	(00568)	DATA 1'60
004A3 00A4	(00569)	DATA 1'64
004A4 00A8	(0056A)	DATA 1'68
004A5 00AC	(0056B)	DATA 1'72
004A6 00B0	(0056C)	DATA 1'76
004A7 00B4	(0056D)	DATA 1'80
004A8 00B8	(0056E)	DATA 1'84
004A9 00BC	(0056F)	DATA 1'88
004AA 00C0	(00570)	DATA 1'92
004AB 00C4	(00571)	DATA 1'96
004AC 00C8	(00572)	DATA 2'00
004AD 00CC	(00573)	DATA 2'04

004100 0000	(000100)	DATA 0000
004101 0000	(000101)	DATA 0000
004102 0000	(000102)	DATA 0000
004103 0000	(000103)	DATA 0000
004104 0000	(000104)	DATA 0000
004105 0000	(000105)	DATA 0000
004106 0000	(000106)	DATA 0000
004107 0000	(000107)	DATA 0000
004108 0000	(000108)	DATA 0000
004109 0000	(000109)	DATA 0000
004110 0000	(000110)	DATA 0000
004111 0000	(000111)	DATA 0000
004112 0000	(000112)	DATA 0000
004113 0000	(000113)	DATA 0000
004114 0000	(000114)	DATA 0000
004115 0000	(000115)	DATA 0000
004116 0000	(000116)	DATA 0000
004117 0000	(000117)	DATA 0000
004118 0000	(000118)	DATA 0000
004119 0000	(000119)	DATA 0000
004120 0000	(000120)	DATA 0000
004121 0000	(000121)	DATA 0000
004122 0000	(000122)	DATA 0000
004123 0000	(000123)	DATA 0000
004124 0000	(000124)	DATA 0000
004125 0000	(000125)	DATA 0000
004126 0000	(000126)	DATA 0000
004127 0000	(000127)	DATA 0000
004128 0000	(000128)	DATA 0000
004129 0000	(000129)	DATA 0000
004130 0000	(000130)	DATA 0000
004131 0000	(000131)	DATA 0000
004132 0000	(000132)	DATA 0000
004133 0000	(000133)	DATA 0000
004134 0000	(000134)	DATA 0000
004135 0000	(000135)	DATA 0000
004136 0000	(000136)	DATA 0000
004137 0000	(000137)	DATA 0000
004138 0000	(000138)	DATA 0000
004139 0000	(000139)	DATA 0000
004140 0000	(000140)	DATA 0000
004141 0000	(000141)	DATA 0000
004142 0000	(000142)	DATA 0000
004143 0000	(000143)	DATA 0000
004144 0000	(000144)	DATA 0000
004145 0000	(000145)	DATA 0000
004146 0000	(000146)	DATA 0000
004147 0000	(000147)	DATA 0000
004148 0000	(000148)	DATA 0000
004149 0000	(000149)	DATA 0000
004150 0000	(000150)	DATA 0000
004151 0000	(000151)	DATA 0000
004152 0000	(000152)	DATA 0000
004153 0000	(000153)	DATA 0000
004154 0000	(000154)	DATA 0000
004155 0000	(000155)	DATA 0000
004156 0000	(000156)	DATA 0000
004157 0000	(000157)	DATA 0000
004158 0000	(000158)	DATA 0000
004159 0000	(000159)	DATA 0000
004160 0000	(000160)	DATA 0000
004161 0000	(000161)	DATA 0000
004162 0000	(000162)	DATA 0000
004163 0000	(000163)	DATA 0000
004164 0000	(000164)	DATA 0000
004165 0000	(000165)	DATA 0000
004166 0000	(000166)	DATA 0000
004167 0000	(000167)	DATA 0000
004168 0000	(000168)	DATA 0000
004169 0000	(000169)	DATA 0000
004170 0000	(000170)	DATA 0000
004171 0000	(000171)	DATA 0000
004172 0000	(000172)	DATA 0000
004173 0000	(000173)	DATA 0000
004174 0000	(000174)	DATA 0000
004175 0000	(000175)	DATA 0000
004176 0000	(000176)	DATA 0000
004177 0000	(000177)	DATA 0000
004178 0000	(000178)	DATA 0000
004179 0000	(000179)	DATA 0000
004180 0000	(000180)	DATA 0000
004181 0000	(000181)	DATA 0000
004182 0000	(000182)	DATA 0000
004183 0000	(000183)	DATA 0000
004184 0000	(000184)	DATA 0000
004185 0000	(000185)	DATA 0000
004186 0000	(000186)	DATA 0000
004187 0000	(000187)	DATA 0000
004188 0000	(000188)	DATA 0000
004189 0000	(000189)	DATA 0000
004190 0000	(000190)	DATA 0000
004191 0000	(000191)	DATA 0000
004192 0000	(000192)	DATA 0000
004193 0000	(000193)	DATA 0000
004194 0000	(000194)	DATA 0000
004195 0000	(000195)	DATA 0000
004196 0000	(000196)	DATA 0000
004197 0000	(000197)	DATA 0000
004198 0000	(000198)	DATA 0000
004199 0000	(000199)	DATA 0000
004200 0000	(000200)	DATA 0000

ACH

MESS-2(REF. # FOR BLACK

PAGE 21: PROG. CSPOSD.FBI

00000 0000 (00000) ?
00001 0000 (00001) STOPS DATA 0000
... (00002) RECS DATA 0000
(00003) ?
(00004) ?
(00005) ? END OF DATA MEMORY
(00006) ? P.0000

1).MESS-1000, # FOR AL.
MESS-1000, # FOR BLOCK 1)
DATA MEM. FOR REGISTER W

1-07

WHEN WE COME BACK TEST 1
***FOR TRAIN TIMING ONLY
**
HAS ADDONE. HAPPENED?

[illegible]

YH:S

SAY AP OUT RUFF EMPTY
HAS APDOME HAPPENED?


```

(00984) *
(00989) *
(00990)
0A07A 2A3C      EVEN
                SECTCL 5,466
0A07B 02044C04  SEB 0,SEAS(R2)
0A07C 2621      INCR R2,1
                EVEN
                SECTCL 4,466
0A07D 2A3C      SEB 0,SEAS(R2)
0A07E 02044C04  INCR R2,1
                EVEN
                SECTCL 3,466
0A07F 2621      SEB 0,SEAS(R2)
0A080 2A3C      EVEN
                SECTCL 2,466
0A081 02044C04  SEB 0,SEAS(R2)
0A082 2621      INCR R2,1
                EVEN
                SECTCL 1,466
0A083 2A3C      SEB 0,SEAS(R2)
0A084 02044C04  INCR R2,1
                EVEN
                SECTCL 0,466
0A085 2621      SEB 0,SEAS(R2)
0A086 2A3C      EVEN
                SECTCL 0,466
0A087 02044C04  INCR R2,1
                EVEN
0A088 2621      EVEN

```

IS PARM BUFFER HIT 5 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

IS PARM BUFFER HIT 4 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

IS PARM BUFFER HIT 3 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

IS PARM BUFFER HIT 2 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

IS PARM BUFFER HIT 1 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

IS PARM BUFFER HIT 0 CLE
AR?
OTHERWISE, SET LOCATION
OF SEN ARRAY TO BE A 1
INCREMENT THE COUNTER R2

0A2C1 0000	(01342)	SAVE
0A2C2 0000000	(01343)	MOVZP ADDRESS
0A2C3 0100000	(01344)	STORE R1,R1SAV
0A2C6 0120000	(01345)	STORE R2,R2SAV
0A2C8 0130000	(01346)	STORE R3,R3SAV
0A2CA 0130000	(01347)	STORE R4,R4SAV
0A2CC 0150000	(01348)	STORE R5,R5SAV
0A2CE 0150000	(01349)	STORE R6,R6SAV
0A2D0 0170000	(01350)	STORE R7,R7SAV
0A2D2 0000	(01351)	PRT
0A2D3 0000	(01352)	SAVE
0A2D4 0010000	(01353)	LOAD R1,R1SAV
0A2D6 0020000	(01354)	LOAD R2,R2SAV
0A2D8 0030000	(01355)	LOAD R3,R3SAV
0A2DA 0040000	(01356)	LOAD R4,R4SAV
0A2DC 0050000	(01357)	LOAD R5,R5SAV
0A2DE 0060000	(01358)	LOAD R6,R6SAV
0A2E0 0070000	(01359)	LOAD R7,R7SAV
0A2E2 0000	(01360)	ADDRP R1,R1
0A2E3 0000	(01361)	SAVE
0A2E4 0000	(01362)	PUSHR R2,R1
0A2E5 0000	(01363)	SAVE
0A2E6 0000000	(01364)	DIP R6,R6TOS1
0A2E8 0000000	(01365)	SAVE
0A2EA 0000000	(01366)	CCS 3
0A2EC 0000000	(01367)	SCS 2
0A2EE 0000000	(01368)	SCS 5
0A2F0 0000000	(01369)	SCS 4
0A2F2 0000000	(01370)	JMP LOGATS
0A2F4 0000000	(01371)	JMP LOGATS
0A2F6 0000000	(01372)	JMP LOGATS
0A2F8 0000000	(01373)	JMP LOGATS
0A2FA 0000000	(01374)	JMP LOGATS

SAVE ONLY THE LAST HIT
 SAVE THE VALUE IN SENSOR
 2)... R2=R2+1
 LOOP BACK UNTIL THE ENT
 IRE FRAME IS DONE.
 FROM SPR=2...
 TO SPR=1
 FROM MRH=2...
 TO MRH=1
 PROTECT SIDERAND

(01478) ***** RCH=100000000 OF SIBERARD & PART OF MAINRARD DATA

SET INPUT BUFFER POINTER

HAS ADDONE HAPPENED?

NO, HOP TO EVEN LOC AFTE

R RET

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

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MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

MOVW R7,1

CLEAR 15 BITS REMAINDER

CLEAR BITS 16,17,18

R5=RB

READ SENS(R2)

GO TO RCH=3S IF SENS(R2)

=0

R6=THENC(R5),R7=THENC(R5

+1)

R3=R3.XOR.R6

R4=R4.XOR.R7

INCREMENT INPUT BUFFER

POINTER

DECREMENT TABLE POINTER

```

0A326 2006 (01519)
0A327 F054C04 (01520)
0A328 2056 (01521)
0A32A F054C04 (01522)
0A32C 2036 (01523)
0A32D F054C10 (01524)
0A32E 2036 (01525)
0A330 F054C11 (01526)
0A332 2026 (01527)
0A333 F054C12 (01528)
0A335 2016 (01529)
0A336 F054C13 (01530)
0A338 2006 (01531)
0A339 F054C14 (01532)
0A33B 2A76 (01533)
0A33C F054C15 (01534)
0A33E 2A66 (01535)
0A33F F054C16 (01536)
0A341 2A56 (01537)
0A342 F054C17 (01538)
0A343 2A46 (01539)
0A345 F054C18 (01540)
0A347 2A36 (01541)
0A349 F054C19 (01542)
0A34A 2A26 (01543)
0A34B F054C1A (01544)
0A34D 2A16 (01545)
0A34F F054C1B (01546)
0A350 2A06 (01547)
0A351 F054C1C (01548)
0A353 2A28 (01549)
0A354 F054C1D (01550)
0A356 2A18 (01551)
0A357 F054C1E (01552)
0A359 2A08 (01553)
0A35A F054C1F (01554)
0A35B * 2018 F2,18
0A35C * 2018 F2,190
0A35D * JAP F054C1F0
0A35E * 2018 F2,560
0A35F * JAP F054C1F1
0A360 *
0A361 *
0A362 *

```

```

F054C1F2

```

PAGE 44: PROG, CSBSP,EXT

0A35C H000A354
(01564) *
(01564) *
(01565) ENDFCS
(01566) *
(01567) *
(01568) *
(01569) *
VIEW
JMP LOCALS
SUBCT

```

(01570) ***** LOAD DIGITAL FRAME DATA INTO SEN1 OR SEN2
(01571) MOVES 4
(01572) CCS 4
(01573) CCS 2
(01574) CCS 4
(01575) CCS 5
(01576) SMPSL 0,APDUELS
(01577) MOV 01,34
(01578) :
(01579) EVEN
(01580) MOVZP APDUELS
(01581) STORE R1,R1SAV
(01582) STORE R2,R2SAV
(01583) STORE R3,R3SAV
(01584) STORE R4,R4SAV
(01585) STORE R5,R5SAV
(01586) STORE R6,R6SAV
(01587) STORE R7,R7SAV
(01588) RET
(01589) EVEN
(01590) LOAD R1,R1SAV
(01591) LOAD R2,R2SAV
(01592) LOAD R3,R3SAV
(01593) LOAD R4,R4SAV
(01594) LOAD R5,R5SAV
(01595) LOAD R6,R6SAV
(01596) LOAD R7,R7SAV
(01597) MOVZP R2,APUELS
(01598) CMPZP R2,XDATAS
(01599) *!!!!!!
(01600) * THIS ERROR THAT MAY BE FORGOTTEN, DELETED FOR DEBUG 5/17/80
(01601) * JMP #P2S,FO * BECAUSE OCCURS SINCE THE SAME RUF IS USED TWICE
(01602) *!!!!!!
(01603) MOVZP R2,XDATAS
(01604) MOVZP R2,SEN2
(01605) MOVZP R4,SEN1-1
(01606) CMPZP APUELS
(01607) :
(01608) JMP #P2S,FO
(01609) *
(01610) MOVZP 1,SEN2
(01611) :
(01612) MOVZP R2,R4,SEN2S
(01613) SMPSL 0,APDUELS

```

FROM SEN=2...
TO SEN=1
FROM MRB=1...
TO MPR=2
HAS APDUELS HAPPENED?
NO, HOP TO EVEN LOC AFTER
R RET

*STORE APUELS TEMP. IN R2
*COMPARE R2 WITH XDATA
*!!!!!!
* THIS ERROR THAT MAY BE FORGOTTEN, DELETED FOR DEBUG 5/17/80
* JMP #P2S,FO * BECAUSE OCCURS SINCE THE SAME RUF IS USED TWICE
*!!!!!!
*RESET XDATA

WHICH BUFFER SHOULD THE
SERIALIZED DATA GO?
IF APUELS=0, LOAD SEN 1
MAKE SYNC BIT 1 FOR BUFFER
R 2
HAS APDUELS HAPPENED?

NO, HOP TO EVEN LOC AFTE
R HFT

0A30A 2021 (0161A) HOP #1+34
0A30A 0800 (0161B) EVEN
0A30C CC000580 (0161F) MOVZM APPDEF15
0A30F F1109F0C (0161H) STORF R1,R15AV
0A3A0 F1209F04 (01619) STORF R2,R25AV
0A3A2 F1309F10 (01620) STORF R3,R35AV
0A3A3 F1409F12 (01621) STORF R4,R45AV
0A3A6 F1509F14 (01622) STORF R5,R55AV
0A3A8 F1609F16 (01623) STORF R6,R65AV
0A3AA F1709F18 (01624) STORF R7,R75AV
0A3AC 0870 (01625) RFT
0A3AD 0800 (01626) EVEN
0A3AF AF109F0C (01627) LOAD R1,R15AV
0A3B0 AF209F04 (01628) LOAD R2,R25AV
0A3B2 AF309F10 (01629) LOAD R3,R35AV
0A3B3 AF409F12 (01630) LOAD R4,R45AV
0A3B6 AF509F14 (01631) LOAD R5,R55AV
0A3B8 AF609F16 (01632) LOAD R6,R65AV
0A3BA AF709F18 (01633) LOAD R7,R75AV
0A3BC 90208C48 (01634) MOVIR R2,SEN2-2+1,SEN12
0A3B4 9030003D (01635) MOVIR R4,SEN1-1
0A3C0 172416CA (01636) MOVIR F2,R3,SEN25+1,SEN12
0A3C2 DF000580 (01637) * MOVLM C1HSG2,SYSFICS
0A3C3 2021 (01638) AMR56.0,APPDEF15
0A3C5 0800 (01641) HOP #1+34
0A3C6 CC000580 (01642) EVEN
0A3C8 F1109F0C (01643) MOVZM APPDEF15
0A3CA F1209F04 (01644) STORF R1,R15AV
0A3CC F1309F10 (01645) STORF R2,R25AV
0A3CE F1409F12 (01646) STORF R3,R35AV
0A3D0 F1509F14 (01647) STORF R4,R45AV
0A3D2 F1609F16 (01648) STORF R5,R55AV
0A3D4 F1709F18 (01649) STORF R6,R65AV
0A3D6 0870 (01650) STORF R7,R75AV
0A3D7 0800 (01651) EVEN
0A3D8 AF109F0C (01652) LOAD R1,R15AV
0A3DA AF209F04 (01653) LOAD R2,R25AV
0A3DC AF309F10 (01654) LOAD R3,R35AV
0A3DE AF409F12 (01655) LOAD R4,R45AV
0A3E0 AF509F14 (01656) LOAD R5,R55AV
0A3E2 AF609F16 (01657) LOAD R6,R65AV

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R HFT

```

0A313 20200F18 (01658)
0A316 00200F04 (01659)
0A318 0000003C (01660)
0A31A 02200F36 (01661)
0A31C 00000040 (01662)
0A31E 2021 (01663)
0A31F 0000 (01664)
0A320 CC000580 (01665)
0A322 F1309F0C (01666)
0A324 F1209F0E (01667)
0A326 F1309F10 (01668)
0A328 F1409F12 (01669)
0A32A F1509F14 (01670)
0A32C F1609F16 (01671)
0A32E F1709F18 (01672)
0A330 0E70 (01673)
0A332 0800 (01674)
0A334 CC000580 (01675)
0A336 F1309F0C (01676)
0A338 F1409F0E (01677)
0A33A F1509F10 (01678)
0A33C F1609F12 (01679)
0A33E F1709F14 (01680)
0A340 F1809F16 (01681)
0A342 F1909F18 (01682)
0A344 CC000C00 (01683)
0A346 02200F14 (01684)
0A348 00000040 (01685)
0A34A 2021 (01686)
0A34C 0800 (01687)
0A34E CC000580 (01688)
0A350 F1309F0C (01689)
0A352 F1409F0E (01690)
0A354 F1509F10 (01691)
0A356 F1609F12 (01692)
0A358 F1709F14 (01693)
0A35A F1809F16 (01694)
0A35C F1909F18 (01695)
0A35E 0E70 (01696)
0A360 0800 (01697)

```

```

LOAD R7,R7SAV
MOVZM R7,SPN12
MOVZM R4,SPN12
MOVZM R2,R2SAV
MOVZM R3,R3SAV
MOVZM R5,R5SAV
MOVZM R6,R6SAV
MOVZM R7,R7SAV
RFT
EVEN
MOVZM AP00FELS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV
STORE R7,R7SAV
RFT
EVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
MOVZM R2,AP00FELS
JMP XMITSE
MOVZM SPNS
MOVZM R2,R4,SPN15
MOVZM R4,AP00FELS
MOVZM R1,R1SAV
MOVZM R2,R2SAV
MOVZM R3,R3SAV
MOVZM R4,R4SAV
MOVZM R5,R5SAV
MOVZM R6,R6SAV
MOVZM R7,R7SAV
RFT
EVEN

```

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTER
R RFT

STORE AFLG TEMP. IN R2
END OF "TRANSMITTER"
MAKE SYNC HIT 0 FOR RUFF
FR 1

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTER
R RFT

```

0A324 A6109F0C (01702)
0A340 A6209F0F (01703)
0A412 A6309F10 (01704)
0A434 A6409F12 (01705)
0A436 A6509F14 (01706)
0A444 A6609F16 (01707)
0A446 A6709F18 (01708)
0A43C 90209F0F (01709)
0A436 90309F10 (01710)
0A440 90409F12 (01711)
0A442 90509F14 (01712)
0A444 90609F16 (01713)
0A446 90709F18 (01714) ;
0A435 0000 (01715)
0A436 00009F0C (01716)
0A438 00009F0F (01717)
0A440 00009F10 (01718)
0A442 00009F12 (01719)
0A444 00009F14 (01720)
0A446 00009F16 (01721)
0A448 00009F18 (01722)
0A450 00009F1A (01723)
0A452 00009F1C (01724)
0A454 00009F1E (01725)
0A456 00009F20 (01726)
0A458 00009F22 (01727)
0A45A 00009F24 (01728)
0A45C 00009F26 (01729)
0A45E 00009F28 (01730)
0A460 00009F2A (01731)
0A462 00009F2C (01732)
0A464 00009F2E (01733)
0A466 00009F30 (01734)
0A468 00009F32 (01735)
0A46A 00009F34 (01736)
0A46C 00009F36 (01737)
0A46E 00009F38 (01738) ;
0A464 0000 (01739)
0A470 00009F0C (01740)
0A472 00009F0F (01741)
0A474 00009F10 (01742)
0A476 00009F12 (01743)
0A478 00009F14 (01744)
0A47A 00009F16 (01745)

```

HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RFT

```

LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
MOVIR R2,SENS-2+1,SEN12
MOVIR R3,1,SEN1-1
MOVIR R2,R4,SEN15+1,SEN12
SENSI,0,APDNFLS
HOP #1,+34
FVFT
MOVIR APDNFLS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV
STORE R7,R7SAV
RFT
FVFT
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
MOVIR R2,SENS-2+2+1,SEN12
MOVIR R4,1,SEN1-2
MOVIR R2,R4,SEN15+2+1,SEN12
SENSI,0,APDNFLS
HOP #1,+34
FVFT
MOVIR APDNFLS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV

```

HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RFT

PAGE 34: PRGCG, CSFUND.TAI

```
0A47C 41604F16 (01746)
0A47D 41704F14 (01747)
0A480 0470 (01748)
0A481 0400 (01749)
0A482 A6104F0C (01750)
0A483 A6204F0B (01751)
0A486 A6304F10 (01752)
0A488 A6404F12 (01753)
0A48A A6504F14 (01754)
0A48C A6604F16 (01755)
0A48E A6704F18 (01756)
0A490 2000 (01757) XMITSE
0A491 0200 (01758)
0A492 04C0 (01759)
0A494 80004F30 (01760)
0A495 0000 (01761)
0A496 0000 (01762)
```

```
*** FOR DEHUG ONLY ***
FROM MPR=2...
TO MPR=1
RETURN TO XMIT UPON THE
NEXT G3 INTERRUPT
```


PAGE NO: PROG. CSPDSP.TXT

```
0A495 0400 (01763) *
0A496 C000514 (01764) *
0A497 8020581 (01765) *
0A498 8020581 (01766) *
0A499 8020581 (01767) *
0A49A 8020581 (01768) *
0A49B 8020581 (01769) *
0A49C 8020581 (01770) *
0A49D 8020581 (01771) *
0A49E 8020581 (01772) *
0A49F 8020581 (01773) *
0A4A0 8020581 (01774) *
0A4A1 8020581 (01775) *
0A4A2 8020581 (01776) *
0A4A3 8020581 (01777) *
0A4A4 8020581 (01778) *
0A4A5 8020581 (01779) *
0A4A6 8020581 (01780) *
```

CLEAR RCVR GO FLAG
TRIL "TIMER" FL THAT CSP
U RCVR CODE IS ON
RESET THE SYNTHESIZER RE
ADY FLAG

0A526 A0309F12 (01869)	LOAD R4,R4SAV	
0A528 A0509F14 (01870)	LOAD R5,R5SAV	
0A52A A0609F16 (01871)	LOAD R6,R6SAV	
0A52C A0709F18 (01872)	LOAD R7,R7SAV	
0A52E 8000A502 (01873)	JMP FL17S	
0A530 80201146 (01874)	MOVW R2,RECVIS-2	SET UP TO UNLOAD RECVI
0A532 07288F40 (01875)	MOVW R2,R4,REIS	HAS ADDONE HAPPENED?
0A534 0E000580 (01876)	SMOVL 0,ADDONEFLS	
0A536 2005 (01877)	ROP #1+6	
0A538 0800 (01878)	EVEN	
0A53A 0E000580 (01879)	MOVWZM APONEFLS	
0A53C 0E70 (01880)	RET	
0A53E 0800 (01881)	EVEN	
0A540 80201270 (01882)	MOVW R2,RECVIS-2+1,SEN22	
0A542 80400040 (01883)	MOVW R4,1,SEN2-1	
0A544 17288F7A (01884)	MOVW R2,R4,REIS+1,SEN22	
0A546 0E000580 (01885)	SMOVL 0,ADDONEFLS	HAS ADDONE HAPPENED?
0A548 2005 (01886)	ROP #1+6	
0A54A 0800 (01887)	EVEN	
0A54C 0E000580 (01888)	MOVWZM APONEFLS	
0A54E 0E70 (01889)	RET	
0A550 0800 (01890)	EVEN	
0A552 8020127A (01891)	MOVW R2,RECVIS-2+2+1,SEN22	
0A554 80400040 (01892)	MOVW R4,1,SEN2	
0A556 17288F7A (01893)	MOVW R2,R4,REIS+2+1,SEN22	
0A558 0E000580 (01894)	SMOVL 0,ADDONEFLS	HAS ADDONE HAPPENED?
0A55A 2005 (01895)	ROP #1+6	
0A55C 0800 (01896)	EVEN	
0A55E 0E000580 (01897)	MOVWZM APONEFLS	
0A560 0E70 (01898)	RET	
0A562 0800 (01899)	EVEN	
0A564 80201365 (01900)	MOVW R2,RECVIS-2+3+1,SEN22+1	
0A566 0E000580 (01901)	MOVW R4,1,SEN-1	
0A568 17288F7A (01902)	MOVW R2,R4,REIS+3+1,SEN-1	
0A56A 0E000580 (01903)	SMOVL 0,ADDONEFLS	MOVE LAST HALF WORD
0A56C 2005 (01904)	ROP #1+6	
0A56E 0800 (01905)	EVEN	
0A570 0E000580 (01906)	MOVWZM APONEFLS	
0A572 0E70 (01907)	RET	
0A574 0800 (01908)	EVEN	
0A576 80201365 (01909)	MOVW R2,RECVIS-2+4+1,SEN-1	
0A578 0E000580 (01910)	MOVW R4,1,SEN-1	
0A57A 17288F7A (01911)	MOVW R2,R4,REIS+4+1,SEN-1	
0A57C 0E000580 (01912)	SMOVL 0,ADDONEFLS	TO BOTH BUFFERS
0A57E 2005 (01913)	ROP #1+6	HAS ADDONE HAPPENED?
0A580 0800 (01914)	EVEN	
0A582 0E000580 (01915)	MOVWZM APONEFLS	
0A584 0E70 (01916)	RET	

```

0A567 0600 (01913)
0A568 902011F6 (01914)
01915 ;
0A56A 9030003C (01916)
0A56C 172891F2 (01917)
0A56E 04000580 (01918)
0A570 2005 (01919)
0A571 0800 (01920)
0A572 CC000580 (01921)
0A574 0670 (01922)
0A575 0800 (01923)
0A576 90201270 (01924)
0A578 9040003C (01925)
0A57A 172891DC (01926)
0A57C 14000580 (01927)
0A57E 2005 (01928)
0A57F 0800 (01929)
0A580 C1000580 (01930)
0A582 0670 (01931)
0A583 0800 (01932)
0A584 902012FA (01933)
0A586 9030003C (01934)
0A588 17289256 (01935)
0A58A 04000580 (01936)
0A58C 2005 (01937)
0A58D 0800 (01938)
0A58F CC000580 (01939)
0A590 0670 (01940)
0A591 0800 (01941)
0A592 0280 (01942)
0A593 06A0 (01943)
0A594 90100F80 (01944)
01945 *SYNC CHECK AND UPDATE, SEARCH IF NECESSARY.
01946 ; R1 - PTR TO THE PNODEM BUFFER BEING SEARCHED
01947 ; P2 - OFFSET INTO THE PNODEM, RSSPF, RSSSS BUFFERS
01948 ; P3 - SEARCH AC
01949 ; P4 - MAXCNT, THE MAX. OF RSSSS(I)
01950 ; P5 - RMAX, THE # OF INSTANCES OF MAXCNT IN RSSSS(I)
01951 ; P6 - IPAX, THE FRAME OFFSET WHERE MAXCNT WAS FOUND
01952 ; VARIANTE;
01953 ; SYNC - OBTAINING OF FRAME OFFSET TO THE SYNC WORD IN THE
01954 ; PNODEM BUFFER
01955 ;
01956 ;
0A594 90100F80 (01955)
01956 ;

```

MOVE SAME BLOCKS TO RUF
3

HAS APDONE HAPPENED?

HAS APDONE HAPPENED?

HAS APDONE HAPPENED?

FROM SRH=2...
TO SRH=1

PREPARE TO USE RFI AS NE
W BUFFER

```

0A500 02000504 (01950)  CPMZ, RDATA$
0A501 0010A50C (01950)  JNP SCURIS, P0
0A502 0010A50F (01950)  MOVIR P1, R25
0A503 02000518 (01960)  CPMZ, RSYN
      (01961) ?
      (01962)
      (01963)
0A504 0010A50E (01963)  JNP RMISLS, P0Z
0A505 0010A50F (01963)  JNP RMISLP, P1Z
0A506 0000A50E (01965)  JNP RMISLP, P1Z
0A507 00000001 (01966)  I*SR0=1
0A508 00000004 (01967)  L*TH=4
0A509 0000000A (01968)  AC*TH=10
0A510 00000111 (01969)  W*LT*H=169
      (01970)
      (01971)
0A511 003104FF (01971)  RMISLS: MOVIR R3, -1
0A512 00300518 (01972)  MOVIR R3, RSYN
0A513 0010A50F (01973)  RMISLP: CALL R1, SYNIN
0A514 00000406 (01974)  JNP R*CVSRA
0A515 0000A504 (01975)  RMISLP: JNP SYNCR
      (01976) ? SYNIN: INITIALIZE BUFFERS, ETC. FOR SYNC SEARCHING. CALL WITH
      (01977) ? P1 POINTING TO RMDEM BUFFER.
      (01978) ? ALSO ZEROS THE ENTIRE "LAST" BITS BUFFER.
      (01979)
      (01980) SYNIN: MOVIR R2, W*LT*H-1
      (01981) ?
      (01982)
0A516 0014 (01982)  ADDR R1, R2
0A517 00320001 (01983)  STSLP: MOVIR R3, R1, 1
      (01984) ?
      (01985)
0A518 00330935 (01985)  MOVIR R3, RMSSPF(R2)
      (01986) ?
      (01987)
0A519 0014 (01987)  DECF R1, 1
0A520 0020A504 (01988)  DNP R2, STSLP
0A521 0020A518 (01989)  MOVIR R2, RSYN
      (01990) ?
      (01991)
0A522 0040091C (01991)  STSHMO: MOVIR R4, RMSSS-WS
0A523 000810C4 (01992)  MOVIR R4, RMSSS
0A524 00200000 (01993)  MOVIR R2, W*LT*H/2
0A525 0044091C (01994)  MOVIR R4, R2, RMSSS+WS
0A526 0014 (01995)  RETIRN
      (01996) ?
      (01997) ?
      (01998) ? SYNCR: SEARCH INCOMING RMDEM BUFFER FOR SYNC, AND IF SYNC
      (01999) ? IS FOUND, SET RSYN TO 41 AND SET SYNC TO THE FRAME OFFSET
      (02000) ? CORRESPONDING TO THE LOGICAL START OF THE FRAME.

```

CHECK ASSUMPTION

NON ZERO MEANS USE R*P2
 ?WHAT DO WE KNOW ABOUT S
 YNC?

?0 MEANS NIL, SO INIT
 ?-1 MEANS WE LACK IT, SO
 SYNC OK, SO COPY BITS

?SAY WE'VE LOST IT

?BUFFER OFFSET = LAST W0
 RD IN BUFFER

?PICK UP DATA BIT (BIT 0
)
 ? AND STASH IT IN RMSSPF(
 1)

?SET RSYN=-1 TO MEAN SFA
 RCH FOR SYNC
 ?CLEAR THE RMSSS BUFFER


```

(02045) ?
(02046)
(02047) ?
(02048)
(02049) ?
(02050)
(02051) ?
(02052)
(02053)
(02054)
(02055) ?
(02056)
(02057) ?
(02058)
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(02073) ?
(02074)
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(02076)
(02077) ?
(02078)
(02079)
(02080) ?
(02081)
(02082) ?
(02083)
(02084)
(02085) ?
(02086)
(02087) ?
(02088)

; NEW MAXCNT
; MAX <= 1 (UNIQUE SO FA
R)
; AND WE SAVE THE OFFSET
IN IMAX

; MAXCNT = RSSSS(1)?
; YES, SO THE MAX ISN'T U
NIQUE

; CLEAR RSSSS(1)
; SAVE NEW DATA HIT IN RS
SPF(1)
; DECR PMODEM HUFFER PTR
; DECR OFFSET AND TEST IF
THE SYNC
; MAXCNT >= AC0TH?
; NO
; YES: IS NMAX=1?
; NO
; YES: WE HAVE SYNC. SET
RSYN TO +1
; SAVE OFFSET IN SYNC5
; INIT FRSYN TO COUNT ERR
ORS
SET # OF FRAMES TO WAIT
BEFORE PUTTING OUT SPEECH
SET FIRST FRAME AFTER SY
NC FLAG
; FOR SUPDAT NEXT FRAMF
; R1 POINTS TO WORD HOUDE
NC SYNC
; PICK UP SYNC HIT AND SA
VE IT IN
; OL5YN FOR USE IN SUPDAT

```



```

0A628 00005760 (02080) ; JMP RCVSYN
(02090) ;
(02091) ;
0A629 00200576 (02092) SSS00 ;
(02093) ;
0A62C 00000576 (02094) ; JMP RCVSYN
(02095) ;
(02096) ;
(02097) ;
(02098) ;
(02099) ;
(02100) ;
(02101) ;
(02102) ;
(02103) ;
(02104) ;
(02105) ;
(02106) ;
(02107) ;
(02108) ;
(02109) ;
(02110) ;
0A62E 00100577 (02111) SUBCAT ADDR R1,SYNCS
(02112) ;
0A630 00A00650 (02113) ; MOVSR R2,00052
(02114) ;
0A632 0420057A (02115) ; XORR R2,01,SYN
0A634 2C04 (02116) ; SRRS 0,R2
(02117) ;
0A635 200C (02118) ; RCP SUSERR
0A636 0200057B (02119) ; CMRZ LSTR
0A638 0110063E (02120) ; JMP SUSERR,R2
0A63A 90400004 (02121) ; MOVIR R3,LS10K
0A63C 0040057C (02122) ; MOVPR R3,PRSYN
(02123) ;
0A63E 0000057D (02124) SUSLEF MOVZM LSTR
(02125) ;
0A640 2009 (02126) ; RCP SUSVAL
(02127) ;
0A641 0800 (02128) ; FPR
0A642 90200001 (02129) SUSLEF MOVIR R2,1
(02130) ;
0A644 0020057E (02131) ; MOVPR R2,1,LS10K
0A646 0400057C (02132) ;

```

; NO SYNC YET: SET TO -1
 (=SEARCH)
 ; FOR GOOD MEASURE

; UPDATE: CHECK MODEM BUFFER TO SEE IF THE DATA HIT AT THE
 EXPECTED SYNC POSITION (START OF BUFFER+SYNCS) HAS THE EXPECTED
 VALUE. IF THERE ARE LSTR ERRORS WITHOUT 2 CONSECUTIVE CORRECT
 COMPAREMENTS, CLEAR RSYN TO SIGNIFY LOSS OF FRAME SYNCHRONIZATION.
 ; ENTER WITH R1 POINTING TO THE LATEST MODEM BUFFER. ALSO, THE
 WORD SYNC HOLDS THE OFFSET IN THE FRAME THAT POINTS TO THE
 SYNC WORD TO BE VERIFIED.
 ; CALLED FROM FIVE MODEM INTERRUPT SERVICE ROUTINE.

; VARIABLES:
 SYNCS = BEGINNING OF FRAME OFFSET
 OLSTR = SYNC HIT FROM PREVIOUS FRAME
 LSTR = RZ IF SYNC ERROR ON PREVIOUS FRAME
 PRSYN = COUNTS SYNC ERRORS (FROM LSTR DOWN TO 0)
 FPR

; R1 NOW POINTS TO NEW SY
 NC WORD
 0(2) INDEXED BY BUFN+SYN
 CS
 ; XOR WITH LAST SYNC HIT
 ; SKIP IF DATA BIT SET (G
 OOD SYNC)

; WAS THERE AN ERROR LAST
 ; YES
 ; NO:
 ; CORRECT FRAMES, SO RES
 ET ERROR COUNT
 ; REMEMBER NONERROR FOR N
 EXT FRAME

; CUM HERE ON SYNC HIT E
 RROF
 ; REMEMBER ERROR FOR NEXT
 ; HAVE WE COUNTED LSTR F

```

00610 01200000 (02143) ; JOP SUSLS, R2
00611 01200000 (02144) ;
00612 01200000 (02145) ;
00613 01200000 (02146) SUSVAL MOVIE R1, R2+R3
00614 01200000 (02147) ;
00615 01200000 (02148) ;
00616 01200000 (02149) ;
00617 01200000 (02150) ;
00618 01200000 (02151) ;
00619 01200000 (02152) ;
00620 01200000 (02153) ;
00621 01200000 (02154) ;
00622 01200000 (02155) ;
00623 01200000 (02156) ;
00624 01200000 (02157) ;
00625 01200000 (02158) ;
00626 01200000 (02159) ;
00627 01200000 (02160) ;
00628 01200000 (02161) ;
00629 01200000 (02162) ;
00630 01200000 (02163) ;
00631 01200000 (02164) ;
00632 01200000 (02165) ;
00633 01200000 (02166) ;
00634 01200000 (02167) ;
00635 01200000 (02168) ;
00636 01200000 (02169) ;
00637 01200000 (02170) ;
00638 01200000 (02171) ;
00639 01200000 (02172) ;
00640 01200000 (02173) ;
00641 01200000 (02174) ;
00642 01200000 (02175) ;
00643 01200000 (02176) ;

```

```

PROPS?
YES, THEREFORE WE'VE LO
ST SYNC
; COMPLEMENT OLSYN IN PRE
PARATION
; FOR USE NEXT FRAME

0(2) INDEXED BY R1

FROM SRH=2...
TO SRH=1
FROM MRH=2...
TO MRH=1

NEW DATA IN RUFF 2, SO R
EAD RUFF1+SYNC

RUFF1(RUFF3) NEW, SO REA
D RUFF2+SYNC

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

```


0A6C4 0800	(02221)	EVF	LOAD R1,R1SAV
0A6D0 A6109F0C	(02222)		LOAD R2,R2SAV
0A6E2 A6209F0F	(02223)		LOAD R3,R3SAV
0A6E3 A6309F10	(02224)		LOAD R4,R4SAV
0A6E6 A6309F12	(02225)		LOAD R5,R5SAV
0A6E8 A6509F1A	(02226)		LOAD R6,R6SAV
0A6EA A6609F16	(02227)		LOAD R7,R7SAV
0A6EC A6709F18	(02228)		MOVW R4,LSF01-1
0A6E5 A6400040	(02229)		MOVW R2,R4,RECVS+4*LSF01
0A6E0 D5289F0F	(02230)		SMRSL 0,APUNFELS
0A6E2 D600580	(02231)		HOP R1,34
0A6E4 2021	(02232)		
0A6E5 0800	(02233)		
0A6E6 C000580	(02234)	EVF	MOVW APUNFELS
0A6E8 A6109F0C	(02235)		STORE R1,R1SAV
0A6EA A6209F0F	(02236)		STORE R2,R2SAV
0A6EC A6309F10	(02237)		STORE R3,R3SAV
0A6EE A6409F12	(02238)		STORE R4,R4SAV
0A6F0 A6509F14	(02239)		STORE R5,R5SAV
0A6F2 A6609F16	(02240)		STORE R6,R6SAV
0A6F4 A6709F18	(02241)		STORE R7,R7SAV
0A6F6 0F70	(02242)		RT
0A6F7 0800	(02243)	EVF	
0A6F8 A6109F0C	(02244)		LOAD R1,R1SAV
0A6FA A6209F0F	(02245)		LOAD R2,R2SAV
0A6FC A6309F10	(02246)		LOAD R3,R3SAV
0A6FE A6409F12	(02247)		LOAD R4,R4SAV
0A700 A6509F14	(02248)		LOAD R5,R5SAV
0A702 A6609F16	(02249)		LOAD R6,R6SAV
0A704 A6709F18	(02250)		LOAD R7,R7SAV
0A706 90400030	(02251)		MOVW R4,LSF01-1
0A708 D5289F0F	(02252)		MOVW R2,R4,RECVS+4*LSF01
0A70A D600580	(02253)		SMRSL 0,APUNFELS
0A70C 2021	(02254)		HOP R1,34
0A70D 0800	(02255)		
0A70E C000580	(02256)		
0A710 A6109F0C	(02257)	EVF	MOVW APUNFELS
0A712 A6209F0F	(02258)		STORE R1,R1SAV
0A714 A6309F10	(02259)		STORE R2,R2SAV
0A716 A6409F12	(02260)		STORE R3,R3SAV
0A718 A6509F14	(02261)		STORE R4,R4SAV
0A71A A6609F16	(02262)		STORE R5,R5SAV
0A71C A6709F18	(02263)		STORE R6,R6SAV

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

```

0A710 F1100F1M (02265)
0A711 0E70 (02266)
0A712 0800 (02267)
0A713 A6100F0C (02268)
0A714 A6200F0B (02269)
0A715 A6300F10 (02270)
0A716 A6400F12 (02271)
0A717 A6500F14 (02272)
0A718 A6600F16 (02273)
0A719 A6700F18 (02274)
0A720 0000003A (02275)
0A721 0520007A (02276)
0A722 0E000050 (02277)
0A723 0C21 (02278)
0A724 0800 (02279)
0A725 C0000580 (02280)
0A726 F1100F0C (02281)
0A727 F1200F0E (02282)
0A728 F1300F10 (02283)
0A729 F1400F12 (02284)
0A730 F1500F14 (02285)
0A731 F1600F16 (02286)
0A732 F1700F18 (02287)
0A733 0E70 (02288)
0A734 0800 (02289)
0A735 A6100F0C (02290)
0A736 A6200F0B (02291)
0A737 A6300F10 (02292)
0A738 A6400F12 (02293)
0A739 A6500F14 (02294)
0A740 A6600F16 (02295)
0A741 A6700F18 (02296)
0A742 A6800F1A (02297)
0A743 0000A7SR (02298)
0A744 0000A7SR (02299)
0A745 0000A7SR (02300)

;
FVEN
MOVZM ADDRPLS
STORE R1,R1SAV
STORE R2,R2SAV
STORE R3,R3SAV
STORE R4,R4SAV
STORE R5,R5SAV
STORE R6,R6SAV
STORE R7,R7SAV
RET
FVEN
LOAD R1,R1SAV
LOAD R2,R2SAV
LOAD R3,R3SAV
LOAD R4,R4SAV
LOAD R5,R5SAV
LOAD R6,R6SAV
LOAD R7,R7SAV
JMP DSRNDFS NOT ENOUGH TIME TO CORRECT
JMP RCHIDDS
EJECT

```

HAS APDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

CORRECT SIDERAND

```

(02401) ***** RCHD DECODING OF SLIPHAND & PART OF MAINHAND DATA
(02402) RCHD05 EVEN
0A75A 00000001 (02403)
0A75A 0000000A (02404)
0A75A 00000000 (02405)
0A75A 00000000 (02406)
0A75A 00000000 (02407)
0A75A 00000000 (02408)
0A75A 00000000 (02409)
0A75A 00000000 (02410)
0A75A 00000000 (02411)
0A75A 00000000 (02412)
0A75A 00000000 (02413)
0A75A 00000000 (02414)
0A75A 00000000 (02415)
0A75A 00000000 (02416)
0A75A 00000000 (02417)
0A75A 00000000 (02418)
0A75A 00000000 (02419)
0A75A 00000000 (02420)
0A75A 00000000 (02421)
0A75A 00000000 (02422)
0A75A 00000000 (02423)
0A75A 00000000 (02424)
0A75A 00000000 (02425)
0A75A 00000000 (02426)
0A75A 00000000 (02427)
0A75A 00000000 (02428)
0A75A 00000000 (02429)
0A75A 00000000 (02430)
0A75A 00000000 (02431)
0A75A 00000000 (02432)
0A75A 00000000 (02433)
0A75A 00000000 (02434)
0A75A 00000000 (02435)
0A75A 00000000 (02436)
0A75A 00000000 (02437)
0A75A 00000000 (02438)
0A75A 00000000 (02439)
0A75A 00000000 (02440)
0A75A 00000000 (02441)
0A75A 00000000 (02442)
0A75A 00000000 (02443)
0A75A 00000000 (02444)

```

SET INPUT BUFFER POINTER
CLEAR FPROR COUNTERS(R1,
R2)
HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

R5=4 OF POWER SUM TABLE
CLEAR POWER SUM S1
CLEAR POWER SUM S3
CLEAR POWER SUM S5

READ INPUT DATA

HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET


```

0A704 1054 (02380)  MOVW R5,R2
0A705 1056 (02390)  ADDW R5,R3
0A706 1058 (02391)  ADDW R5,R4
0A707 0800 (02392)  EVEN
0A708 0010A004 (02393)  JWP RCHDAS,F0
0A709 00000000 (02394)  CALCULATE 10T3+R7=S1**3+5,3=R2**3+R3
0A710 00000000 (02395)  MOVW R5,R2
0A711 0000 (02396)  EVEN
0A712 0000 (02397)  JWP RCHDAS,F0
0A713 0010A000 (02398)  MOVW R7,THRPCS(R2)
0A714 0010A000 (02399)  MOVW R6,R7
0A715 0000 (02400)  ADDW R6,R6
0A716 0000 (02401)  SKPL LT
0A717 00000000 (02402)  SUBW R6,R6
0A718 1A30 (02403)  EVEN
0A719 00000000 (02404)  MOVW R6,S1SPS
0A720 00000000 (02405)  ADDW R6,R7
0A721 0000 (02406)  EVEN
0A722 00000000 (02407)  CMPL R6,R6
0A723 1A30 (02408)  SKPL LT
0A724 00000000 (02409)  SUBW R6,R6
0A725 00000000 (02410)  MOVW R6,R6
0A726 1A30 (02411)  SKPL LT
0A727 00000000 (02412)  SUBW R6,R6
0A728 00000000 (02413)  PEAD FPC TABLE FOR S1**3
0A729 0000 (02414)  EVEN
0A730 00000000 (02415)  MOVW R7,THRPCS(R6)
0A731 00000000 (02416)  MOVW R7,R3
0A732 0000 (02417)  EVEN
0A733 0010A000 (02418)  JWP RCHDAS,F0
0A734 00000000 (02419)  MORE THAN TWO ERRORS OCCUR
0A735 00000000 (02420)  CALCULATE SIGMA2 AND SIGMA3
0A736 00000000 (02421)  CALCULATE S1**2+S3+S5 FOR SIGMA2 CALC.
0A737 0000 (02422)  TEST R2
0A738 1930 (02423)  SKP RF
0A739 0000 (02424)  CLP R3
0A740 0000 (02425)  TEST R3
0A741 0010A000 (02426)  JWP RCHDAS,F0
0A742 00000000 (02427)  MOVW R5,THRPCS(R3)
0A743 00000000 (02428)  ADDW R5,S1SPS
0A744 00000000 (02429)  CMPL R5,R6
0A745 1A30 (02430)  SKPL LT
0A746 00000000 (02431)  SUBW R5,R6
0A747 00000000 (02432)  PEAD FPC TABLE

```

NO ERROR IF P5=0

CLEAR ERROR COUNTER

CHECK R6<63

STORE POWER INDEX OF S1*
#2

R7=S1**3+S3

GO TO RCHDAS IF R7=0


```

0A850 F03A9F6A (02477)  MOVW R4,THAL2S(R5)
0A851 441E (02478)  MOVW R4,R7
0A852 6A00 (02479)  EVEN
0A853 801A80 (02480)  JMP RCHDUS,R0
0A854 F009FEC (02481)  ; 3 OR MORE ERRORS OCCUP
0A855 9051F4C2 (02482)  MOVW R5,PCSS
0A856 4064 (02483)  MOVW R5,-R2
0A857 4466 (02484)  ; CHECK TOP FIRST POSITION
0A858 4466 (02485)  MOVW R6,R2
0A859 4466 (02486)  MOVW R6,R3
0A860 4466 (02487)  MOVW R6,R4
0A861 2761 (02488)  DECR R6,1
0A862 8110A66A (02489)  JMP RCHDUS,R0
0A863 F009FEC (02490)  ; STORE ERROR POSITION
0A864 F0104FED (02491)  INCM R5TS
0A865 F0104FED (02492)  MOVW R1,PCSS+1
0A866 F0249438 (02493)  ; MULTIPLY ALPHA TO R2
0A867 F03A9F6A (02494)  MOVW R2,THAL2S(R2)
0A868 F0489F6A (02495)  ; MULTIPLY ALPHA TO R3
0A869 4064 (02496)  MOVW R3,THAL2S(R3)
0A870 4466 (02497)  MOVW R4,THAL2S(R4)
0A871 4466 (02498)  MOVW R6,R2
0A872 4466 (02499)  MOVW R6,R3
0A873 2761 (02500)  DECR R6,1
0A874 8110A674 (02501)  JMP RCHDUS,R0
0A875 F009FEC (02502)  ; STORE ERROR POSITION
0A876 407A (02503)  INCM R5TS
0A877 4C72 (02504)  MOVW R7,R5
0A878 F0609F6A (02505)  ANDW R7,R1
0A879 F07C0FEC (02506)  MOVW R0,PCSS
0A880 8056A66A (02507)  MOVW R7,PCSS(R0)
0A881 F0509FEC (02508)  RCHDUS TOP R5,RCHDUS
0A882 F2509F6A (02509)  ; CHECK ERROR CORRECTION STATUS
0A883 8110A6A6 (02510)  MOVW R5,PCSS
0A884 407A (02511)  CPWM R5,PCSS
0A885 407A (02512)  MOVW R5,PCSS
0A886 90600001 (02513)  ; CORRECT 3 ERRORS
0A887 F0709F6A (02514)  MOVW R6,1
0A888 F56F9543 (02515)  MOVW R7,PCSS+1
0A889 F0709F6A (02516)  MOVW R6,PCSS-1(R7)
0A890 F56F9543 (02517)  MOVW R7,PCSS+1
0A891 F0709F6A (02518)  MOVW R6,PCSS-1(R7)
0A892 F56F9543 (02519)  MOVW R7,PCSS+1
0A893 F0709F6A (02520)  MOVW R7,PCSS+1

```

R4=R7+51*SIGMA2

TWO ERRORS IF R4=0

R7 IS THE ERROR POSITION

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```
0ARCS 0800 (02565) EVEN
0ARCS 010ARCS (02566) JMP RCH24S,IF
0ARCS 010ARCS (02567) ; CURRENT ERRORS
0ARCS 407A (02568) MOVPR P7,P5
0ARCS 4C72 (02569) ADDR P7,R1
0ARCS 96800001 (02570) MOVPR R6,1
0ARCS F56P9543 (02571) XORR R6,R6CVS-1(P7)
0ARCS 9950A00F (02572) RCH24S LJM P5,RCH25S
0ARCS 8000A004 (02573) JMP RCH0AS
0ARCS 8000A004 (02574) *
0ARCS 8000A004 (02575) *
0ARCS 8000A004 (02576) ; END OF RCH DECODING LIST
0ARCS 8000A004 (02577) FND0CS EVEN
0ARCS 8000A004 (02578) JMP DSR0IFS
0ARCS 8000A004 (02579) EXIT
```


0A906 F1200400 (02621)	STORE R2,R2SAV	SKIP IF THE REC'D HIT IS
0A910 F1200410 (02625)	STORE R3,R3SAV	SET HIT 5 OF PARM TO RE
0A912 F1200412 (02626)	STORE R4,R4SAV	A 1
0A913 F1200413 (02627)	STORE R5,R5SAV	INCREMENT R2 BY 1
0A914 F1200414 (02628)	STORE R6,R6SAV	
0A916 F1200416 (02629)	STORE R7,R7SAV	
0A918 F1200418 (02630)	SET	
0A91A F120041A (02631)	EVFN	
0A91B F120041B (02632)	LOAD R1,R1SAV	
0A91C F120041C (02633)	LOAD R2,R2SAV	
0A91E F120041E (02634)	LOAD R3,R3SAV	
0A920 F1200420 (02635)	LOAD R4,R4SAV	
0A922 F1200422 (02636)	LOAD R5,R5SAV	
0A924 F1200424 (02637)	LOAD R6,R6SAV	
0A926 F1200426 (02638)	LOAD R7,R7SAV	
0A928 F1200428 (02639)	JMP 2100428(P5)	
0A92A F120042A (02640)	EVFN	
0A92C F120042C (02641)	SMOCL 0,RFCVS(R2)	
0A92E F120042E (02642)	SMW 5,R0PHS(P4)	
0A930 F1200430 (02643)	INCR R2,1	
0A931 F1200431 (02644)	EVFN	
0A932 F1200432 (02645)	SMOCL 0,RFCVS(R2)	
0A934 F1200434 (02646)	SMW 4,R0PHS(P4)	
0A936 F1200436 (02647)	INCR R2,1	
0A937 F1200437 (02648)	EVFN	
0A938 F1200438 (02649)	SMOCL 0,RFCVS(R2)	
0A93A F120043A (02650)	SMW 3,R0PHS(P4)	
0A93C F120043C (02651)	INCR R2,1	
0A93D F120043D (02652)	EVFN	
0A93E F120043E (02653)	SMOCL 0,RFCVS(R2)	
0A93F F120043F (02654)	SMW 2,R0PHS(P4)	
0A940 F1200440 (02655)	INCR R2,1	
0A941 F1200441 (02656)	EVFN	
0A942 F1200442 (02657)	SMOCL 0,RFCVS(R2)	
0A943 F1200443 (02658)	SMW 1,R0PHS(P4)	
0A944 F1200444 (02659)	INCR R2,1	
0A945 F1200445 (02660)	EVFN	
0A946 F1200446 (02661)	SMOCL 0,RFCVS(R2)	
0A947 F1200447 (02662)	SMW 1,R0PHS(P4)	
0A948 F1200448 (02663)	INCR R2,1	
0A949 F1200449 (02664)	EVFN	
0A94A F120044A (02665)	SMOCL 0,RFCVS(R2)	
0A94B F120044B (02666)	SMW 1,R0PHS(P4)	
0A94C F120044C (02667)	INCR R2,1	
0A94D F120044D (02668)	EVFN	
0A94E F120044E (02669)	SMOCL 0,RFCVS(R2)	
0A94F F120044F (02670)	SMW 1,R0PHS(P4)	

0A944 2621	(02684)	INCR R2,1	INCREMENT R2 BY 1
0A945 0200	(02685)	EVFN	
0A946 0200	(02686)		
0A94A 0A040544	(02671) *	SMRCL 0,RECVS(R2)	SKIP IF THE REC'D HIT IS
0A94C 02000646	(02672)	SMRCL 0,ROPHS(R4)	SET HIT 0 OF ROPH TO RE
0A94D 0200	(02673) *		1
0A94E 2621	(02674)	INCR R2,1	INCREMENT R2 BY 1
0A94F 0200	(02675)	EVFN	
0A950 2631	(02676) *	INCR R4,1	INCREMENT THE PARM. COUNT
0A951 0200	(02677)	EVFN	TER BY 1
0A952 0A040544	(02678) *	INCR R5,100051	AND GOTO THE NEXT ONE
0A953 0200	(02679)		
0A954 02000646	(02680)		
0A955 0200	(02681) *		
0A956 02000646	(02682) *	ADDER R2,100051	SKIP THE NEXT 18 POSITIO
0A957 0200	(02683)		NS SINCE THESE ARE PART
0A958 02000646	(02684) *		Y BITS FOR ERROR CONTROL
0A959 0200	(02685) *		CLEAR THE PARM BUFFER
0A95A 0A040544	(02686)	ROV7A ROPHS(R4)	SKIP IF THE REC'D HIT IS
0A95B 02000646	(02687)	SMRCL 0,RECVS(R2)	SET HIT 1 OF ROPH TO RE
0A95C 0200	(02688)	SMRCL 0,ROPHS(R4)	1
0A95D 2621	(02689)	INCR R2,1	INCREMENT THE LOOP COUNT
0A95E 0200	(02690)	EVFN	ER BY ONE
0A95F 0A040544	(02691) *	SMRCL 0,RECVS(R2)	SKIP IF THE REC'D HIT IS
0A960 02000646	(02692)	SMRCL 0,ROPHS(R4)	SET HIT 0 OF ROPH TO RE
0A961 0200	(02693) *		1
0A962 2621	(02694)	INCR R2,1	INCREMENT R2 BY 1
0A963 0200	(02695)	EVFN	

FROM THESE DECODED PARAMETERS
HIT ASSIGNMENT RULE FOR DESERIALIZATION
OF HIT CODES IS COMPUTED

0A964 0200	(02696) *
0A965 0200	(02697) *
0A966 0200	(02698) *
0A967 0200	(02699) *
0A968 0200	(02700) *
0A969 0200	(02701) *
0A970 0200	(02702) *
0A971 0200	(02703) *
0A972 0200	(02704) *
0A973 0200	(02705) *
0A974 0200	(02706) *
0A975 0200	(02707) *
0A976 0200	(02708) *
0A977 0200	(02709) *
0A978 0200	(02710) *
0A979 0200	(02711) *

ADDRESS	INSTR	OPERANDS	COMMENT
00000	MOV	R0, #0	INITIALIZE R0
00001	MOV	R1, #1	INITIALIZE R1
00002	MOV	R2, #2	INITIALIZE R2
00003	MOV	R3, #3	INITIALIZE R3
00004	MOV	R4, #4	INITIALIZE R4
00005	MOV	R5, #5	INITIALIZE R5
00006	MOV	R6, #6	INITIALIZE R6
00007	MOV	R7, #7	INITIALIZE R7
00008	MOV	R8, #8	INITIALIZE R8
00009	MOV	R9, #9	INITIALIZE R9
00010	MOV	R10, #10	INITIALIZE R10
00011	MOV	R11, #11	INITIALIZE R11
00012	MOV	R12, #12	INITIALIZE R12
00013	MOV	R13, #13	INITIALIZE R13
00014	MOV	R14, #14	INITIALIZE R14
00015	MOV	R15, #15	INITIALIZE R15
00016	MOV	R16, #16	INITIALIZE R16
00017	MOV	R17, #17	INITIALIZE R17
00018	MOV	R18, #18	INITIALIZE R18
00019	MOV	R19, #19	INITIALIZE R19
00020	MOV	R20, #20	INITIALIZE R20
00021	MOV	R21, #21	INITIALIZE R21
00022	MOV	R22, #22	INITIALIZE R22
00023	MOV	R23, #23	INITIALIZE R23
00024	MOV	R24, #24	INITIALIZE R24
00025	MOV	R25, #25	INITIALIZE R25
00026	MOV	R26, #26	INITIALIZE R26
00027	MOV	R27, #27	INITIALIZE R27
00028	MOV	R28, #28	INITIALIZE R28
00029	MOV	R29, #29	INITIALIZE R29
00030	MOV	R30, #30	INITIALIZE R30
00031	MOV	R31, #31	INITIALIZE R31
00032	MOV	R32, #32	INITIALIZE R32
00033	MOV	R33, #33	INITIALIZE R33
00034	MOV	R34, #34	INITIALIZE R34
00035	MOV	R35, #35	INITIALIZE R35
00036	MOV	R36, #36	INITIALIZE R36
00037	MOV	R37, #37	INITIALIZE R37
00038	MOV	R38, #38	INITIALIZE R38
00039	MOV	R39, #39	INITIALIZE R39
00040	MOV	R40, #40	INITIALIZE R40
00041	MOV	R41, #41	INITIALIZE R41
00042	MOV	R42, #42	INITIALIZE R42
00043	MOV	R43, #43	INITIALIZE R43
00044	MOV	R44, #44	INITIALIZE R44
00045	MOV	R45, #45	INITIALIZE R45
00046	MOV	R46, #46	INITIALIZE R46
00047	MOV	R47, #47	INITIALIZE R47
00048	MOV	R48, #48	INITIALIZE R48
00049	MOV	R49, #49	INITIALIZE R49
00050	MOV	R50, #50	INITIALIZE R50
00051	MOV	R51, #51	INITIALIZE R51
00052	MOV	R52, #52	INITIALIZE R52
00053	MOV	R53, #53	INITIALIZE R53
00054	MOV	R54, #54	INITIALIZE R54
00055	MOV	R55, #55	INITIALIZE R55
00056	MOV	R56, #56	INITIALIZE R56
00057	MOV	R57, #57	INITIALIZE R57
00058	MOV	R58, #58	INITIALIZE R58
00059	MOV	R59, #59	INITIALIZE R59
00060	MOV	R60, #60	INITIALIZE R60
00061	MOV	R61, #61	INITIALIZE R61
00062	MOV	R62, #62	INITIALIZE R62
00063	MOV	R63, #63	INITIALIZE R63
00064	MOV	R64, #64	INITIALIZE R64
00065	MOV	R65, #65	INITIALIZE R65
00066	MOV	R66, #66	INITIALIZE R66
00067	MOV	R67, #67	INITIALIZE R67
00068	MOV	R68, #68	INITIALIZE R68
00069	MOV	R69, #69	INITIALIZE R69
00070	MOV	R70, #70	INITIALIZE R70
00071	MOV	R71, #71	INITIALIZE R71
00072	MOV	R72, #72	INITIALIZE R72
00073	MOV	R73, #73	INITIALIZE R73
00074	MOV	R74, #74	INITIALIZE R74
00075	MOV	R75, #75	INITIALIZE R75
00076	MOV	R76, #76	INITIALIZE R76
00077	MOV	R77, #77	INITIALIZE R77
00078	MOV	R78, #78	INITIALIZE R78
00079	MOV	R79, #79	INITIALIZE R79
00080	MOV	R80, #80	INITIALIZE R80
00081	MOV	R81, #81	INITIALIZE R81
00082	MOV	R82, #82	INITIALIZE R82


```

(02156) ;
0A90C C0000000 (02157) MOVZM R05TE
0A90E 80000000 (02158) JMP R05VSA
0A910 80000000 (02159) LOAD R4,R4SAV
0A912 80000000 (02160) MOVZM R5,R05TE
0A914 7257 (02161) DECR R5,2
0A916 0000 (02162) EVEN
0A918 80000000 (02163) MOVZM R7,R05TE
(02164) ;
0A91A 2111 (02165) DECR R7,1
0A91C 3A71 (02166) IL5 R7,1
(02167) EVEN
(02168) *****
(02169) * SPECIAL CODE TO CORRECT FOR R05TE=0 CASE 6/12/80
(02170) TEST R7
(02171) ;
(02172) ;
(02173) ;
0A91E 0000 (02174) JMP R05TE,LT
(02175) ;
(02176) *****
(02177) JMP R05TE,LT
(02178) ;
(02179) ;
(02180) ;
(02181) ;
(02182) ;
0A91A 00000000 (02183) GRIT4S
0A91C 2021 (02184) SPERSI,0,AP05TE
(02185) ;
0A91E 0000 (02186) EVEN
0A91F C0000000 (02187) MOVZM R05TE
0A921 11000000 (02188) STORE R1,R1SAV
0A923 11000000 (02189) STORE R2,R2SAV
0A925 11000000 (02190) STORE R3,R3SAV
0A927 11000000 (02191) STORE R4,R4SAV
0A929 11000000 (02192) STORE R5,R5SAV
0A92B 11000000 (02193) STORE R6,R6SAV
0A92D 11000000 (02194) STORE R7,R7SAV
0A92F 0000 (02195) RET
0A931 0000 (02196) EVEN
0A933 11000000 (02197) LOAD R1,R1SAV
0A935 11000000 (02198) LOAD R2,R2SAV
0A937 11000000 (02199) LOAD R3,R3SAV
(02200) ;

```

```

ME
RESTORE R4
MOVE FORTY FOUR IN R5

SAVE THE FIRST IBIT VALUE
DECREMENT R7 BY 1
SHIFT LEFT BY 1

WHEN R05TE=0,R7<0 AND 0
IS RC!
SO TEST & JUMP AROUND SE
RIALIZATION SINCE
R05TE=0 IMPLIES NO BITS T
O SERIALIZE!!
*****
GOTO THE CORR.DESERIALIZ
ATION ROUTINE

HAS APODNE HAPPENED?
NO, HUP TO EVEN LOC AFTE
R RET

```

0A9CC A6100F12 (02800)	LOAD R4,R4SAV	SAVE THE 1ST HIT VALUE
0A9CF A6500F19 (02801)	LOAD R5,R4SAV	
0A9D0 A6600F16 (02802)	LOAD R6,R4SAV	
0A9D2 A6700F1B (02803)	LOAD R7,R7SAV	
0A9D4 A6800F18 (02804)	MUVM R7,R7BITS	
0A9D6 A6900F15 (02805)	CLR R4	
0A9D8 A6A00F12 (02806)	EVFN	
0A9DA A6B00F19 (02807)	CMPL R7,R7BITS(R4)	
0A9DC A6C00F16 (02808)	JMP GRITIS,GT	COMPARE THE HIT ASSIGNME NT WITH THE MARK
0A9DE A6D00F13 (02809)		GO TO NEXT HIT DECODING R
0A9DF A6E00F10 (02810)		ROUTINE IF HIT ASSIGN. CH
0A9E0 A6F00F0F (02811)		ANGES
0A9E2 A7000F0D (02812)	SMRCL 0,ADDNPLS	HAS ADDNPLS HAPPENED?
0A9E4 A7100F0A (02813)	HOP R4,R4	NO, HOP TO EVEN LOC AFTE
0A9E6 A7200F07 (02814)	EVFN	R RET
0A9E8 A7300F04 (02815)	MOVZM ADDNPLS	
0A9EA A7400F01 (02816)	STORE R1,R1SAV	
0A9EC A7500F00 (02817)	STORE R2,R2SAV	
0A9EE A7600F00 (02818)	STORE R3,R3SAV	
0A9EF A7700F00 (02819)	STORE R4,R4SAV	
0A9F0 A7800F00 (02820)	STORE R5,R5SAV	
0A9F2 A7900F00 (02821)	STORE R6,R6SAV	
0A9F4 A7A00F00 (02822)	STORE R7,R7SAV	
0A9F6 A7B00F00 (02823)	RET	
0A9F8 A7C00F00 (02824)	EVFN	
0A9FA A7D00F00 (02825)	LOAD R1,R1SAV	
0A9FC A7E00F00 (02826)	LOAD R2,R2SAV	
0A9FE A7F00F00 (02827)	LOAD R3,R3SAV	
0A9FF A8000F00 (02828)	LOAD R4,R4SAV	
0A900 A8100F00 (02829)	LOAD R5,R5SAV	
0A902 A8200F00 (02830)	LOAD R6,R6SAV	
0A904 A8300F00 (02831)	LOAD R7,R7SAV	
0A906 A8400F00 (02832)	SMRCL 0,R4,CVS(R2)	SKIP IF HIT 0 OF RECV(R2) IS CLEARED
0A908 A8500F00 (02833)	SMR 4,R4,PRODUCTS(R4)	
0A90A A8600F00 (02834)	INCR R4,1	
0A90C A8700F00 (02835)	EVFN	
0A90E A8800F00 (02836)	INCR R2,1	INCREMENT THE BUFFER POS ITION COUNTER
0A910 A8900F00 (02837)	EVFN	
0A912 A8A00F00 (02838)	CLP R5,CHIT4S1	CHECK IF R5 +VE
0A914 A8B00F00 (02839)	MUVM R5,F0RFRHS	RELOAD THE COUNTER WITH
0A916 A8C00F00 (02840)		44

00000	00000000	(02000)	CMOVR R2,POSTAS	IS R2 GREATER THAN POST
00001	00000000	(02001)	IFC CMPTAS,LT	3
00002	00000000	(02002)	JMP GRIT2S,GT	ADD 1R TO R2 IF LESS THA
00003	00000000	(02003)	JMP GRIT2S	N OP EQUAL
00004	00000000	(02004)	JMP GRIT2S	GO BACK TO DITTS1
00005	00000000	(02005)	JMP GRIT2S	
00006	00000000	(02006)	JMP GRIT2S	
00007	00000000	(02007)	JMP GRIT2S	
00008	00000000	(02008)	JMP GRIT2S	
00009	00000000	(02009)	JMP GRIT2S	
00010	00000000	(02010)	JMP GRIT2S	
00011	00000000	(02011)	JMP GRIT2S	
00012	00000000	(02012)	JMP GRIT2S	
00013	00000000	(02013)	JMP GRIT2S	
00014	00000000	(02014)	JMP GRIT2S	
00015	00000000	(02015)	JMP GRIT2S	
00016	00000000	(02016)	JMP GRIT2S	
00017	00000000	(02017)	JMP GRIT2S	
00018	00000000	(02018)	JMP GRIT2S	
00019	00000000	(02019)	JMP GRIT2S	
00020	00000000	(02020)	JMP GRIT2S	
00021	00000000	(02021)	JMP GRIT2S	
00022	00000000	(02022)	JMP GRIT2S	
00023	00000000	(02023)	JMP GRIT2S	
00024	00000000	(02024)	JMP GRIT2S	
00025	00000000	(02025)	JMP GRIT2S	
00026	00000000	(02026)	JMP GRIT2S	
00027	00000000	(02027)	JMP GRIT2S	
00028	00000000	(02028)	JMP GRIT2S	
00029	00000000	(02029)	JMP GRIT2S	
00030	00000000	(02030)	JMP GRIT2S	
00031	00000000	(02031)	JMP GRIT2S	
00032	00000000	(02032)	JMP GRIT2S	
00033	00000000	(02033)	JMP GRIT2S	
00034	00000000	(02034)	JMP GRIT2S	
00035	00000000	(02035)	JMP GRIT2S	
00036	00000000	(02036)	JMP GRIT2S	
00037	00000000	(02037)	JMP GRIT2S	
00038	00000000	(02038)	JMP GRIT2S	
00039	00000000	(02039)	JMP GRIT2S	
00040	00000000	(02040)	JMP GRIT2S	
00041	00000000	(02041)	JMP GRIT2S	
00042	00000000	(02042)	JMP GRIT2S	
00043	00000000	(02043)	JMP GRIT2S	
00044	00000000	(02044)	JMP GRIT2S	
00045	00000000	(02045)	JMP GRIT2S	
00046	00000000	(02046)	JMP GRIT2S	
00047	00000000	(02047)	JMP GRIT2S	
00048	00000000	(02048)	JMP GRIT2S	
00049	00000000	(02049)	JMP GRIT2S	
00050	00000000	(02050)	JMP GRIT2S	
00051	00000000	(02051)	JMP GRIT2S	
00052	00000000	(02052)	JMP GRIT2S	
00053	00000000	(02053)	JMP GRIT2S	
00054	00000000	(02054)	JMP GRIT2S	
00055	00000000	(02055)	JMP GRIT2S	
00056	00000000	(02056)	JMP GRIT2S	
00057	00000000	(02057)	JMP GRIT2S	
00058	00000000	(02058)	JMP GRIT2S	
00059	00000000	(02059)	JMP GRIT2S	
00060	00000000	(02060)	JMP GRIT2S	
00061	00000000	(02061)	JMP GRIT2S	
00062	00000000	(02062)	JMP GRIT2S	
00063	00000000	(02063)	JMP GRIT2S	
00064	00000000	(02064)	JMP GRIT2S	
00065	00000000	(02065)	JMP GRIT2S	
00066	00000000	(02066)	JMP GRIT2S	
00067	00000000	(02067)	JMP GRIT2S	
00068	00000000	(02068)	JMP GRIT2S	
00069	00000000	(02069)	JMP GRIT2S	
00070	00000000	(02070)	JMP GRIT2S	
00071	00000000	(02071)	JMP GRIT2S	
00072	00000000	(02072)	JMP GRIT2S	
00073	00000000	(02073)	JMP GRIT2S	
00074	00000000	(02074)	JMP GRIT2S	
00075	00000000	(02075)	JMP GRIT2S	
00076	00000000	(02076)	JMP GRIT2S	
00077	00000000	(02077)	JMP GRIT2S	
00078	00000000	(02078)	JMP GRIT2S	
00079	00000000	(02079)	JMP GRIT2S	
00080	00000000	(02080)	JMP GRIT2S	
00081	00000000	(02081)	JMP GRIT2S	
00082	00000000	(02082)	JMP GRIT2S	
00083	00000000	(02083)	JMP GRIT2S	
00084	00000000	(02084)	JMP GRIT2S	
00085	00000000	(02085)	JMP GRIT2S	
00086	00000000	(02086)	JMP GRIT2S	
00087	00000000	(02087)	JMP GRIT2S	
00088	00000000	(02088)	JMP GRIT2S	
00089	00000000	(02089)	JMP GRIT2S	
00090	00000000	(02090)	JMP GRIT2S	
00091	00000000	(02091)	JMP GRIT2S	
00092	00000000	(02092)	JMP GRIT2S	
00093	00000000	(02093)	JMP GRIT2S	
00094	00000000	(02094)	JMP GRIT2S	
00095	00000000	(02095)	JMP GRIT2S	
00096	00000000	(02096)	JMP GRIT2S	
00097	00000000	(02097)	JMP GRIT2S	
00098	00000000	(02098)	JMP GRIT2S	
00099	00000000	(02099)	JMP GRIT2S	
00100	00000000	(02100)	JMP GRIT2S	

HAS APDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

SAVE THE NEXT IBIT VALUE

COMPARE THE HIT ASSIGNE
NT WITH THE MARK
IF NOT EQUAL, GOTO DECODE
HAS APDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET


```

00002 F100F06 (02942) STORE R1,R15AV
00004 F100F06 (02943) STORE R2,R25AV
00006 F100F10 (02944) STORE R3,R35AV
00008 F100F12 (02945) STORE R4,R45AV
00010 F100F14 (02946) STORE R5,R55AV
00012 F100F16 (02947) STORE R6,R65AV
00014 F100F18 (02948) STORE R7,R75AV
00016 0470 (02949) RET
00018 0800 (02950) FVFN
00020 A6100F0C (02951) LOAD R1,R15AV
00022 A6200F0F (02952) LOAD R2,R25AV
00024 A6300F10 (02953) LOAD R3,R35AV
00026 A6400F12 (02954) LOAD R4,R45AV
00028 A6500F14 (02955) LOAD R5,R55AV
00030 A6600F16 (02956) LOAD R6,R65AV
00032 A6700F18 (02957) LOAD R7,R75AV
00034 F0785A06 (02958) MOVE R7,R1R1TS(R4)
00036 0040 (02959) CLR R4
00038 0800 (02960) FVFN
00040 F7785A06 (02961) CMP R7,R1R1TS(R4)
00042 02952 ;
00044 0020A00F (02963) JRP CR11TS,GT
00046 E8000500 (02964) SWBSL 0,APDNF1S
00048 2021 (02965) HOP R1,R34
00050 0800 (02966) FVFN
00052 C0000500 (02967) MOVZM APDNF1S
00054 F100F0C (02968) STORE R1,R15AV
00056 F1200F0F (02969) STORE R2,R25AV
00058 F1300F10 (02970) STORE R3,R35AV
00060 F1400F12 (02971) STORE R4,R45AV
00062 F1500F14 (02972) STORE R5,R55AV
00064 F1600F16 (02973) STORE R6,R65AV
00066 0470 (02974) RET
00068 0800 (02975) FVFN
00070 A6100F0C (02976) LOAD R1,R15AV
00072 A6200F0F (02977) LOAD R2,R25AV
00074 A6300F10 (02978) LOAD R3,R35AV
00076 A6400F12 (02979) LOAD R4,R45AV
00078 A6500F14 (02980) LOAD R5,R55AV
00080 A6600F16 (02981) LOAD R6,R65AV
00082 A6700F18 (02982) LOAD R7,R75AV
00084 0A030544 (02983) SWBSL 0,R1CVS(R2)

```

SAVE THE IRT VALUE

COMPARE THE BIT ASSIGNMF
NT WITH THE MARK
IF NOT EQUAL,GOTO DECODE
HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

SKIP IF BIT 0 OF R1CVS(R2)

[illegible]

COMPARE THE HIT ASSIGNME
NT WITH THE MARK
GOTO SERIALIZATION ROUTI
NE IF NOT EQUAL
HAS ADDONE HAPPENED?
NO, HOP TO EVEN LOC AFTE
R NET

SKIP IF HIT 0 OF RFCV(R2)
) IS CLEARED

INCRMENT THE BUFFER POSITION COUNTER

```

CHECK IF R5 +VE
RELOAD THE COUNTER WITH
44
IS R2 GREATER THAN POST
1
ADD 18 TO R2 IF LESS THA
N (OR EQUAL, TO

```



```

00070 2802 (04100) ?
00071 12000000 (04100)
00072 2801 (04111) ?
00073 0000 (04112)
00074 00000000 (04113)
00075 2801 (04115)
00076 0000 (04116) ?
00077 0000 (04117)
00078 00000000 (04118)
00079 11000000 (04119)
00080 11000000 (04120)
00081 11000000 (04121)
00082 11000000 (04122)
00083 11000000 (04123)
00084 11000000 (04124)
00085 11000000 (04125)
00086 0000 (04126)
00087 0000 (04127)
00088 00000000 (04128)
00089 00000000 (04129)
00090 00000000 (04130)
00091 00000000 (04131)
00092 00000000 (04132)
00093 00000000 (04133)
00094 00000000 (04134)
00095 00000000 (04135)
00096 00000000 (04136)
00097 00000000 (04137) ?
00098 00000000 (04138)
00099 00000000 (04139)
00100 00000000 (04140)
00101 00000000 (04141)
00102 00000000 (04142)
00103 00000000 (04143)
00104 00000000 (04144) ?
00105 00000000 (04145)
00106 00000000 (04146)
00107 00000000 (04147)
00108 00000000 (04148) ?
00109 00000000 (04149)
00110 00000000 (04150)
00111 00000000 (04151)

```

SPCL 0,01
 SPB 0,ROTDC(R4)
 INC R4,1
 EVEN
 SPCL 0,APDDEL
 RPT #1,14
 EVEN
 MOVZM APDDEL
 STORE R1,R1SAV
 STORE R2,R2SAV
 STORE R3,R3SAV
 STORE R4,R4SAV
 STORE R5,R5SAV
 STORE R6,R6SAV
 STORE R7,R7SAV
 RPT
 EVEN
 LOAD R1,R1SAV
 LOAD R2,R2SAV
 LOAD R3,R3SAV
 LOAD R4,R4SAV
 LOAD R5,R5SAV
 LOAD R6,R6SAV
 LOAD R7,R7SAV
 DJP R5,GRDUSO
 MOVW R5,FORDUS
 CPMR R2,POST352
 JMP GRDUSO,GT
 ADDR R2,FICTUS
 SUBW R6,FICTUS
 LJP R6,OUTOS1
 JMP EXITS
 RET DESERIALIZATION WITHOUT ERROR CONTROL
 MOVW R6,POST35
 SUBW R6,R2

2*1
 DON'T CHANGE ROTDC IF 0
 SET LOW BIT OF THE ROTDC
 T PARM
 INCREMENT R4 BY 1
 HAS APDDE HAPPENED?
 NO, ROP TO EVEN LOC AFTE
 R RPT
 CHECK IF R5 +VE
 RELOAD THE COUNTER WITH
 44
 IS R2 GREATER THAN POST
 3
 LEAVE IF GREATER THAN
 ADD 1H TO R2
 SUBTRACT 1H FROM R6
 LOOP BACK UNTIL THE PNT
 IRE FRAMP IS DONE
 MOVW THE FRAMP LENGTH TO
 R6=LSRW-1-R2

00000 0000	(01152)	EVER	
00000 0030	(01153)	CLR R4	
00007 0000	(01154)	EVER	
00000 00000000	(01155)	ADDIN R2,PCVS-1	
	(01156)		
00000 0022	(01157)	HITUS1	
	(01158)		
00000 0000	(01159)		
00000 00000000	(01160)	SECT. 0, R1	
00000 00000000	(01161)	SPR 0, PCVS(R4)	
00000 0000	(01162)	SPR 0, PCVS(R4)	
	(01163)		
00000 0000	(01164)		
00000 00000000	(01165)	MOVZK APUSLS	
00000 00000000	(01166)	STORE R1, R1SAV	
00000 00000000	(01167)	STORE R2, R2SAV	
00000 00000000	(01168)	STORE R3, R3SAV	
00000 00000000	(01169)	STORE R4, R4SAV	
00000 00000000	(01170)	STORE R5, R5SAV	
00000 00000000	(01171)	STORE R6, R6SAV	
00000 00000000	(01172)	STORE R7, R7SAV	
00000 0000	(01173)	RET	
00000 00000000	(01174)	EVER	
00000 00000000	(01175)	LOAD R1, R1SAV	
00000 00000000	(01176)	LOAD R2, R2SAV	
00000 00000000	(01177)	LOAD R3, R3SAV	
00000 00000000	(01178)	LOAD R4, R4SAV	
00000 00000000	(01179)	LOAD R5, R5SAV	
00000 00000000	(01180)	LOAD R6, R6SAV	
00000 00000000	(01181)	LOAD R7, R7SAV	
00000 0000	(01182)	INC R4, 1	
00000 0000	(01183)	EVER	
00000 00000000	(01184)	JMP R6, HITUS1	
	(01185)		
00000 00000000	(01186)	EXIT	
00000 00000000	(01187)	JMP RCVGRA	
00000 0000	(01188)	JECT	

CLR R4

SET R2 TO 44 THE ADDRESS
:PCVS(R2)
POP RCV(R2)-->R1...R2=H
2+1

SET LOW BIT IN ROTDCT
HAS APDOME HAPPENED?
NO, HOP TO EVEN LOC AFTE
R RET

INCREMENT R4 BY 1

LOOP BACK UNTIL THE ENT
IRE FRAME IS DDNE

OAC04 00000571	(04190)	LOOPS	SWRSL 0,APDRPLS	WAIT TIL AP OUT RUFF FUL
OAC04 00000572	(04191)		JMP LORSRAT	
OAC04 00000573	(04192)	;		
OAC04 00000574	(04193)		MOVEM L,OUTS	
OAC04 00000575	(04194)		MOVEM SP1SGO,SYSP1GS	
OAC04 00000576	(04195)		CCS 3	FROM SRH=2...
OAC04 00000577	(04196)		CCS 2	TO SRH=1
OAC04 00000578	(04197)		CCS 4	FROM MRR=1...
OAC04 00000579	(04198)		CCS 5	TO MRR=2
OAC04 00000580	(04199)		SWRSL 0,APDRPLS	HAS ADDONE HAPPENED?
OAC04 00000581	(04200)		JMP LORSRAT	
OAC04 00000582	(04201)		MOVEM APDRPLS	
OAC04 00000583	(04202)		RET	
OAC04 00000584	(04203)		CCS 3	
OAC04 00000585	(04204)		CCS 2	
OAC04 00000586	(04205)		CCS 4	
OAC04 00000587	(04206)		CCS 5	
OAC04 00000588	(04207)	;		
OAC04 00000589	(04208)		SWRSL 0,APDRPLS	OUTPUT SILENCE IF NO SYN
OAC04 00000590	(04209)		JMP LORSRAT	C
OAC04 00000591	(04210)	;		
OAC04 00000592	(04211)		MOVEM APDRPLS	
OAC04 00000593	(04212)		RET	
OAC04 00000594	(04213)	LOOPS	SWRSL 0,APDRPLS	OUTPUT SPEECH IS WE'VE W
OAC04 00000595	(04214)		JMP LORSRAT	AITED LONG ENUFF
OAC04 00000596	(04215)		CCS 3	COUNT THIS AS ONE MORE F
OAC04 00000597	(04216)		CCS 2	HAVE WAITED
OAC04 00000598	(04217)		CCS 4	
OAC04 00000599	(04218)	;		
OAC04 00000600	(04219)		SWRSL 0,APDRPLS	LOAD BUFFER #2 IF OFLG S
OAC04 00000601	(04220)		JMP LORSRAT	ET
OAC04 00000602	(04221)		MOVEM APDRPLS	
OAC04 00000603	(04222)		RET	
OAC04 00000604	(04223)		CCS 3	
OAC04 00000605	(04224)		CCS 2	
OAC04 00000606	(04225)		CCS 4	
OAC04 00000607	(04226)		CCS 5	
OAC04 00000608	(04227)		SWRSL 0,APDRPLS	LOAD BUFFER #1
OAC04 00000609	(04228)		JMP LORSRAT	
OAC04 00000610	(04229)	LOOPS	SWRSL 0,APDRPLS	
OAC04 00000611	(04230)		JMP LORSRAT	
OAC04 00000612	(04231)		MOVEM APDRPLS	
OAC04 00000613	(04232)		RET	

0000AACA (03277)	DZSN1=SAACA	
00001A8F (03278)	PZASN1=SAAPF	
00004B12 (03279)	TOSSINT=SAH12	
00004B12 (03280)		
0AC02 CC000566 (03281)	RAZP,DZA,MOVW INTERRUPT SERVICE RTNS	
0AC04 CC000567 (03282)	SPNSIF	ANA=0
0AC06 B0004B12 (03283)	MOVW ISAS100	AF1G=0
0AC08 B0004B12 (03284)	JMP TOSSINT	
0AC0A CC000568 (03285)	SPNSZF	ANA=0
0AC0C F0500503 (03286)	MOVW ISAS100	AF1G=1
0AC0E F0500507 (03287)	MOVW P5,ISAS01	
0AC10 CC000569 (03288)	JMP TOSSINT	SYN=0
0AC12 CC000569 (03289)	MOVW ISAS102	SF1G=0
0AC14 F0500503 (03290)	MOVW ISAS104	
0AC16 F2000576 (03291)	MOVW P1,PF0V15	
0AC18 2000 (03292)	CMF#Z FSYH	
0AC1A 1620 (03293)	POP	
0AC1C 1620 (03294)	SKPL LF	DO SYNC UPDATE
0AC1E B0004B2F (03295)	CALL W0,SUPDAT	
0AC20 B0004B12 (03296)	JMP TOSSINT	SYN=0
0AC22 CC00056A (03297)	MOVW ISAS102	SF1G=1
0AC24 F0500503 (03298)	MOVW P5,ISAS01	
0AC26 F0500569 (03299)	MOVW P5,ISAS103	
0AC28 90101469 (03300)	MOVW P1,PF0V25	
0AC2A F2000578 (03301)	CMF#Z KSYN	
0AC2C 2000 (03302)	NOF	
0AC2E 1620 (03303)	SKPL LF	DO SYNC UPDATE
0AC30 B0004B2F (03304)	CALL W0,SUPDAT	
0AC32 B0004B12 (03305)	JMP TOSSINT	OUT=0
0AC34 CC00056A (03306)	MOVW ISAS104	OF1G=0
0AC36 CC00056B (03307)	MOVW ISAS105	
0AC38 B0004B12 (03308)	JMP TOSSINT	OUT=0
0AC3A CC00056A (03309)	MOVW ISAS104	OF1G=1
0AC3C F0500503 (03310)	MOVW P5,ISAS01	
0AC3E F050056B (03311)	MOVW P5,ISAS105	
0AC40 B0004B12 (03312)	JMP TOSSINT	INP=0
0AC42 CC00056C (03313)	MOVW ISAS106	IF1G=0
0AC44 CC00056D (03314)	JMP TOSSINT	
0AC46 B0004B12 (03315)	MOVW ISAS106	INP=0
0AC48 F0500503 (03316)	MOVW P5,ISAS01	IF1G=1
0AC4A F050056C (03317)	MOVW P5,ISAS107	
0AC4C B0004B12 (03318)	JMP TOSSINT	
0AC4E B0004B12 (03319)		
0AC50 B0004B12 (03320)		

00004942	(03321) *	LINE	STOP	OUTPUT
	(03322)			HL=016SS1
04942 90700000	(03323)	D16S11		MOVIR R7,0
04944 86008C62	(03324)			CALL R0,SENSE1F
04946 0470	(03325)			RPT
04947 0800	(03326)			EVFN
04948 90700001	(03327)			MOVIR R7,1
0494A 86008C64	(03328)			CALL R0,SENSE2F
0494C 0470	(03329)			RPT
0494D 0800	(03330)			EVFN
0494E 80001942	(03331)			JMP D16S11
	(03332) *			
	(03333) *	LINE	STOP	INPUT
	(03334)			HL=016SS2
04A00 90700010	(03335)	D16S12		MOVIR R7,16
04A02 86008C70	(03336)			CALL R0,RCVRS1F
04A04 0470	(03337)			RPT
04A05 0800	(03338)			EVFN
04A06 90700011	(03339)			MOVIR R7,17
04A08 86008C7E	(03340)			CALL R0,RCVRS2F
04A0A 0470	(03341)			RPT
04A0B 0800	(03342)			EVFN
04A0C 80003A00	(03343)			JMP D16S12
	(03344) *			
	(03345) *	SPEECH	STOP	OUTPUT
	(03346)			HL=022SS1
04ACA 9070000C	(03347)	D22S11		MOVIR R7,12
04ACC 86008C8E	(03348)			CALL R0,OUTS1F
04ACE 0470	(03349)			RPT
04ACF 0800	(03350)			EVFN
04AD0 9070000D	(03351)			MOVIR R7,13
04AD2 86008C94	(03352)			CALL R0,OUTS2F
04ADA 0470	(03353)			RPT
04AD5 0800	(03354)			EVFN
04AD6 80004ACA	(03355)			JMP D22S11
	(03356) *			
	(03357) *	SPEECH	STOP	INPUT
	(03358)			HL=023SS1
04AEF 9070000F	(03359)	D23S11		MOVIR R7,14
04AF0 86008C9C	(03360)			CALL R0,INS1F
04AF2 0470	(03361)			RPT
04AF3 0800	(03362)			EVFN
04AF4 9070000F	(03363)			MOVIR R7,15
04AF6 86008CA2	(03364)			CALL R0,INS2F

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04AFH 0670 (03365) 441
04AFH 0800 (03366) 442
04AFH 80004044 (03367) JWP 021511
04AFH 80004044 (03368) *
04AFH 80004044 (03369) * PATCH EXECUTIVE TO CREATE BINARY VALUED I/O FLAGS
04AFH 80004044 (03370) JWP 105511
04AFH 80004044 (03371) 443
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LOUIS:	0304C (00944) (01000)		
LOUIS1:	03004 (02616) (02680)		
LOUIS2:	03006 (01206) (03213)		
LOUIS3:	03007 (03216) (03229)		
LOUIS4:	03008 (00066) (00214)	(00214)	(01407)
LOUIS5:	03009 (00065) (00211)	(01403)	(01407)
LOUIS6:	03010 (00070) (00099)	(00901)	(00902)
LOUIS7:	03011 (01734) (02160)	(02183)	(02184)
LOUIS8:	03012 (02273) (02276)		
LOUIS9:	03013 (00071) (01634)	(01636)	(01659)
LOUIS10:	03014 (00072) (01795)	(01826)	(01850)
LOUIS11:	03015 (00073) (01825)	(01827)	(01874)
LOUIS12:	03016 (01429) (01426)	(01433)	(01435)
LOUIS13:	03017 (00046) (02084)	(02119)	(02124)
LOUIS14:	03018 (01967) (02075)	(02121)	
LOUIS15:	03019 (00063) (00213)	(02751)	
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LOUIS268:	03272 (00063)		
LOUIS269:	03273 (00063)		

॥ श्रीगणेशाय नमः ॥

[illegible]

01400 3456789

01400 3456789

SPISG3:	00027 (00020)	(00823)	(00842)
SPISG:	00569 (00082)	(01286)	
SPISG0:	0A56A (01991)		
SPISG:	0A56A (01984)	(01988)	
SPISG:	09F4F (00826)	(00871)	
SSSAL7:	0A562 (02030)		
SSSLP:	0A56H (02018)	(02064)	
SSSLP:	0A604 (02050)	(02053)	(02056) (02060)
SSSLP:	0A602 (02049)	(02059)	
SSSAL:	0A62A (02089)	(02071)	(02092)
SSSAL:	0A600 (00053)		
SSSLP:	0A634 (02126)	(02124)	
SSSLP:	0A632 (02118)	(02129)	
SSSLP:	0A650 (02134)	(02142)	
SSSLP:	0A65A (02126)	(02136)	
SSSLP:	0A62F (02111)	(02195)	(03304)
SVFS:	00382 (00054)	(00055)	
SVFS:	0056H (00061)	(01778)	
SVFS:	007C4 (00186)	(00187)	
SVFS:	00435 (00189)	(00190)	
SVFS:	00577 (00042)	(02074)	(02111) (02159)
SVFS:	0A5AF (01973)	(01986)	
SVFS:	0A5C4 (01978)	(02011)	
SVFS:	1B4CF (00021)	(00704)	(00742) (00823) (00832) (00867) (01781) (02715) (02741) (03194)
SVFS:	(03272)		
THALIS:	09F4H (00465)	(02494)	(02560)
THALIS:	09F7H (00530)	(02496)	(02561)
THALIS:	09F0H (00594)	(02497)	
THALIS:	09CA2 (00224)	(01506)	
THALIS:	0900H (00436)	(02415)	(02434) (02465) (02477)
THALIS:	09CFC (00271)	(02335)	(02348)
THALIS:	090FH (00400)	(02399)	(02427) (02459) (02470)
THALIS:	09C94 (00222)		
THALIS:	09F40 (00704)	(00795)	(01760)
THALIS:	09F22 (00694)	(00762)	(00770)
THALIS:	09F4A (00704)	(00712)	
THALIS:	09F52 (00721)	(00729)	
THALIS:	09F6F (00739)	(00753)	
THALIS:	09F0F (00943)		
THALIS:	0A656 (01965)	(02147)	
THALIS:	00002 (00056)	(01991)	(01994)
THALIS:	00564 (00094)	(01598)	(01603)
THALIS:	00575 (00030)	(00694)	(00822)
THALIS:	09FA2 (00895)	(00822)	

PAGE 00: PROG. EXPUSP.TXT

IMTIS: 03300 (01604) (01757)
IMPCVS: 0A306 (00731) (01774)
7S: 00003 (00057)
77IS: 00004 (00221)
ZPUS: 0000A (00212)
ZBUS: 00203 (00175) (00007) (00000) (00003) (03214) (03226) (03237)

LINES WITH ERRORS: 0 (MAP VERSION 000101.10) F- 0

3.6.3 Fortran Programs

The modules contained in this section are:

FCBGN	PITLPC
FF2R	QDPARM
VMOV1	VPBS
VMOV2	BASIS
VMOV3	ASRT
MPFDVM	SDCT
MPDCTM	BITA
MPQDPP	QDCT
MPDQPP	CHANL
MPMWGX	DPARM
MPFSTV	SSRT
MPSSRT	DDCT
MPIDCM	DCTR
MPASRT	VARDC
MPFSTQ	OUTPUT
MPFSTD	SNR
MPVDNM	TAPE2
MPBASP	OPT1
MPSCAN	OPT2
MPCDBA	OPT3
MASTER	OPT4
INIT	OPT5
USER	OPT6
MAPON	OPT7
CREATE	OPT8
DEFINE	OPT8A (OPT8B and OPT8C are
SETUP	PREMAP similar)
LOAD	FILEN
IOSM	
AOM	
ADAM	
SYSTMA	
INPUT	
DCVAR	
DCTF	

```

0001      INTEGER FUNCTION FCRG(FCRG0,Y,A,U,H,V,C,W,D,IS)
C
C      TITLE: FCRG
C
C      PURPOSE:
C      GENERATE FOR AND START EXECUTION OF MAP
C
C      USAGE:
C
C      MAP = FCRG(FCRG0,Y,A,U,H,V,C,W,D,IS)
C
C      DESCRIPTION OF PARAMETERS
C      FCRG0 INTEGER VARIABLE SPECIFYING FOR NUMBER
C      Y,U,V,W - INTEGER VARIABLES SPECIFYING MAP
C      HOFFER IDENTIFIERS
C      A,H,C,D - INTEGER VARIABLES SPECIFYING MAP
C      SCALAR IDENTIFIERS
C      IS - POINTER TO LOGICAL ARRAY SUPPLIED BY BLOCK DATA. K-TH
C      ELEMENT IS .TRUE. IF K-TH ID ARGUMENT
C      IS TO BE USED, OTHERWISE .FALSE.
C      IFR - OPTIONAL ERROR INDICATOR
C      MAP = 0 INDICATES NO ERROR (IDENTIFIERS WITHIN PROPER BOUNDS).
C      MAP = K INDICATES K-TH ARGUMENT IN
C      THE CALLING LIST IS
C      OUTSIDE LEGAL BOUNDS. MAP EXECUTION ABORTED.
C
C      FUNCTIONS AND SUBROUTINES REQUIRED
C      NOMP(FCRG,FCRS) - INDICATES MAP EXECUTION.
C
0002      INTEGER Y,A,U,H,V,C,W,D,FCRG0,HIDMX,HIDMY,SIDMX,SIDMY
0003      INTEGER FCRG,FCRS,FCRN2,RUNMP
0004      INTEGER FCRSZ,HAS,FCR,HRI
0005      LOGICAL ISARG
0006      DIMENSION ISARG(8,72)
C
0007      COMMON/FPZZZ/FCRG(11),FCRSZ(11,7),HAS,FCR(6),MODCH(4),HRI,LEVEL
0008      DIMENSION FCRS(11)
C
0009      DATA HIDMX/1/,HIDMY/63/,SIDMX/0/,SIDMY/191/
0010      DATA FCRS/0,10*8/
0011      DATA ISARG(1,1),ISARG(2,1),ISARG(3,1),ISARG(4,1),
1      ISARG(5,1),ISARG(6,1),ISARG(7,1),ISARG(8,1)/
2      .FALSE.,.TRUE.,.FALSE.,.TRUE.,.FALSE.,.TRUE.,.TRUE.,.FALSE./
0012      DATA ISARG(1,2),ISARG(2,2),ISARG(3,2),ISARG(4,2),
1      ISARG(5,2),ISARG(6,2),ISARG(7,2),ISARG(8,2)/
2      .TRUE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE./
0013      DATA ISARG(1,3),ISARG(2,3),ISARG(3,3),ISARG(4,3),
1      ISARG(5,3),ISARG(6,3),ISARG(7,3),ISARG(8,3)/
2      .TRUE.,.TRUE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE./
0014      DATA ISARG(1,4),ISARG(2,4),ISARG(3,4),ISARG(4,4),
1      ISARG(5,4),ISARG(6,4),ISARG(7,4),ISARG(8,4)/
2      .TRUE.,.TRUE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE.,.FALSE./
0015      DATA ISARG(1,5),ISARG(2,5),ISARG(3,5),ISARG(4,5),ISARG(5,5),
1      ISARG(6,5),ISARG(7,5),ISARG(8,5)/

```

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2 39.TRUE...59.FALSE../
DATA 1SARG(1,9),1SARG(2,6),1SARG(3,6),1SARG(4,6),
1 1SARG(5,6),1SARG(6,6),1SARG(7,6),1SARG(8,6)/
2 4.FALSE...39.TRUE...49.FALSE../
DATA 1SARG(1,7),1SARG(2,7),1SARG(3,7),1SARG(4,7),
1 1SARG(5,7),1SARG(6,7),1SARG(7,7),1SARG(8,7)/
2 4.FALSE...49.TRUE...39.FALSE../
DATA 1SARG(1,8),1SARG(2,8),1SARG(3,8),1SARG(4,8),
1 1SARG(5,8),1SARG(6,8),1SARG(7,8),1SARG(8,8)/
2 4.TRUE...FALSE...1TRUE...FALSE...39.FALSE../
DATA 1SARG(1,9),1SARG(2,9),1SARG(3,9),1SARG(4,9),
1 1SARG(5,9),1SARG(6,9),1SARG(7,9),1SARG(8,9)/
2 39.TRUE...FALSE...TRUE...39.FALSE../
DATA 1SARG(1,10),1SARG(2,10),1SARG(3,10),1SARG(4,10),
1 1SARG(5,10),1SARG(6,10),1SARG(7,10),1SARG(8,10)/
2 24.FALSE...TRUE...59.FALSE../
DATA 1SARG(1,11),1SARG(2,11),1SARG(3,11),1SARG(4,11),
1 1SARG(5,11),1SARG(6,11),1SARG(7,11),1SARG(8,11)/
2 4.TRUE...FALSE...TRUE...59.FALSE../
DATA 1SARG(1,12),1SARG(2,12),1SARG(3,12),1SARG(4,12),
1 1SARG(5,12),1SARG(6,12),1SARG(7,12),1SARG(8,12)/
2 4.TRUE...39.FALSE...TRUE...39.FALSE../
DATA 1SARG(1,13),1SARG(2,13),1SARG(3,13),1SARG(4,13),
1 1SARG(5,13),1SARG(6,13),1SARG(7,13),1SARG(8,13)/
2 4.TRUE...79.FALSE../
DATA 1SARG(1,14),1SARG(2,14),1SARG(3,14),1SARG(4,14),
1 1SARG(5,14),1SARG(6,14),1SARG(7,14),1SARG(8,14)/
2 89.TRUE../
DATA 1SARG(1,15),1SARG(2,15),1SARG(3,15),1SARG(4,15),
1 1SARG(5,15),1SARG(6,15),1SARG(7,15),1SARG(8,15)/
2 29.TRUE...FALSE...TRUE...FALSE...29.FALSE../
DATA 1SARG(1,16),1SARG(2,16),1SARG(3,16),1SARG(4,16),
1 1SARG(5,16),1SARG(6,16),1SARG(7,16),1SARG(8,16)/
2 69.TRUE...FALSE...TRUE../
DATA 1SARG(1,17),1SARG(2,17),1SARG(3,17),1SARG(4,17),
1 1SARG(5,17),1SARG(6,17),1SARG(7,17),1SARG(8,17)/
2 39.TRUE...FALSE...TRUE...FALSE...TRUE...FALSE../
DATA 1SARG(1,18),1SARG(2,18),1SARG(3,18),1SARG(4,18),
1 1SARG(5,18),1SARG(6,18),1SARG(7,18),1SARG(8,18)/
2 4.TRUE...FALSE...TRUE...39.FALSE...TRUE...FALSE../
DATA 1SARG(1,19),1SARG(2,19),1SARG(3,19),1SARG(4,19),
1 1SARG(5,19),1SARG(6,19),1SARG(7,19),1SARG(8,19)/
2 4.FALSE...FALSE...FALSE...39.FALSE...FALSE...FALSE../
DATA 1SARG(1,20),1SARG(2,20),1SARG(3,20),1SARG(4,20),
1 1SARG(5,20),1SARG(6,20),1SARG(7,20),1SARG(8,20)/
2 4.TRUE...TRUE...FALSE...TRUE...FALSE...TRUE...FALSE../
DATA 1SARG(1,21),1SARG(2,21),1SARG(3,21),1SARG(4,21),
1 1SARG(5,21),1SARG(6,21),1SARG(7,21),1SARG(8,21)/
2 4.TRUE...TRUE...FALSE...FALSE...FALSE...29.FALSE../
DATA 1SARG(1,22),1SARG(2,22),1SARG(3,22),1SARG(4,22),
1 1SARG(5,22),1SARG(6,22),1SARG(7,22),1SARG(8,22)/
2 4.TRUE...FALSE...TRUE...FALSE...TRUE...FALSE...TRUE...FALSE../

```

C

C WORK FORM IS NEG. SPECIAL SUPPORT IS REQUIRED

INCH=0

0033

```

FCN000,FIN
0014 IF (FCN00,LT,0) ISPSH=1
0015 FCN02=1ABS(FCN00)

C LOAD PCV VALUES
0016 FCN0(1) = 0
0017 FCN0(2) = FCN02
0018 FCN0(3) = 1SPSH
0019 FCN0(4) = Y
0020 FCN0(5) = A
0021 FCN0(6) = U
0022 FCN0(7) = R
0023 FCN0(8) = Y
0024 FCN0(9) = C
0025 FCN0(10) = *
0026 FCN0(11) = 0

C
C VALIDATE ARGUMENT IF USED, SET TO ZERO IF NOT.
0027 K=0
0028 DO 400 I=4,10,2
0029 J=1+I
0030 IF (ISARG(I-1,IS)) GO TO 100
0031 FCN0(I)=0
0032 GO TO 200
0033 100 K=K+1
0034 IF (FCN(I),LT,HIDWN,OR,FCN(I),GT,HIDMX) GO TO 1000
0035 200 IF (ISARG(J-1,IS)) GO TO 300
0036 FCN0(J)=0
0037 GO TO 400
0038 300 K=K+1
0039 IF (FCN(J),LT,SIDWN,OR,FCN(J),GT,SIDMX) GO TO 1000
0040 400 CONTINUE
C
C START MAP AND RETURN
0041 FCN06 = MOD#P(FCN0(1),FCN0(1))
0042 RETURN

C
C MAP EXECUTION ABORTED: SET ERROR CODE AND RETURN
0043 1000 FCN06=K
0044 IF (FCN0,NE,0) CALL MPERR(FCN0)
0045 RETURN
0046 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCN001	000446	144
SIDATA	000014	6
SVARS	000010	106
4PZZZ	000312	101

TOTAL SPACE ALLOCATED = 001574 446

FORTRAN IV-PLUS V02-514
FCRGN.PTN /DE/ER

NO PPP INSTRUCTIONS GENERATED

FCRGN.LP/0.1:1=FCHGN/DE/MUTH

21:13:55 11-SEP-80

PAGE 4

```

0001      INTERM FUNCTION FF2R (Y, SA, U, V, W)
0002      INTERM FCHGN, Y, SA, U, V, W
0003      FF2R = FCHGN(-214, Y, SA, U, V, W, 0, 14)
0004      RETURN
0005      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000054	22
SPDATA	000014	6
SIDATA	000030	12
		RW,I,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000120 40

NO PPP INSTRUCTIONS GENERATED

FF2R.LP/LI:1=FF2R/NUTR

```

0001      INTEGER FUNCTION VMOV1(Y)
0002      INTEGER FCNGB,Y
0003      VMOV1=FCNGB(217,Y,0,0,0,0,0,0,0,0,11)
0004      RETURN
0005      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CUN,I,CL
SPDATA	000014	6 RW,D,CUN,I,CL
STDATA	000010	12 RW,D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

VMOV1.LP/LI:1=VMOV1/DE/NOTR

FORTRAN IV-PLUS V02-SIF 13135143 09-AUG-80
VMOV2.FIN /DE/WR

0001 INTEGER FUNCTION VMOV2(Y)
0002 INTEGER FCNCRN,Y
0003 VMOV2=FCNCRN(238,Y,0,0,0,0,0,0,0,1)
0004 RETURN
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CN,I,CL
SPDATA	000014	6 RW,D,CN,I,CL
SIDATA	000030	12 RW,D,CN,I,CL

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

VMOV2.LP/L1:1=VMOV2/DE/NOTR

FORTRAN IV-PLUS V02-SIF 13135147 09-AUG-80

VMOV3.FIN /DP/WH
 0001 INTEGER FUNCTION VMOV3(Y)
 0002 INTEGER FCHGM,Y
 0003 VMOV3=FCHGM(239,Y,0.0,0.0,0.0,0.0,13)
 0004 RETURN
 0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000070 28

NO FPP INSTRUCTIONS GENERATED

VMOV3.LP/LI:1=VMOV3/DP/NOTN

FORTRAN IV-PLUS V02-51P 13:35:51 09-AUG-80

MPEDVM.FTN

0001 C INTEGER FUNCTION MPEDVM(Y,U,V)

C MAP USAGE:

C 1FRR=MPEDVM(Y,U,V)

0002 C INTEGER FCHGN,Y,U,V

0003 MPEDVM=FCHGN(240,Y,0,U,0,V,0,0,0,8)

0004 RETURN

0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	16 RW,I,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000104 34

NO FPD INSTRUCTIONS GENERATED

MPEDVM,LP/LI:1=MPEDVM/DK/NOTR

AD-A691 663

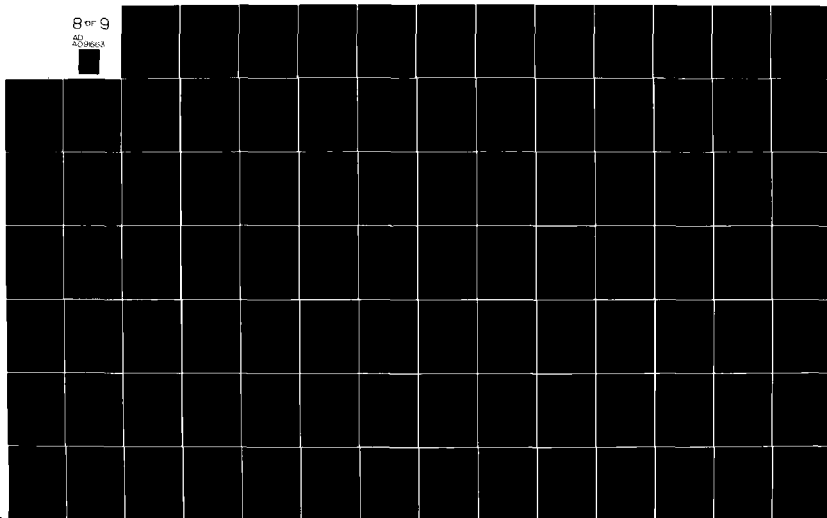
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
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FORTMAN IV-PLUS V02-51E 13135:56 09-AUG-80
 MPDCTM.FTN /DF/WR

```

0001      INTEGER FUNCTION MPDCTM(Y,U,V,W)
           C
           C      MAP USAGE:
           C
           C      IPRH=MPDCTM(Y,U,V,W)
           C
           C      INTEGER FCRGN,Y,U,V,W
           C      MPDCTM=FCRGN(241,Y,0,U,0,V,0,W,0,22)
           C      RETURN
           C      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	19 RW,1,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SIDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPDCTM,LP/LI:1=MPDCTM/DE/NOTR

FORTRAN IV-PLUS V02-SIF 13:36:01 09-AUG-80
 MPDPPP.FTN /DE/WR

```

0001      C      INTEGER FUNCTION MPDPPP(Y,U,V)
          C      MAP USAGE:
          C
          C      TERR=MPDPPP(Y,U,V)
          C
0002      C      INTEGER FCHGN(U,V,V,W
0003      C      MPDPPP=FCHGN(242,Y,0,U,0,V,0,0,0,8)
0004      C      RETURN
0005      C      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000040	16 RW,1,CUN,LCL
SPDATA	000014	6 RW,D,CUN,LCL
STDATA	000030	12 RW,D,CUN,LCL
SVARS	000007	1 RW,D,CUN,LCL

TOTAL SPACE ALLOCATED = 000106 35

NO FPP INSTRUCTIONS GENERATED

MPDPPP,LP/LI:1=MPDPPP/DE/NOTR

FURTRAN IV-PLUS V02-51F 13:36:05 09-AUG-80
 MPDOPP.FTN /JIF/WH

```

0001      C      INTEGER FUNCTION MPDOPP(Y,U,V)
          C      MAP USAGE:
          C
          C      IFRW=MPDOPP(Y,U,V)
          C
0002      C      INTEGER FCRCN,U,Y,V,W
0003      C      MPDOPP=FCRCN(243,Y,0,U,0,V,0,0,0,0,H)
0004      C      RETURN
0005      C      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	16
SPDATA	000014	6
SIDATA	000030	12
SVARS	000002	1
		RW,I,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000106 35

NO FPP INSTRUCTIONS GENERATED

MPDOPP.IP/I.I:1=MPDOPP/DE/NOTR

FORTRAN IV-PLUS V02-51E 13:36:10 09-AUG-80
MPMWGX.FTN /DF/WR

```

0001      C      INTEGER FUNCTION MPMWGX(Y,U,V,W)
           C      MAP USAGE:
           C
           C      IARR=MPMWGX(Y,U,V,W)
           C
0002      INTEGER FCRGN,Y,U,V,W
0003      MPMWGX=FCRGN(244,Y,0,U,0,V,0,W,0,22)
0004      RETURN
0005      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	19 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPMWGX,LP/LI:1=MPMWGX/DE/NOTR

0001 INTEGER FUNCTION MPFSTV(I)

MAP USAGE:

```
C  
C MAP USAGE:  
C IPRR=MPFSTV(I)
```

```

0002      INTEGER FCRGN,U
0003      MPSTV=FCRGN(245,0,0,0,0,0,0,0,0,10)
0004      RETURN
0005      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	RV,I,CON,I,CL
SSPDATA	000014	RV,D,CON,I,CL
SIDATA	000030	RV,D,CON,I,CL

TOTAL SPACE: ALLOCATED = 000070 2A 2A

AND FPP INSTRUCTIONS GENERATED
MPFSTV,LP/LI:1=MPFSTV/DE/NOTR

FORTRAN IV-PLUS V02-SLK
MPSSRT.FTN

13:36:18 09-AUG-80

PAGE 1

0001 C INTEGER FUNCTION MPSSRT(Y)

C MAP USAGE:

C C

C C

C C IENR=MPSSRT(Y)

C

0002 INTEGER FCHGN,Y

0003 MPSSRT=FCHGN(247,Y,0,0,0,0,0,0,0,0,13)

0004 RETURN

0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CUN,I,CL
SPDATA	000014	6 RW,D,CUN,I,CL
SIDATA	000030	12 RW,D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000070 2R

NO FPP INSTRUCTIONS GENERATED

MPSSRT.LP/LI:1=MPSSRT/DE/NOTH

FORTRAN IV-PLUS V02-51F
MPIDCM.FTN

```

0001      INTERM FUNCTION MPIDCM(Y,U,V)
          C
          C      MAP USAGE:
          C
          C      IARR=MPIDCM(Y,U,V)
          C
          C      INTEGER FCRGM,U,V,V,W
          C      MPIDCM=FCRGM(248,Y,U,U,V,U,V,U,V,U,V,U,V,U,V,U,V)
          C      RETURN
          C      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	16
SPDATA	000014	6
SIDATA	000030	12
SVARS	000002	1
		RW,I,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000106 35

NO FPP INSTRUCTIONS GENERATED

MPIDCM,LP/LI:=MPIDCM/DE/NOTR

```

0001      C      INTEGR FUNCTION MPASRT(Y)
          C      MAP USAGE:
          C
          C      IARR=MPASRT(Y)
          C
0002      C      INTEGR FCHGN,Y
0003      MPASRT=FCHGN(249,Y,0.0,0.0,0.0,0.0,13)
0004      RETURN
0005      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCORF1	000024	10 RW,1,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SIDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000070 28

NO FPP INSTRUCTIONS GENERATED

MPASRT,LP/LI1=MPASRT/DF/NOTR

FORTRAN IV-PLUS V02-SIF 11:36:33 04-AUG-80
MPFSTQ.FTN /HF/HR

0001 INTEGER FUNCTION MPFSTQ(Y,U,V,W)

C
C
C
C
C

MAP USAGE:

IFHR=MPFSTQ(Y,U,V,W)

INTEGER FCHGN,Y,U,V,W

MPFSTQ=FCHGN(250,Y,0,U,0,V,0,W,0,22)

RETURN

END

0002

0003

0004

0005

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000046	19 RW,I,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SIDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPFSTQ,1P/1,1:1=MPFSTQ/DE/NOTR

```

0001      INTEGER FUNCTION MPFSTD(Y,U,V,W)
           C
           C      MAP USAGE:
           C
           C      IFMR=MPFSTD(Y,U,V,W)
           C
           C      INTEGER ECHGN,Y,U,V,W
           C      MPFSTD=FCBGN(251,Y,0,U,0,V,0,W,0,22)
           C      RETURN
           C      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	19 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPFSTD,LP/LI:1=MPFSTD/DE/NOTR

FORTRAN IV-PLUS V02-51F
MPVDNM.FTN /OF/WR

```

0001      C      INTEGK FUNCTION MPVDNM(Y,U,V)
          C      MAP USAGE:
          C
          C      TFR=MPVDNM(Y,U,V)
          C
0002      INTEGK FCRGN,U,V,V
0003      MPVDNM=FCRGN(252,Y,0,U,0,V,0,0,0,0,8)
0004      RETURN
0005      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000040	16 RW,I,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SIDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000104 34

NO FPP INSTRUCTIONS GENERATED

MPVDNM,LP/LI:1=MPVDNM/DE/NOTR

```

0001      INTEGER FUNCTION MPRASP(Y,U,V,W)
          C
          C      MAP USAGE:
          C
          C      1ERR=MPRASP(Y,U,V)
          C
          C      0002      INTEGER FCNGN,Y,U,V,W
          C      0003      MPRASP=FCNGN(253,Y,0,U,0,V,0,W,0,0,72)
          C      0004      RETURN
          C      0005      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000046	19 RW,I,CON,LCL
SPDATA	000014	6 RW,D,CON,LCL
SIDATA	000030	12 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000112 37

NO FPP INSTRUCTIONS GENERATED

MPRASP,LP/LI:1=MPRASP/DE/NOTR

FORTRAN IV-PLUS V02-SIF 13:36:52 09-AUG-80
 MPSCAN.FTN /DE/WR

0001 INTEGER FUNCTION MPSCAN(Y)

C
 C
 C
 C

MAP USAGE:

IFRR=MPSCAN(Y)

0002 INTEGER FCNGN(0,Y,V,M)
 0003 MPSCAN=FCNGN(254,Y,0,0,0,0,0,0,0,0,0,13)
 0004 RETURN
 0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SDATA	000030	12 RW,D,CON,I,CL
SVARS	000006	3 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000076 31

NO FPP INSTRUCTIONS GENERATED

MPSCAN,LP/LI:1=MPSCAN/DE/NOTR

FORTRAN IV-PLUS V02-SIF 13:36:56 09-AUG-80
MPCDRA.FTN /DE/WR

0001 INTEGER FUNCTION MPCDRA(Y)

C
C
C
C
C

MAP USAGE:
IFR=MPCDRA(Y)

0002 INTEGER FCRGN,Y,U,V
0003 MPCDRA=FCRGN(255,Y,0,0,0,0,0,0,0,13)
0004 RETURN
0005 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000024	10 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000030	12 RW,D,CON,I,CL
SVARS	000004	2 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000074 30

NO FPP INSTRUCTIONS GENERATED

MPCDRA,LP/LI:1=MPCDRA/DE/NOTR

KONTAK IV-PLUS V02-514
MASTER.ATC /EF/WH

21:16:56 11-SEP-80

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MASTER,LP/1:1=MASTER.ATC/DE/NOTP

FORTRAN IV-PLUS V02-N1P 13:27:26 09-AUG-80
INIT.PTN /DE/WR

```

0023 MSHRT=H
0024 MINDG=Q
C
0025 VERY ACCURATE VALUE FOR PI=3.14159...
C
0026 PI=4.0*ATAN(1.)
C LOGARITHM BASE 10 OF 2
0027 DLOG2=ALOG(2.0)
C TRANSMIT,RECEIVE SIZE,ISEN
ISEN=369
C # OF SIDEHAND HITS/FRAME,LPARM
LPAHM=13
C
0028 FRAME SIZE,LTH
LTH=256
0029 LTH2=2*LTH
0030 LTH3=3*LTH
0031 LTH4=4*LTH
0032 LTHM1=LTH-1
0033 LPC ORDER,LPCN
LPCN=H
C
0034 OUTPUT INTERPOLATING SIZE,NP
NP=10
C
0035 X=NP
0036 CUMULATIVE SNR RATIO,CNS
CNS=0.
C
0037 FRAME COUNTER,ICOUNT
ICOUNT=0
C
0038 ARGUMENT FOR TRIG FUNCTIONS
ARG=PI*(2.*LTH-1.)/(2.*LTH)
C
0039 SET UP SPEECH I/O HANDLER
C
0040 IST=1
0041 NSKIP=1
0042 NSKIPS=NSKIP
C
0043 INPUT RECORD SIZE,NTOT1
NTOT1=LTH-NP
C
0044 INPUT UPDATE SIZE,NTUPS
NTUPS=LTH-NP
C
0045 OUTPUT RECORD SIZE,NTOTO
NTOTO=LTH-NP
C
0046 #SET ALL ARRAYS
DO 1 I=1,NTOT1
0047 1 MIN(I)=0
0048 DO 2 I=1,NTOTO
0049 2 NOUT(I)=0
0050 RETURN
0051 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000366	123
SVARS	000060	24
MTAPF0	002260	600
MTAPF1	000012	5

PW,I,CNN,ICL
PW,D,CNN,ICL
PW,D,OVR,CNN
PW,D,OVR,CNN

FORTRAN IV-PLUS V02-51F
INIT.FTN

13:27:26 09-AUG-80

PAGE 3

MTAPF2	000012	5	RW,D,OVR,CHI.
OVRI	002020	520	RW,D,OVR,CHI.
OVRO	002006	515	RW,D,OVR,CHI.
OVRF	006016	1543	RW,D,OVR,CHI.
OVRA	000152	53	RW,D,OVR,CHI.
OVVP	002022	521	RW,D,OVR,CHI.
OVRR	005004	1282	RW,D,OVR,CHI.
OVH2	001002	257	RW,D,OVR,CHI.
OVRL	000016	7	RW,D,OVR,CHI.
OVRO	000012	5	RW,D,OVR,CHI.
VAPDFF	000200	54	RW,D,OVR,CHI.

TOTAL SPACE ALLOCATED = 025450 5524

INIT.IP/I.I:1=INIT/DE/NOTR

PROGRAM IV-PLUS 002-511
USER,FIN 706/80

21:17:14 11-SEP-80

PAGE 3

0000	006016	1843	RM,D,0VR,GML
0000	000152	53	RM,D,0VR,GML
0000	002022	521	RM,D,0VR,GML
0000	005004	1282	RM,D,0VR,GML
0002	001002	257	RM,D,0VR,GML
0001	000016	7	RM,D,0VR,GML
0000	000012	5	RM,D,0VR,GML
0000	000210	64	RM,D,0VR,GML

TOTAL SPACE ALLOCATED = 037740 4176

NO REP INSTRUCTIONS GENERATED

USER,LP/1:1=USER/D0/MOTR

```

C
PK(0,NA) = NA ; AP(0),6 TN      001,1 JAT(0):05=44N=79
                                00DATT:14=JUL=80

```

ИТАЛ: 14-3UL-40

```

00001 SUPRMLP1AP MAPUN
00002 INTPRM ZPRD(738),PARM11,PCVOPF
00003 DIMENSION SCALAR(171)
00004 INTPRM RUS1,RUS2,RUS3
00005 INTPRM REAR,CMPX,RATG,RVPR,SINGL,DOUBL,RYTE2
00006 INTP4,MYPR,CNVYS,CNVMD,OUTY,STATUS,SHORT
00007 INTPRM WOKH,X1,XPR,X11,IORDM,PWITM,DCMT1
00008 1,DCMT2,RI,AL,RP,PARC,TMR3,TMR2,TMR3,TMR4,X2
00009 2,DUCTM,TVM,OPRPM,ODUCTM
00010 3,VLDNG,CUSZ,VSHRT,YDM,XDM,MAPX
00011 4,MILT,DCFM,OPRM,QTUCFM
00012 5,INW,MIN1,MIN2,NOUIM,NOU11,NOU12
00013 6,RCV1,RCV2,SPN1,SPK2
00014 7,ACPR,AGUCT,ATMT,SK6,IMPR
00015 8,SYNHL,SYNR2,RF1,RF2,OUTF
00016 9,MFCV,AROPR,ARODCT,ARIT
00017 1,DCMT11,DCMT12,IORDM1,IORDM2,MINT1,MINT2,MINT3,MINT4,OTUC11
00018 2,ZHU
00019
00020
00021
00022
00023
00024
00025
00026

```

```
0027 DATA BYTE2,MYT4,MYT8/2,4,8/
0028 DATA LONG,SHORT,CVYF5,CVYF6/0,1,1,0/
0029 DATA POST,POST2,POST3/64,128,192/
0030 DATA WDRK,A1,A16,X11,10KDR1,PAR114,DC11/10,11,0,0,12,13,14/
0031 DATA DC112,RI,A1,A1,PARC,TRP1,102/15,16,17,18,3,19,20/
0032 DATA DRDCTN,YNM/21,0/
0033 DATA VLONG,CUSZ,VSHRT,YDM,XDM,MAPX/23,24,25,26,27,28/
0034 DATA MUNIT,DCIM,OPRM,OUTCTM/29,30,31,32/
0035 DATA MTRM,MTR1,MTR2,ACUTM,MUT1,MUT2/33,34,35,36,37,38/
0036 DATA RCV1,RCV2,SEN1,SEN2/39,40,41,42/
0037 DATA AGRM,AUTDCT,ATR1/43,44,45/
0038 DATA TAP1,TAP4,X2/46,47,48/
0039 DATA SEN,INPR/49,50/
0040 DATA SYN1,M1/62,63/
0041 DATA RCV,AROPR,ARODCT,PRIT/56,57,58,2/
0042 DATA UTM,ZRO,UNITY/59,1,63/
0043 DATA DCT11,DC112,10KDR1,10KDR2,MTR1/51,52,53,54,55/
0044 DATA MTR12,MTR13,MTR14,OUTCT1/60,5,6,7/
0045 DATA PARITY,INIT,RCVDF/114,113,127/
0046 DATA OPRM,ORDDCTM/8,9/
```

K A P - 3 0 0									
R U F F E R A R E A S									
C	REF	POS	BA	MARKS-1	HS	ST	SI	WS	
C	43	1	35328.	35311.	14	1	E	L	
C	44	1	35342.	35327.	256	1	F	L	
C	45	1	35598.	35853.	256	1	E	L	
C	49	1	35854.	36273.	370	1	E	L	
C	50	1	36274.	36479.	256	1	E	L	
C	53	1	36480.	36848.	369	1	E	L	
C		1	36849.	37217.	369	1	E	L	
C		1	37218.	37586.	369	1	E	L	
C		1	37587.	37955.	369	1	E	L	
C	59	1	37956.	38211.	256	1	E	L	
C	56	1	38212.	38581.	370	1	E	L	
C	57	1	38582.	38595.	14	1	E	L	
C	58	1	38596.	38851.	256	1	E	L	
C	62	1	38852.	39270.	369	1	E	L	
C		1	39271.	39589.	369	1	E	L	
C	2	1	39590.	46245.	256	1	E	L	
C	63	1	10060.	14499.	4500	1	E	L	
C		1	8400.	9999.	1200	1	E	L	

[illegible]

FORTNAM IV-PLUS V02-S14
VAPOR.FIN /DE/WR

21:15:27 11-SEP-80

PAGE 5

C OPERA	31	2	11816,0	11829,0	14	1	E	L
C OPERA	8	2	11830,0	11843,0	14	1	E	L
C OPERA	9	2	11844,0	12099,0	256	1	E	L


```

C
C      INITIAL MAP=100 SETUP
C
0047  D      INSTN=0
0048  D      STATUS=MODPN(3)
0049  D      IF(STATUS.NE.0)GO TO 10
0050  D      LGIN=-1
0051  D      STATUS=MPINC(0)
0052  D      IF(STATUS.NE.0)GO TO 10
C
C      DEFINE PROGRAM CONSTANTS
C
C      LARGEST FFT SIZE,LMAX
C      SMALLEST FFT SIZE,LMIN
C      LGAT=200000000
C      LMI=200000000
C      FLMAX=FLOAT(LMAX)
C      FLMIN=FLOAT(LMIN)
C      FRAME PROCESSING SIZE,XETH
C      XLTH=FLOAT(LTH)
C      PARMSIT/RECEIVE BUFFER SIZE,ISFN
C      XLSN=FLOAT(LSN)
C      # OF SIDEHAND BITS/FRAME,IIPARM
C      IIPARM=FLOAT(IIPARM)
C      DARG=1.0/FLOAT(LTH4)
C      XLGCLB=ALOG10(16.0)
C
C      MAP SCALAR TABLE SETUP
C
0062  D      DO 1 I=50,127
0063  C      SCALAR(I)=0.0
0064  C      SCALAR(50)=1.0/FLMAX
0065  C      SCALAR(51)=1.0/FLMIN
0066  C      SCALAR(54)=1.0/FLOAT(LTH)
0067  C      SCALAR(56)=DARG
0068  C      SCALAR(57)=-DARG
0069  C      SCALAR(74)=FLOAT(IIPCN+1)
0070  C      SCALAR(75)=FLOAT(IIPCN)
0071  C      SCALAR(76)=4.0
0072  C      SCALAR(77)=10.0E-10
0073  C      SCALAR(79)=FLOAT(HTLTH)
C
C      TYPE 9101
C      FORMAT(IHS,'BIT MAX.= ')
C      READ(5,9002,END=9100,FRR=9100)BITMAX
C      HITMAX=3.
C
C      TYPE 9001
C      SCALAR(M4)=BITMAX
C
C      TYPE 9000
C      FORMAT(IHS,'LPC THRESHOLD= ')
C      READ(5,9002,END=9000,FRR=9000)THRSH
C      THRSH=.995
C
C      TYPE 9002
C      FORMAT(F15.8)
C      SCALAR(M2)=THRSH
C      SCALAR(M7)=FLOAT(LTH)
C      SCALAR(M8)=0.62293E-01
C      SCALAR(M9)=0.44600E+01

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0007 SCALAR(92)=FLOAT(LTH2)
0008 SCALAR(94)=20.0
0009 SCALAR(95)=X(0)*16
0010 SCALAR(97)=FLOAT(LTH-1)
0011 SCALAR(98)=0.5
0012 SCALAR(100)=0.62293E-01
0013 SCALAR(101)=0.44600E+01
0014 SCALAR(102)=0.62293E-01
0015 SCALAR(103)=0.44600E+01
0016 SCALAR(PARITY)=0.0
0017 INSTR=40
0018 STATUS=NP*ST(50,SCALAR(50),11,(NP*5)
0019 IF(STATUS.NE.0)GO TO 10
0020 INSTR=41
0021 STATUS=NP*ST(INIT,0)
0022 IF(STATUS.NE.0)GO TO 10
0023 INSTR=42
0024 STATUS=NP*ST(FCVUFF,-1)
0025 IF(STATUS.NE.0)GO TO 10
0026
0027 C MAP BUFFER SETUP
0028 C
0029 C
0030 C>>>>>>>DEFINE FFT AREAS
0031 C INSTR=1
0032 STATUS=AP*CLH(RUS3*XI,2048,0,FLMAX,CMPLX,EVERY,LONG)
0033 IF(STATUS.NE.0)GO TO 10
0034 C INSTR=4
0035 STATUS=AP*CLH(RUS3*VLONG,4096,0,FLMAX,REAL,EVERY,LONG)
0036 IF(STATUS.NE.0)GO TO 10
0037 C INSTR=5
0038 STATUS=AP*CLH(RUS3*W0RK,0,0,FLMAX,CMPLX,EVERY,LONG)
0039 IF(STATUS.NE.0)GO TO 10
0040 C INSTR=6
0041 STATUS=AP*DCV(UNITY,1,LMAX,REAL)
0042 IF(STATUS.NE.0)GO TO 10
0043 C INSTR=7
0044 STATUS=VCOS(VLONG,0,UNITY,50,UNITY,0)
0045 IF(STATUS.NE.0)GO TO 10
0046 C INSTR=8
0047 STATUS=AP*DCV(UNITY,1,LTH,REAL)
0048 IF(STATUS.NE.0)GO TO 10
0049 C INSTR=9
0050 STATUS=AP*CLH(RUS2*COSZ,1024,0,XLTH,REAL,EVERY,LONG)
0051 IF(STATUS.NE.0)GO TO 10
0052 C INSTR=10
0053 STATUS=VCOS(COSZ,0,UNITY,56,UNITY,0)
0054 IF(STATUS.NE.0)GO TO 10
0055 C INSTR=11
0056 STATUS=AP*CLH(RUS3*X2,2048,0,FL*16,CMPLX,EVERY,LONG)
0057 IF(STATUS.NE.0)GO TO 10
0058 C INSTR=12
0059 STATUS=AP*CLH(RUS3*V512,0,FL*16,REAL,EVERY,LONG)
0060 IF(STATUS.NE.0)GO TO 10
0061 C INSTR=13
0062 STATUS=AP*DCV(UNITY,1,LM16,REAL)
0063 C INSTR=132

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[illegible]


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0293 D INSTH=1206
0294 STATUS=MPCLH(RUS2+MIHIT3,11304.0,XLTH,INTGR,EVEKY,LANG)
0295 IF (STATUS.NE.0)GOTO 10
0296 D INSTH=1207
0297 STATUS=MPCLH(RUS2+MIHIT4,11360.0,XLTH,INTGR,EVEKY,LANG)
0298 IF (STATUS.NE.0)GOTO 10
0299 D INSTH=1208
0300 STATUS=MPCLH(RUS2+OTUCT1,11048.0,XLTH,INTGR,EVEKY,LANG)
0301 IF (STATUS.NE.0)GOTO 10
0302 D INSTH=1209
0303 TYPE 9004
0304 FORMAT(1MS,'CLEAR ALL BUFFER(S)Y/N)? ')
0305 C READ(5,9005,FPO=9999,FPR=9003)ANSER
0306 C FORMAT(A1)
0307 IF (ANSER.EQ.NU)GOTO 9999
0308 C>>>>>CLEAR BUFFERING AREAS
0309 D INSTH=100
0310 STATUS=VCLH(YOM)
0311 IF (STATUS.NE.0)GOTO 10
0312 D INSTH=101
0313 STATUS=VCLH(XOM)
0314 IF (STATUS.NE.0)GOTO 10
0315 D INSTH=2053
0316 STATUS=MPMDH(MOUT1,ZERO(1),YTE2,CNVYES,ZERO(2*NTUPS))
0317 IF (STATUS.NE.0)GOTO 10
0318 D INSTH=2055
0319 STATUS=MPMDH(MIN1,ZERO(1),YTE2,CNVYES,ZERO(2*NTUPS))
0320 IF (STATUS.NE.0)GOTO 10
0321 D INSTH=2057
0322 STATUS=MPMDH(SEM1,ZERO(1),YTE2,CNVYES,ZERO(2*LSSEN))
0323 IF (STATUS.NE.0)GOTO 10
0324 D INSTH=2059
0325 STATUS=MPMDH(RECV1,ZERO(1),YTE2,CNVYES,ZERO(2*LSSEN))
0326 IF (STATUS.NE.0)GOTO 10
0327 C????????????????????????????????????????????????????????????
0328 C MAY NOT BE A GOOD IDEA TO SET ALHIT(1)=0
0329 D INSTH=2061
0330 STATUS=MPMDH(AHIT,ZERO(1),YTE2,CNVYES,ZERO(LTH))
0331 IF (STATUS.NE.0)GOTO 10
0332 D INSTH=2062
0333 STATUS=MPMDH(RHIT,ZERO(1),YTE2,CNVYES,ZERO(LTH))
0334 IF (STATUS.NE.0)GOTO 10
0335 C????????????????????????????????????????????????????????????
0336 D INSTH=2063
0337 STATUS=MPMDH(AQPH,ZERO(1),YTE2,CNVNO,ZERO(LPARK))
0338 IF (STATUS.NE.0)GOTO 10
0339 D INSTH=2065
0340 STATUS=MPMDH(AUTDCT,ZERO(1),YTE2,CNVNO,ZERO(LTH))
0341 IF (STATUS.NE.0)GOTO 10
0342 D INSTH=2066
0343 STATUS=MPMDH(MF1,ZERO(1),YTE2,CNVYES,ZERO(2*LSSEN))
0344 IF (STATUS.NE.0)GOTO 10
0345 D INSTH=2067
0346 STATUS=MPMDH(SYMP1,ZERO(1),YTE2,CNVYES,ZERO(2*LSSEN))

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0331 D IF (STATUS.NE.0)GOTO 10
0332 D INSTR=2068
0333 D STATUS=VCUR(DCT11)
0334 D IF (STATUS.NE.0)GOTO 10
0335 D INSTR=2069
0336 D STATUS=VCUR(DCT112)
0337 D IF (STATUS.NE.0)GOTO 10
0338 D INSTR=2077
0339 D STATUS=MP*DB(OTUCT1,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0340 D IF (STATUS.NE.0)GOTO 10
0341 D INSTR=2078
0342 D STATUS=MP*DB(OPRM,ZERO(1),BYTE2,CVVES,ZERO(LPARM+1))
0343 D IF (STATUS.NE.0)GOTO 10
0344 D INSTR=2079
0345 D STATUS=MP*DB(OPRM,ZERO(1),BYTE2,CVVES,ZERO(LPARM+1))
0346 D IF (STATUS.NE.0)GOTO 10
0347 D INSTR=2080
0348 D STATUS=MP*DB(NOUT1,ZERO(1),BYTE2,CVVES,ZERO(NUTPS))
0349 D IF (STATUS.NE.0)GOTO 10
0350 D INSTR=2081
0351 D STATUS=MP*DB(NOUT2,ZERO(1),BYTE2,CVVES,ZERO(NUTPS))
0352 D IF (STATUS.NE.0)GOTO 10
0353 D INSTR=2082
0354 D STATUS=MP*DB(ZRO,ZERO(1),BYTE2,CVVES,ZERO(LSER))
0355 D IF (STATUS.NE.0)GOTO 10
0356 D IF 9006 1=1,256
0357 D ZPNO(1)=1-1
0358 D INSTR=2071
0359 D STATUS=MP*DB(IORD1,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0360 D IF (STATUS.NE.0)GOTO 10
0361 D INSTR=2072
0362 D STATUS=MP*DB(IORD2,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0363 D IF (STATUS.NE.0)GOTO 10
0364 D IF 9007 1=1,134
0365 D ZPNO(1)=2
0366 D IF 9008 1=135,256
0367 D ZPNO(1)=0
0368 D INSTR=2073
0369 D STATUS=MP*DB(MINT1,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0370 D IF (STATUS.NE.0)GOTO 10
0371 D INSTR=2074
0372 D STATUS=MP*DB(MINT2,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0373 D IF (STATUS.NE.0)GOTO 10
0374 D INSTR=2075
0375 D STATUS=MP*DB(MINT3,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0376 D IF (STATUS.NE.0)GOTO 10
0377 D INSTR=2076
0378 D STATUS=MP*DB(MINT4,ZERO(1),BYTE2,CVVES,ZERO(LTH))
0379 D IF (STATUS.NE.0)GOTO 10
0380 D IF 9009 1=1,134
0381 D ZPNO(1)=0
0382 D CORR=000
0383 D TYPE=101
0384 D FORMAT(1A,' (1) MAP OPERAND & OFFERS DECLARED')
0385 D RETURN

```

```

C      DIAGNOSTIC TRAP AREA
C
0186      TO      T00, INSTR, STATUS
0187      CALL EXIT
0188      TO      100
0189      FORMAT(IX,100MAP FROM*** "MAPON" INSTR=,I4, HAS STATUS=,I6/)
      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCUD01	010564	234
SPDATA	000374	126
SLDATA	002344	626
SVARS	000134	46
STMPES	000004	2
STAP00	002260	600
STAP01	000012	5
STAPE2	000012	5
OVRI	002020	570
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVVP	002022	521
OVRR	005004	1282
OVW2	001002	257
OVWC	000016	7
OVWD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 040666 H411

MAPON,LP/DI:1=MAPON/DE/NOTH

0028 DATA RIDR2, BASE2, SIZE2/63, MR00, 0, 1200, 0/
 0029 DATA RIDR3, BASE3, SIZE3/63, 14000, 0, 2380, 0/

CONFIGURE 5 BIND FIRST BUFFER REGION

```

0030 C
0031 C
0032 C INSTR=100
0033 C STATUS=MPCLR(RUSI+RIDR1, BASE1, SIZE1, INTR, EVERY, LONG)
0034 C IF (STATUS, NE, 0) GO TO 10
0035 C INSTR=1
0036 C STATUS=MPCHF(RIDR1, 153)
0037 C IF (STATUS, NE, 0) GO TO 10
0038 C INSTR=153
0039 C STATUS=MPFVDM(MAPX, MINM, X2)
0040 C IF (STATUS, NE, 0) GO TO 10
0041 C INSTR=154
0042 C STATUS=MPDCTM(DCTM, X1, X2, COSZ)
0043 C IF (STATUS, NE, 0) GO TO 10
0044 C INSTR=155
0045 C STATUS=MPMWGX(R1, MINM, KE, A1)
0046 C IF (STATUS, NE, 0) GO TO 10
0047 C INSTR=156
0048 C STATUS=MPQDPP(OPRM, PARC, A1)
0049 C IF (STATUS, NE, 0) GO TO 10
0050 C INSTR=157
0051 C STATUS=MPFSTV(A1)
0052 C IF (STATUS, NE, 0) GO TO 10
0053 C INSTR=158
0054 C STATUS=MPPRASP(DCTM1, PMFITM, TMP2, X1)
0055 C IF (STATUS, NE, 0) GO TO 10
0056 C INSTR=159
0057 C STATUS=MPASRT(IORDM)
0058 C IF (STATUS, NE, 0) GO TO 10
0059 C INSTR=160
0060 C STATUS=MPSCAN(DCTM2)
0061 C IF (STATUS, NE, 0) GO TO 10
0062 C INSTR=161
0063 C STATUS=MPCDH(MIRIT3)
0064 C IF (STATUS, NE, 0) GO TO 10
0065 C INSTR=162
0066 C STATUS=MPFSTQ(OTDCTM, MINIT3, TMP1, TMP2)
0067 C IF (STATUS, NE, 0) GO TO 10
0068 C INSTR=163
0069 C STATUS=MPQDPP(PARC, OPRM, A1)
0070 C IF (STATUS, NE, 0) GO TO 10
0071 C INSTR=164
0072 C STATUS=MPFSTD(IORDCTM, QORDCTM, TMP1, IORDM2)
0073 C IF (STATUS, NE, 0) GO TO 10
0074 C INSTR=165
0075 C STATUS=MPIDCM(X2, DRDCTM, COSZ)
0076 C IF (STATUS, NE, 0) GO TO 10
0077 C INSTR=166
0078 C STATUS=MPVDM(NOUTM, YNM, X2)
0079 C IF (STATUS, NE, 0) GO TO 10
0080 C INSTR=167
0081 C STATUS=MPSSRT(IORDPM)
0082 C IF (STATUS, NE, 0) GO TO 10

```

```

0001 D INSTR=2
0002 STATUS=MPTRF(0)
0003 IF (STATUS.NE.0)GOTO 10
C
C CONFIGURE & BIND SECOND BUFFER REGION
C
0004 INSTR=101
0005 STATUS=MPCLR(BUS1+RIDRF2,BASE2,SIZE2,INTER,EVERY,LONG)
0006 IF (STATUS.NE.0)GOTO 10
0007 INSTR=1
0008 STATUS=MPCRF(RIDRF2,168)
0009 IF (STATUS.NE.0)GOTO 10
0010 INSTR=168
0011 STATUS=FFTN(X2,1,X2,VSHRT,WORK)
0012 IF (STATUS.NE.0)GOTO 10
0013 INSTR=169
0014 STATUS=FFTN(X1,1,X1,VLONG,WORK)
0015 IF (STATUS.NE.0)GOTO 10
0016 INSTR=170
0017 STATUS=FFTR(X1,4,X1,VLONG,WORK)
0018 IF (STATUS.NE.0)GOTO 10
0019 INSTR=171
0020 STATUS=FFTN(X2,1,X2,VSHRT,WORK)
0021 IF (STATUS.NE.0)GOTO 10
0022 INSTR=4
0023 STATUS=MPTRF(0)
0024 IF (STATUS.NE.0)GOTO 10
C
C CONFIGURE & BIND THIRD BUFFER REGION
C
0105 INSTR=102
0106 STATUS=MPCLR(BUS1+RIDRF3,BASE3,SIZE3,INTER,EVERY,LONG)
0107 IF (STATUS.NE.0)GOTO 10
0108 INSTR=5
0109 STATUS=MPCRF(RIDRF3,172)
0110 IF (STATUS.NE.0)GOTO 10
0111 INSTR=172
0112 STATUS=VMOV1(AOPR)
0113 IF (STATUS.NE.0)GOTO 10
0114 INSTR=173
0115 STATUS=VMOV2(ORPPM)
0116 IF (STATUS.NE.0)GOTO 10
0117 INSTR=174
0118 STATUS=VMOV3(RIRIT)
0119 IF (STATUS.NE.0)GOTO 10
0120 INSTR=175
0121 STATUS=MPCDHA(MIRIT1)
0122 IF (STATUS.NE.0)GOTO 10
C>>>> SPECIAL "VMOV" WHICH FUNCTION AS NULL MOVEM W/ 1 FRAME DELAY
C CHANGED NEXT PRE-HD-FCN FOR NO DELAY FOR DEMO *****
C
0123 INSTR=176
0124 STATUS=VMOV(AOPR,AOPR)
0125 IF (STATUS.NE.0)GOTO 10
0126 INSTR=177
0127 STATUS=VMOV(SEM1,AOPR)
0128 IF (STATUS.NE.0)GOTO 10

```

```

0129 D INSTR=178
C CHANGED NEXT LINE FOR DEMO -- TOOK OUT DELAY*****
0130 STATUS=VMOV(ARODCT,AOTDCT)
0131 IF (STATUS.NE.0)GOTO 10
0132 D INSTR=179
0133 STATUS=VMOV(SEN2,AOTDCT)
0134 IF (STATUS.NE.0)GOTO 10
0135 D INSTR=6
0136 STATUS=MPHF(0)
0137 IF (STATUS.NE.0)GO TO 10
C
0138 TYPE 101
0139 FORMAT(1X,' (2) PRE-ROUND FUNCTIONS CREATED!')
0140 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0141 10 TYPE 100,INSTR,STATUS
0142 CALL EXIT
0143 100 FORMAT(1X,'***MAP ERROR*** "CREATE" INSTR=',I4,' HAS STATUS=',I6/)
0144 FND
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	002026	523 RW,I,CON,LCL
SPDATA	000030	12 RW,D,CON,LCL
SIDATA	000524	170 RW,D,CON,LCL
SVARS	000110	36 RW,D,CON,LCL
MTAPE0	002260	600 RW,D,OVR,GRL
MTAPE1	000012	5 RW,D,OVR,GRL
MTAPE2	000012	5 RW,D,OVR,GRL
NVR1	002020	520 RW,D,OVR,GRL
NVR0	002006	515 RW,D,OVR,GRL
NVRF	006016	1543 RW,D,OVR,GRL
NVRA	000152	53 RW,D,OVR,GRL
NVRP	002022	521 RW,D,OVR,GRL
NVRR	005004	1282 RW,D,OVR,GRL
NVR2	001002	257 RW,D,OVR,GRL
NVRL	000016	7 RW,D,OVR,GRL
NVRD	000012	5 RW,D,OVR,GRL
MAPDEF	000200	64 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 027714 6118

CREATE.LP/L1:1=CREATE/DE/NOTH

FORTRAN IV-PLUS V02-S1F 13:28:31 09-AUG-80
 DEFINE.FTN /DE/WR

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0070 C      HF(157)=MPFSTV(A1)
0071 D      STATUS=MPXHF(157)
0072 D      IF(STATUS.NE.0)GO TO 10
0073 C      INSTR=41
0074 D      HF(170)=FF2R(X1,4,X1,VLONG,WORK)
0075 D      STATUS=MPXHF(170)
0076 D      IF(STATUS.NE.0)GO TO 10
0077 D      INSTR=42
0078 C      HF(158)=MPHASP(DCTM1,MPFITM,TMP2,X1)
0079 D      STATUS=MPXHF(158)
0080 D      IF(STATUS.NE.0)GO TO 10
0081 D      INSTR=43
0082 C      HF(159)=MPASRT(IONDRM)
0083 D      STATUS=MPXHF(159)
0084 D      IF(STATUS.NE.0)GO TO 10
0085 D      INSTR=44
0086 C      HF(160)=MPSCAN(DCTM2)
0087 D      STATUS=MPXHF(160)
0088 D      IF(STATUS.NE.0)GO TO 10
0089 D      INSTR=45
0090 C      HF(161)=MPCDRA(MIRIT3)
0091 D      STATUS=MPXHF(161)
0092 D      IF(STATUS.NE.0)GO TO 10
0093 D      INSTR=46
0094 C      HF(162)=MPFSTO(OTDCTM,MIRIT,TMP1,TMP2)
0095 D      STATUS=MPXHF(162)
0096 D      IF(STATUS.NE.0)GO TO 10
0097 D      INSTR=48
0098 C      IF(TIMING.EQ.YES)STATUS=MPXFL(ATCSYN)
0099 D      IF(STATUS.NE.0)GO TO 10
0100 D      INSTR=1050
0101 C      STATUS=MPFEFL(FL1)
0102 D      IF(STATUS.NE.0)GO TO 10
0103 C      LIST "ATCSYN": ATC SYNTHESIZER W/EPROM DECODER & DESERIALIZATION
0104 D      INSTR=1055
0105 C      STATUS=MPRFL(ATCSYN)
0106 D      IF(STATUS.NE.0)GO TO 10
0107 C      *****
0108 C      PATCHED OUT FOLLOWING LINE TO EXECUTE IN "DEMO" MODE IF NOT
0109 C      IN TIMING MODE.
0110 C      IF(LIVE.EQ.YES)GO TO 200
0111 C      IF(TIMING.EQ.YES)GO TO 200
0112 C      C>>>> INTRODUCE "MULTI" MODEM
0113 D      INSTR=580
0114 C      HF(176)=VMUV(ARGPH,AOPH)
0115 C      STATUS=MPXHF(176)
0116 D      IF(STATUS.NE.0)GO TO 10
0117 C      C*****DON'T NEED DELAY FOR DEMO***
0118 D      INSTR=581
0119 C      HF(177)=VMUV(RECV1,AOPH)
0120 C      STATUS=MPXHF(177)
0121 D      IF(STATUS.NE.0)GO TO 10
0122 C      C*****
0123 D      INSTR=582
0124 C      HF(178)=VMUV(ARGDCT,AOTDCT)

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0101 C STATUS=MPXHF(178)
      D IF(STATUS.NE.0)GO TO 10
      C***** REMOVED FOR DEMO *****
      CD INSTR=583
      CC HF(179)=VM0V(MPCV2,AOTDCT)
      C STATUS=MPXHF(179)
      CD IF(STATUS.NE.0)GO TO 10
      C*****
0102 200 CONTINUE
0103 D INSTR=60
      C HF(173)=VM0V2(ORPRM)
0104 C STATUS=MPXHF(173)
0105 D IF(STATUS.NE.0)GO TO 10
0106 D INSTR=61
      C HF(163)=MPDOPP(PARC,ORPRM,A1)
0107 C STATUS=MPXHF(163)
0108 D IF(STATUS.NE.0)GO TO 10
0109 D INSTR=62
      C HF(157)=MPFSTV(A1)
0110 C STATUS=MPXHF(157)
0111 D IF(STATUS.NE.0)GO TO 10
0112 D INSTR=63
      C HF(170)=FF2R(X1,4,X1,VLONG,WORK)
0113 C STATUS=MPXRF(170)
0114 D IF(STATUS.NE.0)GO TO 10
0115 D INSTR=64
      C HF(158)=MPRASP(DCTM1,PWFTM,TMP2,X1)
0116 C STATUS=MPXHF(158)
0117 D IF(STATUS.NE.0)GO TO 10
0118 D INSTR=65
      C HF(167)=MPSSRT(IORDRM)
0119 C STATUS=MPXRF(167)
0120 D IF(STATUS.NE.0)GO TO 10
0121 D INSTR=66
      C HF(160)=MPSCAN(DCTM2)
0122 C STATUS=MPXHF(160)
0123 D IF(STATUS.NE.0)GO TO 10
0124 D INSTR=67
      C HF(175)=MPCDRA(MIRIT1)
0125 C STATUS=MPXHF(175)
0126 D IF(STATUS.NE.0)GO TO 10
0127 D INSTR=670
      C HF(174)=VM0V3(CRIRIT)
0128 C STATUS=MPXHF(174)
0129 D IF(STATUS.NE.0)GO TO 10
0130 D INSTR=68
      C HF(164)=MPFSTD(DRDCTM,ORDCTM,TMP1,IORDR2)
0131 C STATUS=MPXHF(164)
0132 D IF(STATUS.NE.0)GO TO 10
0133 D INSTR=69
      C HF(165)=MPIDCM(X2,DRDCTM,COSZ)
0134 C STATUS=MPXHF(165)
0135 D IF(STATUS.NE.0)GO TO 10
0136 D INSTR=70
      C HF(171)=FFTTIN(X2,1,X2,VSHRT,WORK)
0137 C STATUS=MPXHF(171)

```

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 DFTIME,FTN /DB/WR

```

0138 D IF(STATUS.NE.0)GO TO 10
0139 D INSTR=11
0140 C MP(146)=MPVDM(NMUTM,YDM,X2)
0141 D STATUS=MPXMF(146)
0142 D IF(STATUS.NE.0)GO TO 10
0143 D INSTR=1070
0144 C STATUS=MPFEL(FL2)
0145 C IF(STATUS.NE.0)GOTO 10
0146 C
0147 C LIST "WAIT": THE WAIT LOOP
0148 C INSTR=1040
0149 C STATUS=MPFEL(WAIT)
0150 D IF(STATUS.NE.0)GOTO 10
0151 D INSTR=1085
0152 D STATUS=MPIF(INIT,F0.0,FL4,FL0)
0153 D IF(STATUS.NE.0)GOTO 10
0154 D INSTR=1090
0155 D STATUS=MPIF(SYN,F0.0,ATCSYN,FL0)
0156 D IF(STATUS.NE.0)GOTO 10
0157 D INSTR=1095
0158 D STATUS=MPIF(ANA,F0.0,ATCANA,FL0)
0159 D IF(STATUS.NE.0)GOTO 10
0160 C
0161 C LIST "STRTUP":INITIALIZATION LOOP
0162 C INSTR=1105
0163 D STATUS=MPFEL(STRTUP)
0164 D IF(STATUS.NE.0)GOTO 10
0165 D INSTR=1104
0166 D STATUS=MPIST(INIT,1)
0167 D IF(STATUS.NE.0)GOTO 10
0168 D INSTR=1111
0169 D STATUS=MPIST(OUT,1)
0170 D IF(STATUS.NE.0)GOTO 10
0171 D INSTR=1112
0172 D STATUS=MPIST(INP,1)
0173 D IF(STATUS.NE.0)GOTO 10
0174 D INSTR=1113
0175 D STATUS=MPIST(AFLG,0)
0176 D IF(STATUS.NE.0)GOTO 10
0177 D INSTR=1114
0178 D STATUS=MPIST(SFLG,0)
0179 D IF(STATUS.NE.0)GOTO 10
0180 D INSTR=1115
0181 D STATUS=MPIST(IFLG,0)
0182 D IF(STATUS.NE.0)GOTO 10
0183 D INSTR=1116
0184 D STATUS=MPIST(OFILG,0)
0185 D IF(STATUS.NE.0)GOTO 10
0186 D INSTR=1117
0187 D STATUS=MPIST(SPIN,-1)
0188 D IF(STATUS.NE.0)GOTO 10
0189 D INSTR=1118
0190 D STATUS=MPIST(SPOUT,-1)

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```
0189 D IF (STATUS.NE.0)GOTO 10
0190 D INSTR=1122
0191 STATUS=MPIST(KOUNT,SYNCRNT)
0192 IF (STATUS.NE.0)GOTO 10
0193 D INSTR=1123
0194 STATUS=MPIST(RDATA,-1)
0195 IF (STATUS.NE.0)GOTO 10
0196 D INSTR=1124
0197 STATUS=MPIST(XDATA,-1)
0198 IF (STATUS.NE.0)GOTO 10
0199 D INSTR=1125
0200 STATUS=MPIST(APDNFL,0)
0201 IF (STATUS.NE.0)GOTO 10
0202 D INSTR=1126
0203 STATUS=MPIST(RG0,0)
0204 IF (STATUS.NE.0)GOTO 10
0205 D INSTR=1127
0206 STATUS=MPIST(XG0,0)
0207 IF (STATUS.NE.0)GOTO 10
0208 D INSTR=1128
0209 STATUS=MPIST(RG02,0)
0210 IF (STATUS.NE.0)GOTO 10
0211 D INSTR=1160
0212 STATUS=MPIST(SYNC,0)
0213 IF (STATUS.NE.0)GOTO 10
0214 D INSTR=1161
0215 STATUS=MPIST(RSYN,0)
0216 IF (STATUS.NE.0)GOTO 10
0217 D INSTR=1162
0218 STATUS=MPIST(NFWSYN,0)
0219 IF (STATUS.NE.0)GOTO 10
0220 D INSTR=1163
0221 STATUS=MPIST(OLSYN,0)
0222 IF (STATUS.NE.0)GOTO 10
0223 D INSTR=1164
0224 STATUS=MPIST(LSTEP,0)
0225 IF (STATUS.NE.0)GOTO 10
0226 D INSTR=1165
0227 STATUS=MPIST(FERSYN,0)
0228 IF (STATUS.NE.0)GOTO 10
0229 D INSTR=1137
0230 STATUS=MPIDS(AOM,IOS,ADMPM)
0231 IF (STATUS.NE.0)GOTO 10
0232 D INSTR=1138
0233 STATUS=MPRNS(AOM,IOS,ADMSA)
0234 IF (STATUS.NE.0)GOTO 10
0235 D INSTR=1139
0236 STATUS=MPIDS(ADAM,IOS,ADAMPM)
0237 IF (STATUS.NE.0)GOTO 10
0238 D INSTR=1140
0239 STATUS=MPRNS(ADAM,IOS,ADMSA)
0240 IF (STATUS.NE.0)GOTO 10
0241 D INSTR=1135
0242 STATUS=MPIDS(IOS10,IOS,IOSPM)
0243 IF (STATUS.NE.0)GOTO 10
0244 D INSTR=1136
```

```

0245 STATUS=MPRNS(IOSID,IOS,IUSSA)
0246 IF(STATUS.NF.0)GOTO 10
0247 INSTR=1150
0248 STATUS=MPPEL(FL4)
0249 IF(STATUS.NF.0)GOTO 10
      C
0250 LIST "TIMER": THE ANALYZER/SYNTHESIZER TIMER LOOP
      INSTR=1280
0251 STATUS=MPNFI(TIMER)
0252 IF(STATUS.NF.0)GOTO 10
0253 INSTR=1285
0254 STATUS=MPIIF(0,EO,0,ATCANA,FL0)
0255 IF(STATUS.NF.0)GOTO 10
0256 INSTR=1286
0257 STATUS=MPIIF(1,EO,1,ATCSYN,FL0)
0258 IF(STATUS.NF.0)GOTO 10
0259 INSTR=1290
0260 STATUS=MPPEL(FL7)
0261 IF(STATUS.NF.0)GOTO 10
0262 TYPE 101
0263 FORMAT(IX,' (3) FUNCTION LISTS DEFINED!')
0264 RETURN
      C
      C DIAGNOSTIC TRAP AREA
      C
0265 TYPE 100,INSTR,STATUS
0266 CALL EXIT
0267 FORMAT(IX,'***MAP ERROR*** "DEFINE" INSTR=',I3,' HAS STATUS=',I6/)
0268 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	003320	472 RW,I,CON,I,CL
SPDATA	000150	52 RW,D,CON,I,CL
SYDATA	000714	230 RW,D,CON,I,CL
SVARS	000266	91 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,NVR,CHL
MTAPE1	000012	5 RW,D,NVR,CHL
MTAPE2	000012	5 RW,D,NVR,CHL
NVR1	002020	520 RW,D,NVR,CHL
NVR0	002006	515 RW,D,NVR,CHL
NVRF	006016	1543 RW,D,NVR,CHL
NVRA	000152	53 RW,D,NVR,CHL
NVRP	002022	521 RW,D,NVR,CHL
NVRH	005004	1282 RW,D,NVR,CHL
NVR2	001002	257 RW,D,NVR,CHL
NVRL	000016	7 RW,D,NVR,CHL
NVRD	000012	5 RW,D,NVR,CHL
NAPDEF	000200	64 RW,D,NVR,CHL

TOTAL SPACE ALLOCATED = 031674 6672

FORTRAM IV-PLUS V02-51F
DEFINE.FTN /DE/WW

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NO PDP INSTRUCTIONS GENERATED

DEFINE.LP/1.1:1=DEFINE/DE/NOTR


```

FORTRAN IV-PLUS V02-51F 13:28:51 09-AUG-80 PAGE 2
SETUP.FTN /DE/WR

00026 EQUIVALENCE (IOSPMR(1),XR(1)),(PRGRM(1),DCT2(1)),(MSGERR,ITOT)
00027 DATA REAL,CNPLX,INTGR/0.1,2/
00028 DATA ERYE,SINGLE,DOUBLE/1,2,3/
00029 DATA RYTE2,RYTE4,RYTE8/2,4,8/
00030 DATA LONG,SHORT,CNVYS,CNVN/0.1,1.0/
00031 DATA RUS1,RUS2,RUS3/64,128,192/
00032 DATA MONU,DUAL,INT,FXT,DAFLTP,DAFLXP/0.1,0.2,0.4/
00033 DATA CCOFSR,CCOSSR,CC1FSR,CC1SSR/0.8,0.16/
00034 DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0.32,0.64/
00035 DATA DFFLT,DFFFT/0.128/
00036 DATA OFFSET,INTT,FXTT/7.0,1/
00037 DATA PZDIS,PZEN/0.4/
00038 DATA CHANS1/1,-1/
00039 DATA IOSPMR,OVRHD/600*0.12./
00040 DATA IOSPM,IOS,IOSRA,IOSSA/61.2,22100.,0./
00041 DATA ADMPM,ADMPHA,ADMSA,ADMSZ/62,22700.,0.,512./
00042 DATA ADAMPM,ADAMRA,ADAMS7/63,23300.,0.,512./
00043 DATA HEXC/0,'1','2','3','4','5','6','7','8','9',
1'A','B','C','D','E','F'/
00044 DATA CWDRD/'E','C','C','C','4'/

C
C
C
SFT UP IOS-2(MODEM SCROLL)

00045 CALL ASSIGN(3,'SYSLINE,PRJ')
00046 CALL LOAD
00047 IF(MSGERR.EQ.0)GO TO 2000
00048 TYPE 2001
00049 FORMAT(IX,'***FATAL: READ ERROR ON "LINE,PRJ" ***'//)
00050 STOP
00051 CONTINUE

C
C
C
IOSM(IOS-2 SCROLL)
CALL IOSM

C
C
ADM(D/A SCROLL)
CALL ADM

C
C
ADM(A/D SCROLL)
CALL ADM
RETURN

C
C
DIAGNOSTIC TRAP AREA

C
C
TYPE 100,INSTR,STATUS
CALL EXIT
00056
00057
00058
00059
FORMAT(IX,'***MAP ERROR*** "SETUP" INSTR='13,' HAS STATUS='16//)
END

```


PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000106	35
SPDATA	000020	8
SIDATA	000152	53
SVARS	000232	77
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVBP	002022	521
OVBB	005004	1287
OVBR	001002	257
OVRL	000016	7
OVRO	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025534 5550

NO FPP INSTRUCTIONS GENERATED

FORTAN IV-PLUS V02-51E
SFTUP.FTN /DE/WR

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SETUP.LP/LI:1=SETUP/DE/NUTR


```

C00000
C      DD=00, NULL, BYTE
C00000
30      LEFT=78
0034      READ(3,116)DT,(REFM(I),I=1,LEFT)
0035      PRINT 113,LINE,DT
0036      GO TO 5

C00000
C      DD=01,SET AUS HITS
C00000
31      LEFT=76
0037      READ(3,116)DT,ZERO,M,(REFM(I),I=1,LEFT)
0038      PRINT 113,LINE,DT,ZERO,M
0039      GO TO 5

C00000
C      DD=02,SET ADDRESS ON AUS
C00000
32      LEFT=72
0040      READ(3,116)DT,ZERO,(H(I),I=1,5),(REFM(I),I=1,LEFT)
0041      PRINT 113,LINE,DT,ZERO,(H(I),I=1,5)
0042      LADDR=0
0043      DO 46 J=1,5
0044      IF(H(J).EQ.WEXC(I))GO TO 46
0045      CONTINUE
0046      TYPE 112,DT,H(J),LINE
0047      IFRR=1
0048      GO TO 3000
0049      LADDR=LADDR+(16,*(5-J))*(I-1)
0050      GO TO 5

C00000
C      DD=10,PROGRAM HEADER
C00000
33      LEFT=0
0051      READ(3,116)DT,(K(I),I=1,78)
0052      PRINT 113,LINE,DT,(K(I),I=1,78)
0053      TYPE 104,(K(I),I=1,78)
0054      GO TO 5

C00000
C      DD=12,MODULE HEADER
C00000
34      LEFT=0
0054      READ(3,103)ONE,H(1),(L(I),I=1,6),(K(I),I=1,72)
0055      PRINT 114,LINE,(ONE,H(1),(L(I),I=1,6),(K(I),I=1,72)
0056      IF(H(1).EQ.ASC111)PRNC=APU
0057      IF(H(1).EQ.ASC112)PRNC=APS
0058      IF(H(1).EQ.ASC113)PRNC=HJM
0059      IF(H(1).EQ.ASC114)PRNC=IOS2
0060      IF(H(1).GT.ASC114)PRNC=UNKN
0061      TYPE 106,(S(I,PRNC),I=1,4)
0062      TYPE 107,(L(I),I=1,6)
0063      GO TO 5

C00000
C      DD=1F,MODULE END
C00000
35      LEFT=0

```

```

0063      READ(3,117)DT,(H(I),I=1,4)
          PRINT 113,LINE,DT,(H(I),I=1,4)
          TYPE 108
          GO TO 5
0064      C#####
          C
          C#####
          36      LEFT=0
0065      READ(3,105)DT,(N(I),I=1,2)
0066      PRINT 119,LINE,DT,(N(I),I=1,2)
          NUMWRD=0
0067      DO 41 J=1,2
0068      DO 40 I=1,16
0069      IF(N(J).EQ.HEXC(1))GO TO 41
0070      CONTINUE
0071      TYPE 112,DT,N(J),LINE
          GO TO 3000
0072      NUMWRD=NUMWRD+(16.*(2-J))*(I-1)
0073      NUMHEX=NUMWRD*4
0074      IF(NUMWRD.EQ.0)GO TO 5
0075      BACKSPACE 3
0076      READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,NUMHEX)
0077      DO 43 I=1,NUMWRD
          XVALUE=0.0
0078      IIRASE=(I-1)*4
0079      DO 44 J=1,4
          JJ=IIRASE+J
0080      DO 42 I=1,16
0081      IF(H(JJ).EQ.HEXC(1))GO TO 44
          CONTINUE
0082      TYPE 112,DT,H(JJ),LINE
          IERR=1
0083      GO TO 3000
0084      XVALUE=XVALUE+(16.*(4-J))*(I-1)
0085      IF(XVALUE.LE.32767.0)GO TO 440
          XVALUE=XVALUE-65536.0
0086      XVALUE=IFIX(XVALUE)
0087      PRINT 120,(S(I,PRNC),I=1,4),ADDR,(H(IIRASE+1),I=1,4),VALUE
0088      PMIADDR=VALUE
0089      ADDR=ADDR+1
0090      GO TO 5
0091      C#####
          C
          C#####
          DO=21,REPEATED 16 HIT DATA
0092      LEFT=72
0093      READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,4),(REFM(I),I=1,LEFT)
0094      PRINT 113,LINE,DT,(N(I),I=1,2)
          GO TO 5
0095      C#####
          C
          C#####
          DO=22,REPEATED 32 HIT DATA
0096      LEFT=68
0097      READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,8),(REFM(I),I=1,LEFT)
          PRINT 113,LINE,DT,(N(I),I=1,2)
          GO TO 5
0098      C#####
          C
          C#####
          38      LEFT=68
0099      READ(3,116)DT,(N(I),I=1,2),(H(I),I=1,8),(REFM(I),I=1,LEFT)
          PRINT 113,LINE,DT,(N(I),I=1,2)
          GO TO 5
0100      C#####

```

```

C
C *****
0101 30 DD=FF,PROGRAM END
0102 30 LEFT=76
      READ(3,116)DT,(F(1),I=1,2),(RM(I),I=1,LEFT)
      PRINT 113,LINE,DT,(F(1),I=1,2)
      TYPE 111,LINE,ADDR-1
      SIZE=ADDR-1
      GO TO 3000
C
C FILE STRUCTURE PROBLEMS
C
0105 2000 TYPE 109
0106 2001 GO TO 3000
0107 2001 TYPE 110
0108 2001 GO TO 3000
C
C NORMAL EXIT
C
0109 3000 CALL CLOSE(3)
0110 3000 END FILE 6
0111 3000 RETURN
C
C FORMAT DECLARATIONS
C
0100 FORMAT(IX,'*** DD ERROR *** DT =',A2,' IN LINE ',I4/)
0101 FORMAT(1HS,'PRJ' FILE TO BE READ =)
0102 FORMAT(1HS,'DMP' FILE TO BE SAVED =)
0103 FORMAT(80A1)
0104 FORMAT(//IX,'PROGRAM HEADER: ',80A1)
0105 FORMAT(A2,2A1)
0106 FORMAT(IX,'SUB-PROCESSOR TYPE: ',4A1)
0107 FORMAT(IX,'SUB-PROCESSOR MODULE HEADER: ',6A1)
0108 FORMAT(IX,'END OF SUB-PROCESSOR MODULE!')
0109 FORMAT(IX,'*** EOF BEFORE PROGRAM END FOUND ***')
0110 FORMAT(IX,'*** PRJ FILE STRUCTURE ERROR ***')
0111 FORMAT(IX,'PROGRAM ENDS W/ LINE ',I4,' & LAST PC=',I8//)
0112 FORMAT(IX,'***HEX CHAR. IS 7*** DT=',A2,' 7=',A1,' IN LINE ',I4/)
0113 FORMAT(IX,'LINE=',I4,' DATA DESCRIPTOR=',I4,A2,' CODE=',80A1)
0114 FORMAT(IX,'LINE=',I4,' DATA DESCRIPTOR=',I4,A1,A1,' CODE=',80A1)
0115 FORMAT(A2,78A1)
0116 FORMAT(A2,4A1)
0117 FORMAT(IX,'LINE=',I4,' DATA DESCRIPTOR=',I4,A2,' LENGTH=',2A1)
0118 FORMAT(IX,4A1,IX,'PM(',I3,')= H:',4A1,2X,'I:',I6)
      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	003030	780 RW,1,COM,I,CL
SPDATA	000070	28 RW,D,COM,I,CL
SIDATA	000146	51 RW,D,COM,I,CL
SVARS	000574	190 RW,D,COM,I,CL
STEPS	000002	1 RW,D,COM,I,CL
OVRR	005004	1282 RW,D,OVR,CHL

FORTRAN IV-PLUS V02-S1P
LOAD.FTN

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OVRL 000016 7
OVRD 000016 7

RW.D.OVR,GRI.
RW.D.OVR,GRI.

TOTAL SPACE ALLOCATED = 011124 2346

LOAD.IF/11:1=LOAD/NOTR


```

C2005 CONTINUE
C TYPE 2003
C2004 FORMAT(1X,'***FATAL: ILLEGAL CHARACTER IN ICM ***')
C STOP
C2006 DATE=DATE+16.0**((4-J))*(1-1)
C2004 CONTINUE
C IF (DATUM.GT.32767.)DATE=DATE+65536.
C IOSPM(4)=FIX(DATUM)
C NEXT FCC4 TO CLOCKS
C IOSPM(4)=4924
0061 C
C
C
C
C IOS-2(400PM SCROLL)
LASTR=1000
STATUS=MPCLR(HUS1+IOSPM,IOSHA,PSIZE+OVRHD,INTGR,EVERY,LONG)
IF (STATUS.NE.0)GOTO 10
C
C
C
C
C IOS-2(400PM SCROLL)
IOVRHD=FIX(OVRHD)
STATUS=MP+DH(IOSPM,IOSPMH(1),BYTE2,CNVHD,IOSPMH(SIZE+IOVRHD))
IF (STATUS.NE.0)GOTO 10
TYPE 101
FORMAT(1X,' (4) IOS-2 SETUP COMPLETE')
RETURN
C
C
C
C
C DIAGNOSTIC TRAP AREA
TYPE 100,INSTR,STATUS
CALL EXIT
FORMAT(1X,'***MAP ERROR*** "IOSM" INSTR=14, HAS STATUS=1,16//')
END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCD001	000622	201
STDATA	000156	55
SVARS	000240	40
STEMPS	000002	1
WTAP0	002260	600
WTAP1	000012	5
WTAP2	000012	5
WVR0	00220	520
WVR0	002006	515
WVR0	006016	1543
WVRA	000152	54
WVRP	002022	521
WVRP	005004	1282
WVR2	001002	257
WVR1	000016	7

FORTRAN IV-PLUS 902-514
 IUSM,PL10 /DE/AR

USER 000012 5
 NAME 000200 64
 PW, D, 000, GRU
 PW, D, 000, GRU

TOTAL SPACE ALLOCATED = 026244 5714

IUSM,LP/L1:1=IUSM/LP/NOTM

FURTHAN	IV-PLUS	V02-51F	13:29:19	09-AUG-80	PAGE 2
A0M.FIN	/MH				


```

0026 EQUIVALENCE (IUSPMR(1),XM(1)),(PROGRAM(1),DCT2(1))
0027 DATA WFAU,CMPUX,INTGR/0,1,2/
0028 DATA EVERY,SINGLEF,DOUBLEF/1,2,3/
0029 DATA RYTF2,RYTF4,RYTF8/2,4,8/
0030 DATA LONG,SHORT,CNVYFS,CNVMO/0,1,1,0/
0031 DATA RUS1,RUS2,RUS3/64,128,192/
0032 DATA MONO,DUAL,INT,EXT,DAFLTP,DAFLXP/0,1,0,2,0,4/
0033 DATA CC0FSR,CC0SSR,CC1FSR,CC1SSR/0,8,0,16/
0034 DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035 DATA D0FFLT,D0FFIX/0,128/
0036 DATA D0FFST,INTT,EXTT/2,0,1/
0037 DATA P20IS,P2EN/0,4/
0038 DATA CHANS1/1,-1/
0039 DATA IUSPMR,OVPHD/600*0,12./
0040 DATA IUSPM,IOS,IOSRA,IOSSA/61,2,22100.,0./
0041 DATA A0MPM,A0MRA,A0MSA,A0MSZ/62,22700.,0.,512./
0042 DATA ADAPM,ADAMRA,ADAMSA,ADAMSZ/63,23300.,0.,512./
0043 DATA HEXC/0,'1','2','3','4','5','6','7','8','9',
      'A','B','C','D','E','F'/
      C A0M(D/A SCROLL)
      C INSTR=1001
      D STATUS=MPCUB(RUS1+A0MPM,A0MRA,A0MSZ,INTGR,EVERY,LONG)
      IF(STATUS.NE.0)GOTO 10

      C A0M(D/A SCROLL)
      C KNTRL=M0NU+DAFIXP+EXT
      FREQ=1.0
      INSTR=2010
      D STATUS=A0MID(A0MPM,FREQ,KNTRL,NOUT1,NOUT2,OFFSET)
      IF(STATUS.NE.0)GOTO 10
      TYPE 101
      101 FORMAT(1X,' (5) A0M SETUP COMPLETE!')
      RETURN

      C DIAGNOSTIC TRAP AREA
      C
      C TYPE 100,INSTR,STATUS
      CALL EXIT
      100 FORMAT(1X,'***MAP ERROR*** "A0M" INSTR=',I3,' HAS STATUS=',I6/)
      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000136	47
SIDATA	000156	55
SVARS	000240	40
WTAPF0	002260	600
WTAPF1	000012	5
WTAPF2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVPP	002022	521
OVRR	005004	1262
OVZ	001002	257
OVRL	000016	7
OVRO	000012	5
WAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025556 5559

FORTRAN IV-PLUS V02-51E
AUM.FTH

13:29:23

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ANN.SP/1.11=AUM/MNTR


```

0026      EQUIVALENCE (IOSPMH(1),XR(1)),(PROGRM(1),DCT2(1))
0027      DATA REAL,CMPLEX,INTGR/0,1,2/
0028      DATA EVERY,SINGLF,DOUBLE/1,2,3/
0029      DATA BYTE2,BYTE4,BYTE8/2,4,8/
0030      DATA LONG,SHORT,CNVYFS,CNVNO/0,1,1,0/
0031      DATA MUS1,MUS2,MUS3/64,128,192/
0032      DATA MONO,DUAL,INT,EXT,DAFLTP,DAFLXP/0,1,0,2,0,4/
0033      DATA CC0FSR,CC0SSR,CC1FSR,CC1SSR/0,8,0,16/
0034      DATA CC2FSR,CC2SSR,CC3FSR,CC3SSR/0,32,0,64/
0035      DATA DDFILT,DDFLX/0,128/
0036      DATA OFFSET,INTT,EXTT/2,0,1/
0037      DATA P2DIS,P2FN/0,4/
0038      DATA C,MUS1/1,-1/
0039      DATA IOSPMH,OVRHD/600*0,12/
0040      DATA IOSPM,IOS,IOSHA,IOSSA/61,2,22100,0,0/
0041      DATA ADMPM,ADMPA,ADMSA,ADMSZ/62,22700,0,512/
0042      DATA ADAMP,ADAMRA,ADAMSA,ADAMSZ/63,23300,0,512/
0043      DATA HEXC/0,1,1,2,3,4,5,6,7,8,9,
      1,A,B,C,D,E,F/
C
C      ADAM(A/D SCROLL)
C      INSTH=1002
D      STATUS=MPC1H(RUS1+ADAMP,ADAMRA,ADAMSA,ADAMSZ,INTGR,EVERY,LONG)
C      IF (STATUS.NE.0)GOTO 10
C
C      ADAM(A/D SCROLL)
C      KNTRI=DDFLX+CC0SSR+CC1FSR+CC2SSR+CC3SSR
C      KNTRL=KNTRI+INTT+P2DIS
C      FREQ0=1.
C      KNTRI=KNTRI+EXT
C      FREQ1=FREQ
C      FREQ2=FREQ
C      INSTH=2005
D      STATUS=ADMSD(ADAMP,FREQ1,FREQ2,KNTRI,CHAN$1,MIN1,MIN2)
D      IF (STATUS.NE.0)GOTO 10
101      TYPE 101
C      FORMAT(1X,' (6) ADAM SETUP COMPLETE!')
C      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
C      TYPE 100,INSTR,STATUS
C      CALL EXIT
100      FORMAT(1X,'***MAP ERROR*** "ADAM" INSTR=1,I3,' HAS STATUS=1,I6/')
C      END

```

FORTMAN IV-PLUS V02-S1E
ADAM.FTN /WH

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PAGE 3

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000176	63
STDATA	000162	57
SVARS	000250	84
MTAPF0	002260	600
MTAPF1	000012	5
MTAPF2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVRR	002022	521
OVRL	005004	1282
OVRL	001002	257
OVRL	000016	7
OVRL	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025632 5581

FORTAN IV-PLUS V02-SIE
ADAM.FIN /WR

13:29:30

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ADAM.IP/LI:1=ADAM/NOTR

PROGRAM NAME:SYSTM,PTN ORIG:DATE:13-FEB-80
UPDATE:22-MAY-80[illegible]

PORTMAN IV-PHUS 002-SIP 21:11:48 11-SEP-80
SYSTEMA.PTN 014/000

```

0027 DATA RYTR7,RYTR4,RYTR2,4,8/
0028 DATA LONG,SUMF,CNVS,CNVM0,1,1,0/
0029 DATA RUS1,RUS2,RUS3/64,128,192/
0030 DATA ATCANA,ATCSYN,WAIT,STPTOP,TIMER/1,2,3,4,7/
0031 DATA IUSID,AOM,ADAM/16,22,23/
0032 DATA FQ,NE,GP,GT,DE,LT/0,1,2,3,4,5/
0033 DATA INIT,PARITY/113,114/
0034 DATA CM,BELL/040,5007/

C
C EXECUTE PRE-ROUND FUNCTION LIST
C
0035 IF (RUS1,FO,NO,AND,LIVE,FO,NO) RETURN
0036 IF (TIMING,FO,NO) GO TO 1
C>>>>>> FOR TIMING PURPOSES ONLY!
0037 D INSTR=0
0038 D STATUS=MPWHL(PARITY,FO,TIMER)
0039 D IF (STATUS,NE,0) GO TO 10
0040 CALL DATE (ARRAY1)
0041 CALL TIME (ARRAY2)
0042 3 TYPE 20,BELL,ARRAY1,ARRAY2
0043 20 FORMAT(//1X,'PDP <=> MAP LINKAGE HAS BEEN SUSPENDED',5A1,/
1,AX,9A1,' AT ',HAI/
2,1X,'TYPE "RES ATCA" TO TERMINATE'//)
CALL SUSPND
GO TO 1000

C>>>>>> ANALYZER
0044 1 IF (LIVE,FO,YES) GO TO 2
0045 D INSTR=10
0046 STATUS=MPXFL(ATCANA)
0047 D IF (STATUS,NE,0) GO TO 10
C>>>>>> SYNTHESIZER
0048 D INSTR=11
0049 STATUS=MPXFL(ATCSYN)
0050 D IF (STATUS,NE,0) GO TO 10
0051 RETURN
C>>>>>> REAL-TIME LIVE SYSTEM
0052 2 CONTINUE
0053 D INSTR=2090
0054 STATUS=MPWHL(PARITY,FO,WAIT)
0055 D IF (STATUS,NE,0) GO TO 10
0056 CALL DATE (ARRAY1)
0057 CALL TIME (ARRAY2)
0058 TYPE 20,BELL,ARRAY1,ARRAY2
0059 CALL SUSPND
0060 INSTR=0002
0061 STATUS=MPSAAL(AOM)
0062 D IF (STATUS,NE,0) GO TO 10
0063 D INSTR=0003
0064 STATUS=MPSAAL(ADAM)
0065 D IF (STATUS,NE,0) GO TO 10
0066 D INSTR=0004
0067 STATUS=MPDPL(ATCANA)
0068 D IF (STATUS,NE,0) GO TO 10
0069 D INSTR=0005
0070 STATUS=MPDPL(ATCSYN)
0071 D IF (STATUS,NE,0) GO TO 10
0072 D INSTR=0006
0073 STATUS=MPDPL(ATCSYN)

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000670	220
SPDATA	000004	2
SIDATA	000336	115
SVAMS	000170	60
WTAP0	002260	600
WTAP1	000012	5
WTAP2	000012	5
WVRI	002020	520
WVR0	002008	515
WVRF	006016	1543
WVRA	000152	53
WVRD	002022	521
WVRH	005004	1282
WVR2	001002	257
WVRL	000016	7
WVRD	000012	5
WAP0F	000174	62

TOTAL SPACE ALL CAPED = 026430 5772

NO FOR INSTRUCTIONS GENERATED

SYSTMA,LP/1:1=SYSTMA/DE/MUTR


```

C
0026 IF(ICOUNT)5012,5010,5012
0027 NTOTI=NP
0028 NTUPS=NP
0029 CALL TAPE2(8)
0030 CALL TAPE2(1)
0031 INSTR=1
0032 STATUS=MPMDR(NINM,NIN(1),HYTE2,CNVVES,NIN(NP))
0033 IF(STATUS.NE.0)GO TO 10
0034 INSTR=2
0035 STATUS=VFILT(XOM,39,NINM,0)
0036 IF(STATUS.NE.0)GO TO 10
0037 NTOTI=LTN-NP
0038 NTUPS=LTN-NP
0039 CALL TAPE2(1)
0040 IF(NEND.NE.0)GO TO 5000
0041 IF(NERR.NE.0)GO TO 5000
0042 ICOUNT=ICOUNT+1
0043 IF(ICOUNT-LT-START)GO TO 5012
0044 IF((ICOUNT-START+1).GT.JHAC)GO TO 5000
0045 WRITE(6,899)ICOUNT
0046 FORMAT(1H1,40(1H>),'TRANSMIT FRAME # ',14,40(1H<))
0047 INSTR=11
0048 STATUS=MPMDR(NINM,NIN(1),HYTE2,CNVVES,NIN(NTOTI))
0049 IF(STATUS.NE.0)GO TO 10
0050 INSTR=13
0051 STATUS=VMUV(IMPH,NINM)
0052 IF(STATUS.NE.0)GO TO 10
0053 INSTR=12
0054 STATUS=MPMDR(NINM,NIN(1),HYTE2,CNVVES,NIN(NTOTI))
0055 IF(STATUS.NE.0)GO TO 10
0056 WRITE(6,913)(I,NIN(I),I=1,NTOTI)
0057 FORMAT(1X,4(1X,'NIN(',13,')=',16,2X))
0058 RETURN
C
C FOF ON INPUT,WRITE FOF
C
0059 NEND=1
0060 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0061 10
0062 TYPE 101,INSTR,STATUS
0063 CALL EXIT
0064 FORMAT(1X,'***MAP ERROR*** "INPUT" INSTR=',13,' HAS STATUS=',16/)
END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000672	201 RW,I,CUN,LCL
SPDATA	000020	H RW,D,CUN,LCL
SIDATA	000230	76 RW,D,CUN,LCL
SVARS	000054	22 RW,D,CUN,LCL

FORTRAN IV-PLUS V02-51E
INPUT.FTN /OE/WR

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MTAPE0	002260	600	RW,D,OVR,GRI,
MTAPE1	000012	5	RW,D,OVR,GRI,
MTAPE2	000012	5	RW,D,OVR,GRI,
OVRI	002020	520	RW,D,OVR,GRI,
OVRO	002006	515	RW,D,OVR,GRI,
OVRF	006016	1543	RW,D,OVR,GRI,
OVRA	000152	53	RW,D,OVR,GRI,
OVVP	002022	521	RW,D,OVR,GRI,
OVRR	005004	1282	RW,D,OVR,GRI,
OVRI	001002	257	RW,D,OVR,GRI,
OVRI	000016	7	RW,D,OVR,GRI,
OVRO	000012	5	RW,D,OVR,GRI,
MAPDEF	000200	64	RW,D,OVR,GRI,

TOTAL SPACE ALLOCATED = 026150 56R4

INPUT.LP/LI:1=INPUT/DF/NOTR


```

C
C      SERIALIZE SIDEHAND W/ 1 FRAME DELAY ...
C      AND MAINHAND W/ 2 FRAMES DELAY
C
0028 D      INSTR=0
0029 D      STATUS=VMNOVI(AOPR)
0030 D      IF (STATUS.NE.0) GOTO 10
C
C      CALCULATE DC & VAR THEN NORMALIZE & COMPRESS
C
0031 D      INSTR=1
0032 D      STATUS=MPFDVM(MAPX,NIMM,X2)
0033 D      IF (STATUS.NE.0) GO TO 10
C
C      RESULTS
C
0034 D      CALL MPRDR(MINM,NIN(1),HYTE2,CNVYES,NIN(NTOTI))
0035 D      WRITE(6,898)(1,MIN(1),I=1,NTOTI)
0036 D      FORMAT(1X,4(1X,'MIN(',I3,')='',16,2X))
0037 D      CALL MPRST(52,DCRIAS,1,CNVYES)
0038 D      WRITE(6,900)DCRIAS
0039 D      FORMAT(1X,'DCRIAS=',F12.5)
0040 D      CALL MPRST(55,VAR,1,CNVYES)
0041 D      WRITE(6,901)VAR
0042 D      FORMAT(1X,'VAR=',F12.5)
0043 D      CALL MPRDR(X2,XR(1),HYTE4,CNVYES,XR(LTH))
0044 D      WRITE(6,920)(1,XR(1),I=1,LTH)
0045 D      FORMAT(1X,4(1X,'XR(',I3,')='',F15.8,2X))
0046 D      CALL MPRDH(COSZ,XR(1),HYTE4,CNVYES,XR(LTH))
0047 D      WRITE(6,922)(1,XR(1),I=1,LTH)
0048 D      FORMAT(1X,4(1X,'COS(',I3,')='',F15.8,2X))
0049 D      CALL MPRDR(AOPR,NOUT(1),HYTE2,CNVYES,NOUT(LPARM))
0050 D      WRITE(6,899)(1,NOUT(1),I=1,LPARM)
0051 D      FORMAT(1X,4(1X,'AOPR(',I3,')='',16,2X))
0052 D      CALL MPRDH(AOTDCT,OTDCT(1),HYTE2,CNVYES,OTDCT(LTH))
0053 D      WRITE(6,910)(1,OTDCT(1),I=1,LTH)
0054 D      FORMAT(1X,4(1X,'AOTDCT(',I3,')='',16,2X))
0055 D      CALL MPRDH(AIHT,IHT(1),HYTE2,CNVYES,IHT(LTH))
0056 D      WRITE(6,911)(1,IHT(1),I=1,LTH)
0057 D      FORMAT(1X,4(1X,'AIHT(',I3,')='',16,2X))
0058 D      CALL MPRDH(MINIT4,IHT(1),HYTE2,CNVYES,IHT(LTH))
0059 D      WRITE(6,902)(1,IHT(1),I=1,LTH)
0060 D      FORMAT(1X,4(1X,'MINIT4(',I3,')='',16,2X))
0061 D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
0062 I0     TYPE 101, INSTR, STATUS
0063       CALL EXIT
0064 101    FORMAT(1X, ****MAP ERROR*** "DCVAR" INSTR=',I4,' HAS STATUS=',I6/)
0065       END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
0062		
0063		
0064	101	
0065		

FURTRAN IV-PLUS V02-SIE
DCVAR.FTH /DF/WH

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SCONE1	001334	366	RM,I,CON,I,CL
SPDATA	000014	6	RM,D,CON,I,CL
SIGDATA	000576	191	RM,D,CON,I,CL
SVARS	000054	27	RM,D,CON,I,CL
MTAPE0	002260	600	RM,D,OVR,GRI
MTAPE1	000012	5	RM,D,OVR,GRI
MTAPE2	000012	5	RM,D,OVR,GRI
OVR1	002020	520	RM,D,OVR,GRI
OVR0	002006	515	RM,D,OVR,GRI
OVRF	006016	1543	RM,D,OVR,GRI
OVR4	000152	53	RM,D,OVR,GRI
OVRP	002022	521	RM,D,OVR,GRI
OVR8	005004	1282	RM,D,OVR,GRI
OVR2	001002	257	RM,D,OVR,GRI
OVR1	000016	7	RM,D,OVR,GRI
OVR0	000012	5	RM,D,OVR,GRI
OVR0T	001032	269	RM,D,OVR,GRI
MAPDEF	000200	64	RM,D,OVR,GRI

TOTAL SPACE ALLOCATED = 030256 6231

DCVAR.I.P/I.I:1=DCVAR/DF/NOTR

FORTRAN IV-PLUS V02-51F 13:29:5R 09-AUG-80

DCTF.FTN /DP/WR

APPLY POST WEIGHTING W/ HALF SAMPLE DELAY

```

0026 C
0027 C INSTR=1
0028 STATUS=FFTM(X2,1,X2,VSHRT,WORK)
0029 IF (STATUS.NE.0) GO TO 10
0030 INSTR=2
0031 STATUS=MPDCTM(DCTM,X1,X2,CONS)
0032 IF (STATUS.NE.0) GO TO 10
0033 INSTR=3
0034 STATUS=FFTM(X1,1,X1,VLONG,WORK)
0035 IF (STATUS.NE.0) GO TO 10
0036 C
0037 C RESULTS
0038 C
0039 CALL MPRDHA(DCTM,DCT(1),HYTF4,CNVYES,DCT(1,TH))
0040 WHITE(6,907)(1,DCT(1),1=1,1TH)
0041 FORMAT(1X,4(1X,'DCT(',13,')=' ,E15.8,2X))
0042 RETURN

```

DIAGNOSTIC TRAP AREA

```

0039 10 TYPE 100, INSTR, STATUS
0040 CALL EXIT
0041 FORMAT(1X, '***MAP ERROR*** "DCTF" INSTR=',13, ' HAS STATUS=',16/)
0042 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000320	104 RW,I,CON,LCL
SPDATA	000004	2 RW,D,CON,LCL
SIDATA	000176	63 RW,D,CON,LCL
SVARS	000054	22 RW,D,CON,LCL
MTAPE0	002260	600 RW,D,OVR,GRU
MTAPE1	000012	5 RW,D,OVR,GRU
MTAPE2	000012	5 RW,D,OVR,GRU
OVR1	002020	520 RW,D,OVR,GRU
OVR0	002006	515 RW,D,OVR,GRU
OVRF	000016	1543 RW,D,OVR,GRU
OVRH	000152	53 RW,D,OVR,GRU
OVRP	002022	521 RW,D,OVR,GRU
OVRB	005004	1282 RW,D,OVR,GRU
OVR2	001002	257 RW,D,OVR,GRU
OVR1	000016	7 RW,D,OVR,GRU
OVRD	000012	5 RW,D,OVR,GRU
MAPDEF	000200	64 RW,D,OVR,GRU

TOTAL SPACE ALLOCATED = 025600 556R

DCTF.LP/1.1=DCTF/DP/WR

FORTRAN IV-PLUS V02-51F
PITLPC.FTN /DF/WR

13:30:04 00-AUG-80

PAGE 3

OVRA	000152	53	RM.D.OVR.GRI.
OVRA	002027	521	RM.D.OVR.GRI.
OVRA	005004	1282	RM.D.OVR.GRI.
OVRA	001002	257	RM.D.OVR.GRI.
OVRA	000016	7	RM.D.OVR.GRI.
OVRA	000012	5	RM.D.OVR.GRI.
OVRA	000200	64	RM.D.OVR.GRI.

TOTAL SPACE ALLOCATED = 026556 5815

PITLPC.LP/LI:1=PITLPC/DE/MOTR


```

0028      DATA HUS1,HUS2,HUS3/64,128,192/
C
C
C
0029      QUANTIZE PARCOR(J),J=1,...,LPCN,DCHIAS,VAR,G,M
0030      DEQUANTIZE PARCOR(J),J=1,...,LPCN,G,M
C
C
C
0031      INSTR=1
0032      STATUS=MPQDP(PARM,PARC,A1)
0033      IF(STATUS.NE.0)GO TO 10
C
C
C
0034      RESULTS
C
C
C
0035      CALL MPDRH(OPRM,NOUT(1),RYTF2,CNVYES,NOUT(LPARM+1))
0036      DO 651 I=1,LPCN
0037      OUPAR(I)=NOUT(I)
0038      WRITE(6,924)(J,OTPAR(J),J=1,LPCN)
0039      FORMAT(1X,4(1X,'OTK(',12,')=' ,16,2X))
0040      OTVAR=NOUT(LPCN+1)
0041      WRITE(6,926)OTVAR
0042      FORMAT(1X,'OTVAR=' ,16)
0043      QIDC=NOUT(LPCN+2)
0044      WRITE(6,925)QIDC
0045      FORMAT(1X,'QIDC=' ,16)
0046      QTM=NOUT(LPCN+3)
0047      OTM=NOUT(LPCN+4)
0048      OTV=NOUT(LPCN+5)
0049      WRITE(6,927)QTM,OTG,OTV
0050      FORMAT(1X,'OTM=' ,16, ', OTG=' ,16, ' AND OTV=' ,16)
0051      CALL MPDRH(PARC,OTPAR(1),RYTF4,CNVYES,OTPAR(LPCN))
0052      WRITE(6,928)(J,OTPAR(J),J=1,LPCN)
0053      FORMAT(1X,4(1X,'OTK(',12,')=' ,16,2X))
0054      CALL MPST(RR,OTDC,1,CNVYES)
0055      CALL MPST(R9,OTVAR,1,CNVYES)
0056      CALL MPST(Q1,DTM,1,CNVYES)
0057      CALL MPST(104,DTV,1,CNVYES)
0058      WRITE(6,929)DTM,DTG,DTV
0059      FORMAT(1X,'DTM=' ,F15.8, ' DTG=' ,F15.8, ' AND DTV=' ,F15.8)
0060      CALL MPDRH(A1,DTA(1),RYTF4,CNVYES,DTA(LPCN))
0061      WRITE(6,931)(J,DTA(J),J=1,LPCN)
0062      FORMAT(1X,4(1X,'DTA(',12,')=' ,16,2X))
0063      CALL MPST(60,ENG,1,CNVYES)
0064      WRITE(6,932)ENG
0065      FORMAT(1X,'ENG=' ,F15.8)
0066      RETURN
C
C
C
0067      DIAGNOSTIC TRAP AREA
C
C
C
0068      TYPE 100, INSTR, STATUS
0069      CALL EXIT
0070      FORMAT(1X, '====MAP ERRORS==== "QDPARM" INSTR=' ,13, ' HAS STATUS=' ,16 /)
0071      END

```

PROGRAM SECTIONS

NAME SIZE ATTRIBUTES

FORTHAN IV-PLUS V02-51F
QDPARM.FTN /04/WR

13:30:12 09-AUG-80

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SC0061	001102	289	RM,I,CON,I,CL
SPDATA	000034	14	RM,D,CON,I,CL
SICDATA	000530	172	RM,D,CON,I,CL
SVARS	000056	23	RM,D,CON,I,CL
MTAPE0	002260	600	RM,D,OVR,GHL
MTAPE1	000012	5	RM,D,OVR,GHL
MTAPE2	000012	5	RM,D,OVR,GHL
OVR1	002020	520	RM,D,OVR,GHL
OVR0	002006	515	RM,D,OVR,GHL
OVR6	006016	1543	RM,D,OVR,GHL
OVR5	000152	53	RM,D,OVR,GHL
OVRP	002022	521	RM,D,OVR,GHL
OVR8	005004	1282	RM,D,OVR,GHL
OVR2	001002	257	RM,D,OVR,GHL
OVR1	000016	7	RM,D,OVR,GHL
OVR0	000012	5	RM,D,OVR,GHL
OVR0T	001032	269	RM,D,OVR,GHL
OVR0T	002124	554	RM,D,OVR,GHL
MAP0FF	000200	64	RM,D,OVR,GHL

TOTAL SPACE ALLOCATED = 032124 669H

NO FPP INSTRUCTIONS GENERATED

QDPARM,LP/LI:1=QDPARM/DE/NOTR


```

C
0026 D INSTR=1
0027 STATUS=MPFSTV(A1)
0028 IF (STATUS.NE.0) GO TO 10
0029 INSTR=2
0030 STATUS=FF2R(X1.4,X1.VLONG,WORK)
0031 IF (STATUS.NE.0) GO TO 10
C
C RESULTS
C
0032 CALL MPNST(7,FGH,I,CNVYES)
0033 WITH(6,906)K,FGH
0034 FORMAT(1X,'K=',I3,' AND FGH=',F15.8)
0035 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0036 10 TYPE 100, INSTR, STATUS
0037 CALL EXIT
0038 FORMAT(1X,'***MAP ERROR*** VPMS INSTR=',I3,' HAS STATUS=',I6/)
0039 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	000208	67 RW,I,CON,I,CL
SPDATA	000014	6 RW,D,CON,I,CL
SIDATA	000150	52 RW,D,CON,I,CL
SVARS	000060	24 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
OVRI	002020	520 RW,D,OVR,GHL
OVR0	002006	515 RW,D,OVR,GHL
OVRF	006016	1543 RW,D,OVR,GHL
OVPA	000152	53 RW,D,OVR,GHL
OVRP	002022	521 RW,D,OVR,GHL
OVRH	005004	1282 RW,D,OVR,GHL
OVR2	001002	257 RW,D,OVR,GHL
OVR1	000016	7 RW,D,OVR,GHL
OVRD	000012	5 RW,D,OVR,GHL
MAP0FF	000200	64 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 025454 5526

VPMS.LP/I.1:1=VPMS/DE/NOTR


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FORTRAN IV-PLUS V02-51F
BASIS.FTN /Dk/mr 11:30:26 09-AUG-80 PAGE 2

C
0026 D INSTR=1
0027 D STATUS=MPHASP(DCTM1,DWFTM,TMP2,X1)
0028 D IF (STATUS.NE.0)GO TO 10
C
C RESULTS
C
0029 D CALL MWRDR(DCTM1,DCT1(1),RYTF4,CNVVFS,DCT1(LTH))
0030 D WRITE(6,908)(1,DCT1(1),1=L,LTH)
0031 D908 FORMAT(/1X,4(1X,'DCT1(',13,')=',E15.8,2X))
0032 D CALL MPRMR(TMP2,DCT2(1),RYTF4,CNVVFS,DCT2(LTH))
0033 D WRITE(6,909)(1,DCT2(1),1=L,LTH)
0034 D909 FORMAT(/1X,4(1X,'DCT2(',13,')=',E15.8,2X))
0035 D RETURN
C
C DIAGNOSTIC TRAP AREA
C
0036 I0 TYPE 100, INSTR, STATUS
0037 CALL EXIT
0038 I00 FORMAT(1X, '***MAP ERROR*** BASIS* INSTR=',13, ' HAS STATUS=',16/)
0039 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000350	116 RW,1,CON,LCI
SIDATA	000216	71 RW,D,CON,LCI
SVARS	000054	22 RW,D,CON,LCI
MTAPE0	002260	600 RW,D,OVR,GRL
MTAPE1	000012	5 RW,D,OVR,GRL
MTAPE2	000012	5 RW,D,OVR,GRL
OVRI	002020	520 RW,D,OVR,GRL
OVRO	002006	515 RW,D,OVR,GRL
OVRF	006016	1543 RW,D,OVR,GRL
OVRA	000152	54 RW,D,OVR,GRL
OVRP	002022	521 RW,D,OVR,GRL
OVRR	005004	1282 RW,D,OVR,GRL
OVR2	001002	257 RW,D,OVR,GRL
OVR1	000016	7 RW,D,OVR,GRL
OVRD	000012	5 RW,D,OVR,GRL
MAPDEF	000200	64 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 025644 5586

NO FPP INSTRUCTIONS GENERATED

BASIS,IP/LI:1=BASIS/DE/NOTP

FORTRAN IV-PLUS V02-51F 13:10:13 09-AUG-80 PAGE 2
 ASRT.PTN /DE/MH

```

C      SORT TMP2 TO DCTM2
C      SORT DCTM TO TMP3
C      SORT DCTM1 TO TMP4

0026  D      INSTR=1
0027  D      STATUS=MPASRT(IORDRM)
0028  D      IF (STATUS.NE.0) GO TO 10

C      M$RESULTS

0029  D      CALL MPRDH(DCTM2,DCT2(1),MYTF4,CNVVFS,DCT2(LTH))
0030  D      CALL MPRDH(IORDRM,IORDR(1),MYTF2,CNVVFS,IORDR(LTH))
0031  D      WRITE(6,910)(1,IORDR(1),1=1,LTH)
0032  D910    FORMAT(/1X,6(1X,'IORDR(',13,')'=',13,2X))
0033  D      WRITE(6,909)(1,IORDR(1),1=1,LTH)
0034  D909    FORMAT(/1X,4(1X,'DCT2(',13,')'=',F15.8,2X))
0035  D      CALL MPRDH(TMP4,DCT1(1),MYTF4,CNVVFS,DCT1(LTH))
0036  D      WRITE(6,911)(1,DCT1(1),1=1,LTH)
0037  D911    FORMAT(/1X,4(1X,'DCT1(',13,')'=',F15.8,2X))
0038  D      CALL MPRDH(TMP3,DCT(1),MYTF4,CNVVFS,DCT(LTH))
0039  D      WRITE(6,912)(1,DCT(1),1=1,LTH)
0040  D912    FORMAT(/1X,4(1X,'DCT(',13,')'=',F15.8,2X))
0041  D      RETURN

C      DIAGNOSTIC TRAP AREA
C
0042  10    TYPE 100,INSTR,STATUS
0043  C      CALL EXIT
0044  100    FORMAT(1X,'***MAP ERROR*** "ASRT" INSTR=',13,' HAS STATUS=',16/)
0045  C      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCNDF1	000620	RM,I,CUN,LCL
SIDATA	000324	RM,D,CUN,LCL
SVANS	000054	RM,D,CUN,LCL
MTAPF0	002260	RM,D,OVR,GHL
MTAPF1	000012	RM,D,OVR,GHL
MTAPF2	000012	RM,D,OVR,GHL
OVR1	002020	RM,D,OVR,GHL
OVR0	002006	RM,D,OVR,GHL
OVRF	006016	RM,D,OVR,GHL
OVR4	000152	RM,D,OVR,GHL
OVRP	002022	RM,D,OVR,GHL
OVR8	005004	RM,D,OVR,GHL
OVR2	001002	RM,D,OVR,GHL
OVR1	000016	RM,D,OVR,GHL
OVR0	000012	RM,D,OVR,GHL
MAPDF	000200	RM,D,OVR,GHL

TOTAL SPACE ALLOCATED = 026222 5705

FORTRAN IV-PLUS V02-51P
ASRT.FIN /DD/WH

11:10:11

09-AUG-80

PAGE 1

NO PDP INSTRUCTIONS GENERATED

ASNT.LP/L1:1=ASRT/DD/MOTH

AD-A091 663

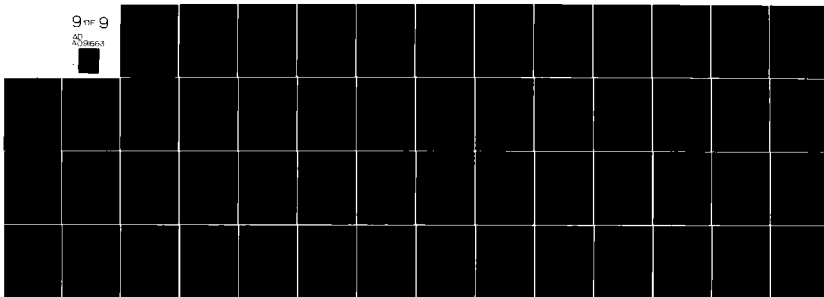
GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND. VOLUME 2. REAL-TIME S0--ETC(U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

9 of 9

AD-A091 663



END

DATE

FILED

12-80

DTIC

PROGRAM NAME:HITA.FTN
(ORIGINATED:05-APR-79
UPDATE:23-MAY-80

[illegible]

```

C
C      ASSIGN BITS TO DCT ELEMENTS
C
C      ANALYSIS:MODUL=1,SYNTHESIS:MODUL=2
C      INSTR=1
0028 IF(MODUL.EQ.1)STATUS=MPCDRA(MIRIT3)
0029 IF(MODUL.EQ.2)STATUS=MPCDRA(MIRIT1)
0030 IF(MODUL.EQ.2)STATUS=MPCDRA(MIRIT1)
0031 IF(STATUS.NE.0)GO TO 10
C
C      RESULTS
C
0032 IF(MODUL.EQ.1)CALL MPDRH(MIRIT3,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0033 IF(MODUL.EQ.2)CALL MPDRH(MIRIT1,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0034 IF(STATUS.NE.0)GO TO 10
0035 WRITE(6,911)(I,IRIT(I),I=1,LTH)
0036 FORMAT(1X,6(1X,'IRIT(',I3,')=',I3,2X))
0037 RETURN
C
C      DIAGNOSTIC TRAP AREA
C
0038 10 TYPE 100,INSTR,STATUS
0039 CALL EXIT
0040 100 FORMAT(1X,'***MAP ERROR*** RITA' INSTR='I3,' HAS STATUS='I6/)
0041 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000334	110
SIDATA	000160	56
SVARS	000054	22
SECTION	000002	1
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
VR1	002020	520
VR0	002006	515
VRP	006016	1543
VRPA	000152	53
VRP	002022	521
VRP	005004	1282
VR2	001002	257
VR1	000016	7
VR0	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025574 5566

NO FPP INSTRUCTIONS GENERATED

RITA,IP/LI:=RITA/DE/NOTR

FORTHAN IV-PLUS V02-51E
QDCT.1TN /DE/WR

13130152 09-AUG-80

PAGE 3

TOTAL SPACE ALLOCATED = 031122 6441

NO FPP INSTRUCTIONS GENERATED

QDCT.LP/LI:1=QDCT/DE/NOTR


```

PORTMAN IV-PLUS V02-51F          13:30:59    09-AUG-80          PAGE 2
CHANL..PTN /DB/WH

0027 D      WRITE(6,699)ICOUNT
0028 D699   FORMAT(1H1,40(1H>),'RECEIVE FRAME 0 ',14,40(1H<))
      C
      C      PASS DATA W/ NO ERRORS
      C
0029 D      INSTR=1
0030 D      STATUS=MPMDR(AQDCT,IAQDCT(1),BYTE2,CNVYES,IAQDCT(LTH))
0031 D      IF(STATUS.NE.0)GO TO 10
0032 D      INSTR=2
0033 D      STATUS=MPMDR(AQPR,IAQPR(1),BYTE2,CNVYES,IAQPR(LPARM+1))
0034 D      IF(STATUS.NE.0)GO TO 10
0035 D      WRITE(6,999)
0036 D899   FORMAT(/IX,'*** MODEM OUTPUT ***')
0037 D      WRITE(6,900)(IAQPR(I),I=1,LPARM)
0038 D900   FORMAT(/IX,6(IX,'AQPR(',12,')=' ,16,2X))
0039 D      WRITE(6,901)(IAQDCT(I),I=1,LTH)
0040 D901   FORMAT(/IX,6(IX,'AQDCT(',13,')=' ,16,2X))
      C
      C      ADD TRANSMISSION DELAY
      C
0041 D      INSTR=3
0042 D      STATUS=MPMDR(AQDCT,IAQDCT(1),BYTE2,CNVYES,IAQDCT(LTH))
0043 D      IF(STATUS.NE.0)GO TO 10
0044 D      INSTR=4
0045 D      STATUS=MPMDR(AQPR,IAQPR(1),BYTE2,CNVYES,IAQPR(LPARM+1))
0046 D      IF(STATUS.NE.0)GO TO 10
0047 D      WRITE(6,799)
0048 D799   FORMAT(/IX,'*** MODEM INPUT ***')
0049 D      WRITE(6,800)(IAQPR(I),I=1,LPARM)
0050 D800   FORMAT(/IX,6(IX,'AQPR(',12,')=' ,16,2X))
0051 D      WRITE(6,801)(IAQDCT(I),I=1,LTH)
0052 D801   FORMAT(/IX,6(IX,'AQDCT(',13,')=' ,16,2X))
0053 D      RETURN
      C
      C      DIAGNOSTIC TRAP AREA
      C
0054 I0     TYPE 100,INSTR,STATUS
0055 CALL EXIT
0056 I00    FORMAT(IX,'***MAP ERROR*** "CHANL" INSTR=',13,' HAS STATUS=',16/)
0057 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000644	210
SIDATA	000452	149
SVANS	000054	22
MTAPF0	002260	600
MTAPF1	000012	5
MTAPF2	000012	5
CHANFL	001034	270
OVRF1	002020	520
OVRF0	002006	515
OVRF	006016	1543

FORTRAN IV-PLUS V02-SIE
CHANL.FTN

13:30:59 09-AUG-80

PAGE 3

OVRA	000152	53	RW,D,OVR,CRI.
OVRP	002022	521	RW,D,OVR,CRI.
OVRB	005004	1282	RW,D,OVR,CRI.
OVR2	001002	257	RW,D,OVR,CRI.
OVR1	000016	7	RW,D,OVR,CRI.
OVRD	000012	5	RW,D,OVR,CRI.
MAPDEF	000200	64	RW,D,OVR,CRI.

TOTAL SPACE ALLOCATED = 027430 602R

NO FPP INSTRUCTIONS GENERATED

CHANL.IP/LI:1=CHANL/DE/NOTR

C DIAGNOSTIC TRAP AREA
 C
 0073 10 TYPE 100, INSTR, STATUS
 0074 CALL EXIT
 0075 100 FORMAT(IX, '***MAP ERROR*** "DPARM" INSTR=', I4, ' HAS STATUS=', I6 /)
 0076 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001542	433 RW, I, CON, LCL
SPDATA	000044	14 RW, D, CON, LCL
SIDATA	001036	271 RW, D, CON, LCL
SVAR5	000056	23 RW, D, CON, LCL
MTAPF0	002260	600 RW, D, OVR, GHL
MTAPF1	000012	5 RW, D, OVR, GHL
MTAPF2	000012	5 RW, D, OVR, GHL
OVRT	002020	520 RW, D, OVR, GHL
OVRD	002006	515 RW, D, OVR, GHL
OVRF	006016	1543 RW, D, OVR, GHL
OVRV	000152	53 RW, D, OVR, GHL
OVRP	002022	521 RW, D, OVR, GHL
OVRQ	005004	1282 RW, D, OVR, GHL
OVR2	001002	257 RW, D, OVR, GHL
OVRU	000016	7 RW, D, OVR, GHL
OVRD	000012	5 RW, D, OVR, GHL
OVRDT	002124	554 RW, D, OVR, GHL
OVRDT	001032	269 RW, D, OVR, GHL
MAPDEF	000200	64 RW, D, OVR, GHL

TOTAL SPACE ALLOCATED = 033102 6945

DPARM, LP/1:1=DPARM/DE/NOTR


```

SSRT.PTN
C      SORT TMP2 TO DCT#2
C      SORT DCT#1 TO TMP4
C
0026  D      INSTR#1
0027  D      STATUS=MPSSRT(IORDRM)
0028  D      IF (STATUS.NE.0)GO TO 10
C
C      RESULTS
C
0029  D      CALL MPRDR(DCT#2,DCT2(1),BYTE4,CNVVFS,DCT2(LTH))
0030  D      CALL MPRDR(IORDRM,IORDR(1),BYTE2,CNVVFS,IORDR(LTH))
0031  D      WRITE(6,910)(I,IORDR(1),I=1,LTH)
0032  D      FORMAT(/X,6(1X,'IORDR(',I3,')'=',I3,2X))
0033  D      WRITE(6,909)(I,DCT2(1),I=1,LTH)
0034  D      FORMAT(/X,4(1X,'DCT2(',I3,')'=',E15.8,2X))
0035  D      CALL MPRDR(TMP4,DCT1(1),BYTE4,CNVVFS,DCT1(LTH))
0036  D      WRITE(6,911)(I,DCT1(1),I=1,LTH)
0037  D      FORMAT(/X,4(1X,'DCT1(',I3,')'=',E15.8,2X))
0038  D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
0039  10  TYPE 100,INSTR,STATUS
0040  CALL EXIT
0041  100  FORMAT(1X,'***MAP ERROR*** SSRT" INSTR=',I3,' HAS STATUS=',I6/)
0042  END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000470	156 RW,I,CON,LCL
SIDATA	000256	87 RW,D,CON,LCL
SVARS	000054	72 RW,D,CON,LCL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
OVR1	002020	520 RW,D,OVR,GHL
OVR0	002006	515 RW,D,OVR,GHL
OVRF	006016	1543 RW,D,OVR,GHL
OVR4	000152	53 RW,D,OVR,GHL
OVRP	002022	521 RW,D,OVR,GHL
OVR2	005004	1282 RW,D,OVR,GHL
OVR2	001002	257 RW,D,OVR,GHL
OVR4	000016	7 RW,D,OVR,GHL
OVR0	000012	5 RW,D,OVR,GHL
MAPDEF	000200	64 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 026024 5642

NO FPP INSTRUCTIONS GENERATED

SSRT,LP/LI:1=SSRT/DE/NOTH

DEQUANTIZATION OF MAINBAND LAGS MAINBAND DESERIALIZATION
BY 1 FRAME DUE TO CSPIU IN THE BACKGROUND

INSTR=1
GIVE CSPIU WIT ASSIGNMENT FOR UNPACKING OF MAINBAND
STATUS=VM0V3(RHIT)
IF (STATUS.NE.0) GO TO 10

DEQUANTIZE DCT(1)

INSTR=2
STATUS=MPFST0(DRDCTM,ORDCTM,TMP1,IORDR2)
IF (STATUS.NE.0) GO TO 10

RESULTS

CALL MPRDH(DRDCTM,DTDCT(1),HYTE4,CNVYES,DTDCT(LTH))
WRITE(6,934)(I,DTDCT(I),I=1,LTH)
FORMAT(/1X,4(1X,'DRDCTM',13,')=',F15.8,2X))
CALL MPRDH(MHIT,IRIT(1),HYTE2,CNVYES,IRIT(LTH))
WRITE(6,935)(I,IRIT(I),I=1,LTH)
FORMAT(/1X,4(1X,'RHIT',13,')=',16,2X))
CALL MPRDH(DCT1,DCT1(1),HYTE4,CNVYES,DCT1(LTH))
WRITE(6,936)(I,DCT1(I),I=1,LTH)
FORMAT(/1X,4(1X,'DCT1',13,')=',F15.8,2X))
CALL MPRDH(IORDR,IORDR(1),HYTE2,CNVYES,IORDR(LTH))
WRITE(6,937)(I,IORDR(I),I=1,LTH)
FORMAT(/1X,4(1X,'IORDR',13,')=',16,2X))
CALL MPRDH(MIRIT,IRIT(1),HYTE2,CNVYES,IRIT(LTH))
WRITE(6,938)(I,IRIT(I),I=1,LTH)
FORMAT(/1X,4(1X,'MIRIT',13,')=',16,2X))
CALL MPHST(103,DRVAR,1,CNVYES)
WRITE(6,940)DRVAR
FORMAT(1X,'DRDC(102)=',F15.8,2X,' AND DRVAR(103)=',F15.8)
RETURN

DIAGNOSTIC TRAP AREA

TYPE 100, INSTR, STATUS

CALL EXIT

FORMAT(1X,'**MAP ERROR**' DDCT" INSTR=',I4,' HAS STATUS=',I6/)
END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	001056	279
SPDATA	000014	6
SIDATA	000506	163
SVARS	000064	26
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5

13:31:24 09-AUG-80

FORTRAN IV-PLUS V07-S1F
DDCT.FTN /PF/WH

OVRI	002020	520	RW,D,OVR,GRL
OVRD	002006	515	RW,D,OVR,GRL
OVRP	006016	1543	RW,D,OVR,GRL
OVRA	000152	53	RW,D,OVR,GRL
OVRP	002022	521	RW,D,OVR,GRL
OVRP	005004	1262	RW,D,OVR,GRL
OVR2	001002	257	RW,D,OVR,GRL
OVR1	000016	7	RW,D,OVR,GRL
OVRD	000012	5	RW,D,OVR,GRL
OVRDT	002124	554	RW,D,OVR,GRL
MAPDFF	000200	64	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 031012 6405

DDCT.LP/LL:1=DDCT/DE/NOTR

FORTRAN IV-PLUS V02-51F 13:31:32 09-AUG-80

```

DCTR.FTN
C
0026 D INSTR=1
0027 STATUS=MPIDCM(X2,DWDCTM,CUSZ)
0028 IF (STATUS.NE.0) GO TO 10
0029 CALL MPDRH(X2,XR(1),RYTF8,CNVVFS,XR(LTH2))
0030 WRITE(6,914)(I,XR(I),I=1,LTH2)
0031 INSTR=2
0032 STATUS=FTIN(X2,I,X2,VSHRT,WORK)
0033 IF (STATUS.NE.0) GO TO 10
C
C RESULTS
C
0034 CALL MPDRH(X2,XR(1),RYTF4,CNVVFS,XR(LTH))
0035 WRITE(6,914)(I,XR(I),I=1,LTH)
0036 FORMAT(1X,4(1X,' ',I3,' '),F15.8,2X))
0037 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0038 10 TYPE 100, INSTR, STATUS
0039 CALL EXIT
0040 100 FORMAT(1X, '***MAP ERROR*** DCTR' INSTR=',I3,' HAS STATUS=',I6/)
0041 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000406	131
SPDATA	000004	2
SINATA	000172	61
SVARS	000054	22
MTAPF0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRT	002020	520
OVR0	002006	515
OVR6	006016	1543
OVR4	000152	53
OVRP	002022	521
OVR8	005004	1282
OVR2	001002	257
OVR1	000016	7
OVRD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025062 5593

DCTR.LP/LI:1=DCTR/DE/NOTR

PROGRAM NAME: VARDC.FTN
ORIGINATED: 17-SEP-79
UPDATE: 09-MAY-80

[illegible]

FURTHAN IV-PLUS V02-51F
VARDC.FTN /DF/WR

```

0027 STATUS=MPVDNM(NOUTM,YNM,X2)
0028 IF(STATUS.NE.0)GO TO 10
0029 C
0030 C RESULTS
0031 C
0032 CALL MPST(100,DCHAS,1,CNVYES)
0033 WRITE(6,900)DCHAS
0034 FORMAT(/IX,'DCHAS=',F15.8)
0035 CALL MPST(101,VAR,1,CNVYES)
0036 WRITE(6,901)VAR
0037 FORMAT(/IX,'VAR=',F15.8)
0038 RETURN
0039 C
0040 C DIAGNOSTIC TRAP AREA
0041 C
0042 C
0043 C TYPE 101, INSTR, STATUS
0044 CALL EXIT
0045 FORMAT(IX,'***MAP ERROR*** VARDC" INSTR=',I3,' HAS STATUS=',I6/)
0046 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
\$CODE1	000200	64
\$DATA	000014	6
\$DATA	000160	56
\$VARS	000052	21
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRT	002020	520
OVR0	002006	515
OVR1	006016	1543
OVR2	000152	53
OVR3	002022	521
OVR4	005004	1292
OVR5	001002	257
OVR6	000016	7
OVR7	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025450 5524

NO FPP INSTRUCTIONS GENERATED

VARDC.LP/LI:1=VARDC/DF/N0TR


```

C
C CHECK FOR REQUEST FROM "INPUT"
IF (NPROG.NE.0) GO TO 5000
INSTR=1
STATUS=MPDEF(1,1)
STATUS=MPDEF(NOUT,NOUT(1),NUT2,CNUTFS,NOUT(NUTOT))
IF (STATUS.NE.0) GO TO 10
*ITP(6,913)(1,NOUT(1),1=1,NUTOT)
FORMAT(1X,4(1X,'NOUT(',13,')='),16,2X))
C
C DATA OUTPUT
C
C CALL TAPF2(2)
RETURN
C
C FOR ON INPUT,WRITE FOR
C
C COUNT=ICOUNT-1
DO 5001 1=1,NUTFS
NOUT(1)=0
DO 5002 1=1,32
CALL TAPF2(2)
NSKIP=NSKIPS
NEND=0
151=1
CALL TAPF2(6)
DO 5005 1=1,ICOUNT
CALL TAPF2(1)
IF (NEND) 5077,5076,5077
DO 5006 J=1,NUTFS
NOUT(J)=NIN(J)
CALL TAPF2(2)
DO 5007 1=1,NUTFS
NOUT(1)=0
DO 5008 1=1,4
CALL TAPF2(2)
CALL TAPF2(5)
CALL MPCLS(0)
C
C TYPE 100,ICOUNT
FORMAT(1X,'ATC' F0NE AFTER ',16,' FRAMES!')
C
C TYPE 100
FORMAT(1X,'ATC SYSTEM READY')
CALL EXIT
RETURN
C
C DIAGNOSTIC TRAP AREA
C
C
C TYPE 101,INSTR,STATUS
CALL EXIT
C
C TYPE 101
FORMAT(1X,'***MAP ERROR*** "OUTPUT" INSTR=',13,' HAS STATUS=',16/)
END

```

PROGRAM SECTIONS

ATTRIBUTES

SIZE

NAME

SC0001	000466	155	RW, I, CON, LCL
SPDATA	000020	8	RW, D, CON, LCL
SIDATA	000132	45	RW, D, CON, LCL
SWARS	000056	23	RW, D, CON, LCL
STMPAS	000002	1	RW, D, CON, LCL
ATAPR0	002260	600	RW, D, CON, LCL
ATAPR1	000012	5	RW, D, CON, LCL
ATAPR2	000012	5	RW, D, CON, LCL
OVPT	002020	520	RW, D, CON, LCL
OVPR0	002006	515	RW, D, CON, LCL
OVPR1	000016	1543	RW, D, CON, LCL
OVPR2	000152	53	RW, D, CON, LCL
OVPR3	002016	519	RW, D, CON, LCL
OVPR4	005003	1282	RW, D, CON, LCL
OVPR5	001002	257	RW, D, CON, LCL
OVPR6	000016	7	RW, D, CON, LCL
OVPR7	000012	5	RW, D, CON, LCL
4APR04	000200	64	RW, D, CON, LCL

TOTAL SPACE ALLOCATED = 025716 5607

NO PDP INSTRUCTIONS GENERATED

OUTPUT,LP/LI:1:OUTPUT/DE/NOTR

C PROGRAM NAME:SNP.FTN ORIGINATED:05-SEP-79
 C UPDATES:11-MAY-80

```

0001 SUBROUTINE SNP
0002 REAL, NOISE
0003 INTEGER MISC1,CUS2,RUS1
0004 1,MYPE4,MYPE5,CNVE5,CNVE6,UNITY,STATUS,SHORT
0005 1,UTCTM,MDR,X1,XIR,X11,IORDRM,PEFTM,DUCTM1
1,UCTM2,M1,A1,KF,PARC,TMP1,TMP2,TMP3,TMP4,X2
2,ORDCM,YM,OPRM,ORUCTM
3,VLONG,CUS2,VSHRT,YM,XDM,MAPX
4,MINT,DUCTM,OPRM,OTUCTM
5,MIRM,MINT,MINT2,MINTM,MINT1,MINT2
6,RECCT,RECCT2,SEMI,SEMI2
7,ADPH,AOTUCT,ATM1,SEMI,IPR
8,SYND1,SYND2,MFI,MFI2,OUTM
9,RECCT,ADPH,ARODCT,MINT
1,UCTM1,UCTM2,IORDM1,IORDM2,MINT1,MINT2,MINT3,MINT4,OTUCT1
C
0006
0007 DIMENSION NIND(246)
0008 LOGICAL A1,TIMING,MULTM,L14P,ANSPP,YES,NO
0009 COMMON/TAPE/NTM(300),NIND(300)
0010 COMMON/MTAPE1/NSKP,IST,NTOT1,RTOPS,CTOTO
0011 COMMON/MTAPE2/NEAD,NEPH,NFILE,NLKS,NODIS
0012 COMMON/DELAY/M3,M2,M1,IV4,IV3,IV2,IV1
0013 COMMON/UVR1/LTH,UCHIAS,VAR,X(256),ICUORT,IFEC,NSKIPS
0014 COMMON/UVR2/X(256),NP,XB
0015 COMMON/UVR3/UCT(256),UTH2,UTH3,UTH4,ARG,MUNG,MSHRT
0016 COMMON/UVR4/P(9),A(8),PARCUN(8),LPCW,FNG
0017 COMMON/UVR5/M,G,VO,PEFT(256),TIMING,KL31M,L14P,ANSPP,LSPN,LPRM
0018 COMMON/UVR6/UCT2(256),ORUG2,UCT3(256),IBIT(256)
0019 COMMON/UVR7/IORDR(256),LTHM1
0020 COMMON/UVR8/SLUG,LENGTH,CTEMP,ITOT,RTLTH
0021 COMMON/UVR9/XR(512)
0022 COMMON/UVR10/CSN,START,SIZE,YES,NO
0023 COMMON/MAPEF/KORR,X1,XIR,X11,IORDRM,PEFTM,DUCTM1
1,UCTM2,M1,A1,KF,PARC,TMP1,TMP2,TMP3,TMP4,X2
2,ORDCM,YM,OPRM,ORUCTM
3,VLONG,CUS2,VSHRT,YM,XDM,MAPX
4,MINT,DUCTM,OPRM,OTUCTM
5,MIRM,MINT,MINT2,MINTM,MINT1,MINT2
6,RECCT,RECCT2,SEMI,SEMI2
7,ADPH,AOTUCT,ATM1,SEMI,IPR
8,SYND1,SYND2,MFI,MFI2,OUTM
9,RECCT,ADPH,ARODCT,MINT
1,UCTM1,UCTM2,IORDM1,IORDM2,MINT1,MINT2,MINT3,MINT4,OTUCT1
C
0024 EQUIVALENCE (NIND(1),XR(1))
0025
0026 DATA KVAL,CNVE5,INTCUB,1,2/
0027 DATA MYPE4,MYPE5,STATUS,1,2,3/
DATA MINT2,MINT4,RYTEM/2,4,8/

```

```

FORTRAN IV-PLUS          21:16:36          11-SEP-80          PAGE 2
SMR.FIN

0026      DATA LORG,OROR1,CNVYFS,CNVXND/0,1,1,0/
0029      DATA POS1,POS2,POS3/64,128,192/

      ADD 3 FRAMES DELAY IN INPUT FOR S/N CALCULATION

      INSTREQ
      STATUS=APPROX(SNR,SUM4-1),NYTF2,CNVYFS,NIND3(NTUPS))
      STATUS=APPROX(IN2,TOROR(1),NYTF2,CNVYFS,TOROR(NTUPS))
      STATUS=APPROX(SNR,TOROR(1),NYTF2,CNVYFS,TOROR(NTUPS))
      STATUS=APPROX(IN1,TOROR(1),NYTF2,CNVYFS,TOROR(NTUPS))
      STATUS=APPROX(IN2,TOROR(1),NYTF2,CNVYFS,TOROR(NTUPS))
      STATUS=APPROX(IN1,NIND3(1),NYTF2,CNVYFS,NIND3(NTUPS))
      IF(STATUS.NE.0)GO TO 10
      SUM DELAY M & VUV FOR CONSISTENCY
      M4=M4
      M3=M3
      M2=M2
      M1=M1
      IV4=IV4
      IV3=IV3
      IV2=IV2
      IV1=IV1
      IV1=IFIX(VU)

      COMPUTE S/N RATIO
      SUM1=0.0
      SUM2=0.0
      NIND(1) MATCHES NOUT(NP+1), FREQ NTUPS=NP MATCHES:
      DO 3605 I=1,NIND(NP)
      SOURCE=FLOAT(NIND3(I))
      SIGNAL=FLOAT(NOUT(NP+1))
      NOTISE=SIGNAL-SOURCE
      SUM1=SUM1+SOURCE**2
      SUM2=SUM2+NOTISE**2
      SN=0.0
      FREQ=ICOUNT-START+1
      IF(FREQUM,1,1)GO TO 200
      SN=10.0*ALOG10(SUM1/SUM2)
      IF(FREQUM,10,4)CSN=SN
      CSN=((FREQUM-1)/FREQUM)*(SN+(1./FREQUM)*SN
      *RTF(3,255))ICOUNT,SN,CSN,M4,IV4
      *RTF(5,255))ICOUNT,SN,CSN,M4,IV4
      FORMAT(IX,'FREQ= ',I3,IX,'SN= ',F6,2,IX,'CSN= ',F6,2,3A
      1,'RTFCH= ',I3,' K VUV= ',I2)
      CONTINUE
      RETURN

      DIAGNOSTIC TRAP AREA
      C
      C
      TYPE 100,INSTN,STATUS
      CALL EXIT
      FORMAT(IX,'***MAP PROGRAM*** SNR= ',I3,' HAS STATUS= ',I6/)
      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000720	232
SI0ATA	000162	52
SVARS	000110	36
STEMPS	000002	1
ATAPF0	002260	600
ATAPF1	000012	5
ATAPF2	000012	5
DELAY	000020	4
OVR1	002020	520
OVR0	002006	515
OVRF	006016	1543
OVR4	000152	53
OVRP	002022	521
OVRN	005004	1282
OVR2	001002	257
OVR1	000016	7
OVR0	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 026236 5711

SNR.LP/1.1:1=SNR/DF/NOTR


```

0001 SUBROUTINE TAPF2(IU)
0002 IMPLICIT INTEGER(-2)
0003 LOGICAL A1, APPEND
0004 COMMON/MTAPE0/NIN(300),NIUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NIUT1,NTUPS,NTOTO
0006 COMMON/MTAPE2/NEPD,NFPP,NFILE,NINS,NIUTS
0007 COMMON/MTAPE3/NRF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISM(2),IOATT,IOSUC,IFALN,IORWD
0010 1,IOWLR,IEVER,IOSPF,IEFOF,IORLB,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0013 DATA IORWD,IOWLR,IEVER,IOSPF,IORLB/02400,0400,-4,02440,01000
0014 DATA IOSPF,IEFOF,IORLB/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/
0018 GO TO (700,800,900,1100,1000,1200,1300,1400),IU
0019 CALL OPT1
0020 RETURN
0021 CALL OPT2
0022 RETURN
0023 CALL OPT3
0024 RETURN
0025 CALL OPT4
0026 RETURN
0027 CALL OPT5
0028 RETURN
0029 CALL OPT6
0030 RETURN
0031 CALL OPT7
0032 RETURN
0033 CALL OPT8
0034 CALL OPT3
0035 IF (APPEND) CALL OPT4
0036 RETURN
END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC00F1	000226	75 RW,I,CON,LCI,
SP00TA	000072	9 RW,D,CON,LCI,
SI00TA	000002	1 RW,D,CON,LCI,
MTAPE0	002260	600 RW,D,OVW,GML
MTAPE1	000012	5 RW,D,OVW,GML
MTAPE2	000012	5 RW,D,OVW,GML
MTAPE3	012260	2648 RW,D,OVW,GML
MTAPE4	000004	1 RW,D,OVW,GML
MTAPE5	000064	26 RW,D,OVW,GML
MTAPE6	000002	1 RW,D,OVW,GML

TOTAL SPACE ALLOCATED = 015130 3172

FURTHAN IV-PLUS V02-51F
TAPE2.FTM /MP

11:41:28

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NO FPP INSTRUCTIONS GENERATED

TAPE2.I/P/LI:1=TAPE2/NOTH

```

0001 SUBROUTINE OPT1
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 APPEND
0004 COMMON/MTAPE0/MIN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NTOT1,NTUPS,NTOT0
0006 COMMON/MTAPE2/NEED,NFPR,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IEALN,IORWD
0010 1,IOWH,IEVER,IOSPE,IEFDF,IORUR,MT0(6),MT1(6),DSW
0011 DIMENSION ICARD(64)
0012 DATA IOATT,IOSUC,IEALN/0001400,1,-34/
0013 DATA IORWD,IOWH,IEVER,IOSPE,IORUR/02400,0400,-4,02440,01000
0014 DATA IOSPE,IEFDF,IORUR/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,204R,0,0,0,0/
0017 DATA MT1/0,204R,0,0,0,0/

C
C INPUT DATA(=1)
C
0018 CONTINUE
0019 IST=IST+NTUPS
0020 IF(1324.GE.IST) GO TO 200
0021 IST=IST-1024
0022 NSKIP=NSKIP+1
0023 KOVER=IST+NTOT1-1325
0024 IF(KOVER.GT.0) GO TO 305
0025 DO 5000 I=1,NTOT1
0026 MIN(I)=NRF(IST+I-1)
0027 IST=IST+NTUPS
0028 GO TO 6002
0029 IPEP=IST-1024
0030 DO 5001 I=1,300
0031 NRF(IPEP+I-1)=NRF(IST+I-1)
0032 ISTE=IPEP
0033 IF(NINS.EQ.0) GO TO 2100
C>>>>>>DISK INPUT
0034 DO 5010 I=1,16
0035 READ(7,END=2001,FRR=6000)(ICARD(J),J=1,64)
0036 K=64*(I-1)+300
0037 DO 5010 J=1,64
0038 NRF(K+J)=ICARD(J)
0039 IF(NFPR.NE.0) GO TO 6000
0040 GO TO 200
0041 NEND=1
0042 GO TO 200
C>>>>>>TAPE INPUT
0043 NEND=0
0044 NFPR=0
0045 CALL GETADM(MT0,NRF(301))
0046 CALL MTOTU(IORUR,2,1,0,ISW,MT0,DSW)
0047 IF(10SUC.EQ.IAND(MASK,ISW(1)))GO TO 200
0048 IF(IAND(IEFDF,MASK).EQ.IAND(MASK,ISW(1)))NEND=1
0049 IF(IAND(IEVER,MASK).EQ.IAND(MASK,ISW(1)))NFR=1
0050 GO TO 200

```

FORTRAN IV-PLUS V02-51E 11:41:33 10-AUG-80
OPT1.FTN /WP

0051 6000 TYPE 6001
0052 6001 FORMAT(IX,'***INPUT FILE ERROR***')
0053 MFRM=1
0054 6002 RETURN
0055 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000646	211
SPDATA	000014	6
SIDATA	000062	25
SVARS	000212	69
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016034 3598

NO FPP INSTRUCTIONS GENERATED

OPT1.LP/1:1=OPT1/NOTH

FORTRAN IV-PIPS V02-51E
OPT2.FTN /WR

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SCDDF1	000432	141	PW,I,CON,I,CL
SPDATA	000014	6	PW,D,CON,I,CL
SIDATA	000062	25	PW,D,CON,I,CL
SVARS	000212	69	PW,D,CON,I,CL
MTAPF0	002260	600	PW,D,OVR,GRL
MTAPF1	000012	5	PW,D,OVR,GRL
MTAPF2	000012	5	PW,D,OVR,GRL
MTAPF3	012260	2648	PW,D,OVR,GRL
MTAPF4	000004	2	PW,D,OVR,GRL
MTAPF5	000064	26	PW,D,OVR,GRL
MTAPF6	000002	1	PW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 015620 3528

NO FPP INSTRUCTIONS GENERATED

OPT2.LP/LI:1=OPT2/NOTR

```

0001 SUBROUTINE UPT3
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL,PARAMETER=1
0004 COMMON/MTAPE0/NTIN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NTOT,NTIPS,NTOTO
0006 COMMON/MTAPE2/NFND,NFRR,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NHUF(1324)
0008 COMMON/MTAPE4/IST,TRC
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IUSUC,IFALN,IORWD
0010 I,IWLH,IEVER,IOSPF,IEFOF,IOKOF,IORLH,MT0(6),MTI(6),DSW
0011 DIMENSION ICAPD(64)
0012 DATA IOATT,IUSUC,IFALN/0001400,1,-34/
0013 DATA IORWD,IWLH,IEVER,IOSPF,IORLH/02400,0400,-4,02440,01000/
0014 DATA IOSPF,IEFOF,IOKOF/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MTI/0,2048,0,0,0,0/

C
C INITIALIZE(=3)
C
0018 CONTINUE
0019 NEND=0
0020 NERR=0
0021 IF((NINS+NOUTS).GT.1)GO TO 901
0022 UNIT=0
0023 DO 902 LIN=2,3
0024 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 902
0025 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 902
C>>>>>>MT: ATTACH
0026 CALL ASNLUN(LUN,'MT',UNIT,DSW)
0027 IF(DSW.NE.1)GO TO 6000
0028 CALL WTOIO(IOATT,LUN,1,0,ISW(1),0,DSW)
0029 IF(IUSUC.FO.IAND(MASK,ISW(1)))GO TO 902
0030 IF(IAND(IEALN,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0031 UNIT=UNIT+1
0032 DO 903 LIN=2,3
0033 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 903
0034 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 903
C>>>>>>MT: REWIND
0035 CALL WTOIOIORWD,LUN,1,0,ISW(1),0,DSW)
0036 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6001
0037 CONTINUE
0038 IF(NFILE.FO.0)GO TO 905
0039 IF(NINS.NE.0)GO TO 913
C>>>>>>MT: FILE SKIP
0040 DO 907 I=1,NFILE-1
0041 CALL GETADR(MT0,NHF(301))
0042 CALL WTOIOIORLH,2,1,0,ISW(1),MT0,DSW)
0043 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6003
0044 MT0(1)=1
0045 CALL WTOIOIOSPF,2,1,0,ISW(1),MT0,DSW)
0046 DO 912 J=1,NSKIP
0047 IF(NINS.FO.0)GO TO 910
C>>>>>>DISK INPUT
0048 DO 914 I=1,16

```

```

0049 READ(7,END=905,KRP=6002)((CARD(IJ),JJ=1,64)
0050 K=64*(I-1)+300
0051 DO 914 JJ=1,64
0052 914 NHF(I+JJ)=ICARD(IJ)
0053 GO TO 912
C>>>>>TAPF INPUT
0054 910 CALL GETAPR(MTO,NRF(301))
0055 CALL MTGID(TORIN,2,1,0,ISW(1),MTO,USW)
0056 IF(IOSUC.FO.IAND(MASK,ISW(1)))GO TO 912
0057 IF(IAND(TKVER,MASK).FO.IAND(MASK,ISW(1)))GO TO 6002
0058 IF(IAND(IREF,MASK).NF.IAND(MASK,ISW(1)))GO TO 912
0059 NFND=1
0060 912 CONTINUE
0061 995 IREF=1
0062 I=IST+300
0063 I=IST-NTIPS
0064 RETURN
0065 TYPE 100
0066 GO TO 6010
0067 TYPE 101
0068 GO TO 6010
0069 TYPE 102
0070 GO TO 6010
0071 TYPE 103
0072 6010 NFND=1
0073 RETURN
0074 100 FORMAT(IX,'*** MT: ATTACH FAILURE! ***')
0075 101 FORMAT(IX,'*** MT: REWIND FAILURE! ***')
0076 102 FORMAT(IX,'*** INPUT FILE "NSKIP" FAILURE! ***')
0077 103 FORMAT(IX,'*** MT: "NFILE" FAILURE! ***')
0078 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001212	325 RW,1,CNN,I,CL
SPDATA	000020	R RW,D,CNN,I,CL
SIDATA	000332	109 RW,D,CNN,I,CL
SVARS	000214	70 RW,D,CNN,I,CL
STFAPS	000004	2 RW,D,CNN,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
MTAPE3	012260	2648 RW,D,OVR,GHL
MTAPE4	000004	2 RW,D,OVR,GHL
MTAPE5	000064	26 RW,D,OVR,GHL
MTAPE6	000002	1 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 016662 3801

NO FPP INSTRUCTIONS GENERATED

OPT3.LP/LI:1=OPT3/NUIN


```

0001 SUBROUTINE OPT4
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/TAPE0/NTN(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTUT,NTUPS,NTUTO
0006 COMMON/TAPE2/NEND,NERR,NFILE,NINS,NOUTS
0007 COMMON/TAPE3/NHF(1324),NRUF(1324)
0008 COMMON/TAPE4/IST,TRFC
0009 COMMON/TAPE5/MASK,ISM(2),I0ATT,I0SUC,IFALN,I0RWD
0010 I,I0WLH,I0VER,I0SPE,IFEOF,I0FOR,I0R,H,MT0(6),MT1(6),DSW
0011 COMMON/TAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA I0ATT,I0SUC,IFALN/0001400,1,-34/
0014 DATA I0RWD,I0WLH,I0VER,I0SPE,I0FOR/02400,0400,-4,02440,01000/
0015 DATA I0SPE,IFEOF,I0FOR/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MT1/0,2048,0,0,0,0/
0019
0018 C OUTPUT FILE SKIP(=4)
0019 C
0020 CONTINUE
0021 NERR=0
0022 NEND=0
0023 IREG=1
0024 CALL GETADR(MT1(1),NRUF(1))
0025 CALL MT0IO(I0R,H,3,1,0,ISW(1),MT1,DSW)
0026 IF(I0SUC.EQ.IAND(MASK,ISW(1)))GO TO 1
0027 IF(IAND(IFEOF,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0028 CALL GETADR(MT1(1),NRUF(1))
0029 CALL MT0IO(I0R,H,3,1,0,ISW(1),MT1,DSW)
0030 IF(I0SUC.EQ.IAND(MASK,ISW(1)))GO TO 1
0031 IF(IAND(IFEOF,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0032 MT1(1)=1
0033 CALL MT0IO(I0SPE,3,1,0,ISW(1),MT1,DSW)
0034 RETURN
0035 TYPE 100
0036 RETURN
0037 FORMAT(1X, '*** OUTPUT "APPEND" FAILURE! ***')
0038 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDP1	000274	74 RW,I,CUN,ICL
SPDATA	000014	6 RW,D,CUN,ICL
SIDATA	000114	34 RW,D,CUN,ICL
SVARS	000200	64 RW,D,CUN,ICL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
MTAPE3	012260	2648 RW,D,OVR,GHL
MTAPE4	000004	2 RW,D,OVR,GHL
MTAPE5	000064	26 RW,D,OVR,GHL

FORTMAN IV-PLUS V02-51E
OPT4.FTN /rR

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MTAPPA 000002 1 MM,D.OVR,GML

TOTAL SPACE ALLOCATED = 015432 3469

NO FPP INSTRUCTIONS GENERATED

OPT4.OPT/1.1:1=OPT4/MOTR

```

0001 SURROUTINE OPTS
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 APPEND
0004 COMMON/MTAPE0/NIN(300),NOUT(300)
0005 COMMON/MTAPE1/MTSKIP,IST,NTOT1,MTOPS,MTOT0
0006 COMMON/MTAPE2/NEND,NFPP,NELF,NINS,NOUTS
0007 COMMON/MTAPE3/NHF(1324),NHUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IOHWD
0010 1,IOH1H,IEVER,IOSPF,IEFOP,IOENF,IOENH,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0014 DATA IOHWD,IOH1H,IEVER,IOSPF,IOENH/02400,0400,-4,02440,01000
0015 DATA IOSPF,IEFOP,IOENF/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
    DATA MT1/0,2048,0,0,0,0/
C
C   END-OF-FILE(=5)
C
0018 CONTINUE
0019 NERR=0
0020 NEND=0
0021 IF(NOUTS.EQ.0)GO TO 1001
C>>>>>>DISK EOF
0022 CALL CLOSE(3)
0023 GO TO 1003
C>>>>>>TAPE EOF
0024 1001 DO 1002 I=1,2
0025 1002 CALL MT0IOIOENF,3,1,0,ISW(1))
0026 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
    MT1(1)=-1
0027
0028 CALL MT0IOIOSPF,3,1,0,ISW(1),MT1,DSW)
0029 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
    1003
0030 INFC=1
0031 RETURN
0032 TYPE 100
0033 NERR=1
0034 RETURN
0035 100 FORMAT(IX,'*** MT: FOR FAILURE! ***'/)
0036 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDF1	000204	66 RW,I,CUN,I,C,L
SPDATA	000014	6 RW,D,CUN,I,C,L
SIDATA	000076	31 RW,D,CUN,I,C,L
SVARS	000202	65 RW,D,CUN,I,C,L
MTAPE0	002260	600 RW,D,OVR,CNL
MTAPE1	000012	5 RW,D,OVR,CNL
MTAPE2	000012	5 RW,D,OVR,CNL
MTAPE3	012260	2648 RW,D,OVR,CNL

FORTMAN IV-PLUS V02-514
OPTS.FTN /MR

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MTAPE4	000004	2	PM,D,OVR,GHL
MTAPE5	000064	26	RM,D,OVR,GHL
MTAPE6	000002	1	RM,D,OVR,GHL

TOTAL SPACE ALLOCATED = 015376 3455

NO FPP INSTRUCTIONS GENERATED

OPTS,IP/LI:1=OPTS/MOTR

```

FORTRAN IV-PLUS 002-51F 11:47:06 10-AUG-80 PAGE 1
OPTN.FTN

0001 SUBROUTINE OPT6
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A91 APPEND
0004 COMMON/MTAPE/MTN(300),MOUT(300)
0005 COMMON/MTAPE/NSKIP,IST,MTOTL,NTOPS,NTOTD
0006 COMMON/MTAPE/MTEND,MTERR,MTFLX,MTINS,MOUTS
0007 COMMON/MTAPE/MTNRE(1324),MNUF(1324)
0008 COMMON/MTAPE/MTST,THEG
0009 COMMON/MTAPE/MTASF,ISW(2),IOATT,IUSUC,IFARN,IORWD
0010 1,IOVRH,IFVPR,IUSPF,IFEOF,IOFOP,IOVRH,MT0(6),MT1(6),DSW
0011 DIMENSION ICARD(64)
0012 DATA IOATT,IUSUC,IFARN/0001400,1,-34/
0013 DATA IOVRH,IOVRH,IFVPR,IUSPF,IOVRH/02400,0400,-4,02440,01000/
0014 DATA IUSPF,IFOP,IOFOP/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2044,0,0,0,0/
0017 DATA MT1/0,2044,0,0,0,0/

C
C REWIND/SEARCH INPUT ONLY(=6)
C
0018 CONTINUE
0019 NEND=0
0020 NERR=0
0021 IF(MINS.FO.0)GO TO 1222
C>>>>>>DISK REWIND
0022 REWIND 2
0023 GO TO 1204
C>>>>>>MT: REWIND
0024 GO TO 1204
0025 CALL MTOTL(IORWD,2,1,0,ISW(1),0,DSW)
0026 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6002
0027 IF(MFLX.EE.1)GO TO 1204
0028 DO 1205 I=1,MTFLX-1
0029 CALL GETADR(MT0,MNF(301))
0030 CALL MTOTL(IUVR,2,1,0,ISW(1),MT0,DSW)
0031 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0032 MT0(1)=1
0033 CALL MTOTL(IUSPF,2,1,0,ISW(1),MT0,DSW)
0034 DO 1210 J=1,NSKIP
0035 IF(MINS.FO.0)GO TO 1204
C>>>>>>DISK SEARCH
0036 DO 1207 I=1,16
0037 READ(2,FND=1202,FRR=6001)(ICARD(J),JJ=1,64)
0038 K=64*(I-1)+100
0039 DO 1207 JJ=1,64
0040 MNF(K+JJ)=ICARD(JJ)
0041 GO TO 1210
C>>>>>>TAPE SEARCH
0042 CALL GETADR(MT0,MNF(301))
0043 CALL MTOTL(IUVR,2,1,0,ISW(1),MT0,DSW)
0044 IF(IUSUC.FO.IAND(MASK,ISW(1)))GO TO 1210
0045 IF(IAND(IUVR,MASK).FO.IAND(MASK,ISW(1)))GO TO 6000
0046 IF(IAND(IUVR,MASK).NE.IAND(MASK,ISW(1)))GO TO 1210
0047 NEND=1
0048 GO TO 6000
0049 CONTINUE
0050

```

```

0049  IST=IST+100
0050  IST=IST-NTHPS
0051  RETURN
0052  TYPE 100
0053  GO TO 6010
0054  TYPE 101
0055  GO TO 6010
0056  TYPE 102
0057  NTHPS=1
0058  RETURN
0059  100  FORMAT(IX,'*** INPUT FILE "NSKIP" FAILURE! ***')
0060  101  FORMAT(IX,'*** INPUT FILE "NFILE" FAILURE! ***')
0061  102  FORMAT(IX,'*** MT: REWIND FAILURE! ***')
0062  END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000712	229
SPDATA	000014	6
SIDATA	000246	83
SVARS	000210	68
STMP5	000004	2
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016266 3675

NO FPP INSTRUCTIONS GENERATED

OPT6.LP/1:1=OPT6/NUTR

```

OPT7.FIN
0001 SUBROUTINE OPT7
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),ROUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NTOT1,NTOPS,NTOTO
0006 COMMON/MTAPE2/WEND,NEPR,NFILF,NTHS,ROUTS
0007 COMMON/MTAPE3/NHF(1374),NHUF(1324)
0008 COMMON/MTAPE4/LST,INFG
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IOHWD
0010 COMMON/MTAPE6/APPEND
0011 DIMENSION ICARD(64)
0012 DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0013 DATA IOHWD,IOHWR,IOHVR,IOSPF,IOHLR/02400,0400,-4,02440,01000
0014 DATA IOSPF,IOEOF,IOEOF/02440,-10,03000/
0015 DATA MASK/0377/
0016 DATA MT0/0,2048,0,0,0,0/
0017 DATA MT1/0,2048,0,0,0,0/

```

```

C
C CLOSE INPUT FILE
C
0018 CONTINUE
0019 NFNED=0
0020 NFNED=0
0021 IF(NINS.EQ.1)CALL CLOSE(2)
0022 RETURN
0023 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	000032	13 RW,I,CON,I,CL
SPDATA	000004	2 RW,D,CON,I,CL
SDATA	000004	2 RW,D,CON,I,CL
SVARS	000200	64 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GRI
MTAPE1	000012	5 RW,D,OVR,GRI
MTAPE2	000012	5 RW,D,OVR,GRI
MTAPE3	012260	2648 RW,D,OVR,GRI
MTAPE4	000004	2 RW,D,OVR,GRI
MTAPE5	000064	26 RW,D,OVR,GRI
MTAPE6	000002	1 RW,D,OVR,GRI

TOTAL SPACE ALLOCATED = 015120 3368

NO FPP INSTRUCTIONS GENERATED

OPT7.LP/L1:1=OPT7/NOTH

```

0001 SUBROUTINE OPTM
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 EXTIN,EXTOUT,APPEND
0004 COMMON/MTAPE0/MIN(100),NOUT(100)
0005 COMMON/MTAPE1/MSKIP,IST,NTOT1,NTOPS,NTOT0
0006 COMMON/MTAPE2/NEED,NFRR,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NPF(1324),NHUF(1324)
0008 COMMON/MTAPE4/IST,IRFG
0009 COMMON/MTAPE5/MASK,ISM(2),IOATT,IOSUC,IEALN,IORWD
0010 1,LOWLW,IEVER,IOSPF,IEEOF,IOELH,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION EXTIN(3),EXTOUT(3)
0013 DATA YES,NO,'Y','N',/
0014 DATA EXTIN,'I','N','P',/
0015 DATA EXTOUT,'I','O','T',/
0016 DATA NFRR,NFRR,NFILE/0,0,0/
0017 DATA MSKIP,IST/1,1/
0018 DATA IOATT,IOSUC,IEALN/0001400,1,-34/
0019 DATA IORWD,LOWLW,IEVER,IOSPF,IOELH/02400,0400,-4,02440,01000
0020 DATA IOSPF,IEEOF,IOEOF/02440,-10,03000/
0021 DATA MASK/0377/
0022 DATA MT0/0.2048,0.0,0.0/
0023 DATA MT1/0.2048,0.0,0.0/

C
C GFT FILE INFO(=8)
C
0024 APPEND=.FALSE.
0025 TYPE 100
0026 FORMAT(/,1HS,'IS THE INPUT ON MAG. TAPE(Y/N)? ')
0027 READ(5,102,END=999,FRR=900)ANSER
0028 FORMAT(A1)
0029 NINS=1
0030 IF(ANSER.EQ.NO)NINS=1
0031 NOUTS=1
0032 TYPE 103
0033 FORMAT(1HS,'IS THE OUTPUT GOING TO MAG TAPE(Y/N)? ')
0034 READ(5,102,END=999,FRR=901)ANSER
0035 IF(ANSER.EQ.NO)NOUTS=1
0036 IF(NOUTS.EQ.1)GO TO 5
0037 TYPE 104
0038 FORMAT(1HS,'APPEND DATA? ')
0039 READ (5,102,END=999,FRR=902)ANSER
0040 IF(ANSER.EQ.YES)APPEND=.TRUE.
0041 IF(NOUTS.EQ.0)GO TO 151
0042 C
0043 C RSX11 SUPPORTED FILE
0044 TYPE 105
0045 FORMAT(1HS,'OUTPUT FILE NAME= ')
0046 CALL FILEN(3,EXTOUT)
0047 C
0048 C BEGINNING OF INPUT
0049 C
0045 IF(NINS.EQ.1)GO TO 155
0046 TYPE 106
0047 FORMAT(1HS,'MT FILE NO.=(13) ')
0048 READ(5,101,END=999,FRR=904)NFILE

```


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```

FURTRAN IV-PLUS V02-SIF
OPT0.FTN
0049 101 FORMAT(13)
0050      GO TO 14
0051 155 TYPE 107
0052 107 FORMAT(1HS,'INPUT FILE NAME= ')
0053      CALL FILEN(2,EXTN)
0054      NFILE=1
0055 14      RETURN
0056 999      CALL EXIT
0057      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000524	170
SPDATA	000010	4
SIDATA	000300	96
SVARS	000214	70
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016126 3627

NO FPP INSTRUCTIONS GENERATED

```

0001      SUBROUTINE FILE(UNIT,EXT)
0002      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
0003      FROM THE TTY DEVICE
0004      DEFAULT DEVICE
0005      UNLESS SPECIFIED IN INPUT STRING
0006      UNIT=UNIT NUMBER
0007      EXT = LOGICAL*1 BUFFER OF EXTENSION
0008
0009      IMPLICIT INTEGER(A-Z)
0010      LOGICAL*1 INSTR,UNIT,RLNK,EXT
0011      DIMENSION INSTR(40)
0012      DIMENSION EXT(3)
0013      DATA RLNK,UNIT/ ' ',' '
0014
0015      INPUT FILE
0016      READ (5,99,FND=999,FPR=1%2)(INSTR(I),I=1,40)
0017      FORMAT(A9A1)
0018      CHECK FOR END OF LINE
0019      DO 1600 I=40,1,-1
0020      J=I
0021      IF (INSTR(I).NE.RLNK)GO TO 1601
0022      TYPE 1%1
0023      FORMAT(1HS,'>')
0024      GO TO 1%2
0025      DO 1602 I=1,J
0026      IF (INSTR(I).NE.RLNK) GO TO 1602
0027
0028      RLNK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
0029      DO 1603 K=1,J-1
0030      INSTR(K)=INSTR(K+1)
0031      INSTR(J)=RLNK
0032      J=J-1
0033      GO TO 1601
0034      CONTINUE
0035      DO 103 I=1,J
0036      IF (INSTR(I).EQ.DUT) GO TO 2%
0037      INSTR(J+1)=DUT
0038      INSTR(J+2)=EXT(1)
0039      INSTR(J+3)=EXT(2)
0040      INSTR(J+4)=EXT(3)
0041      J=J+4
0042      CALL SCAN(INSTR,J)
0043      CALL ASSIGN(UNIT,INSTR,J)
0044      RETURN
0045      CALL EXIT
0046      FND

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1 000412	157	RM,I,COM,LOC
SIDATA 000044	14	RM,D,COM,LOC

FORTMAN IV-PLUS V02-SIE
OPTS.FTN /MM

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SVARS 000060 24 RM.D.COM,I.CL
STEMPS 000004 2 RM.D.COM,I.CL

TOTAL SPACE ALLOCATED = 000622 201

NO FPP INSTRUCTIONS GENERATED

```

C
0001      SUBROUTINE SCAN(RUF,LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*1 RUF,DEVICE
0004      DIMENSION RUF(1),DEVICE(4)
0005      DATA DEVICE/'S','V','O','.'/
0006      DO 1 I=1,LTH
0007      1 IF(RUF(I).EQ.DEVICE(4))RETURN
0008      LTH=LTH+4
0009      DO 2 I=LTH,5,-1
0010      2 RUF(I)=RUF(I-4)
0011      DO 3 I=1,4
0012      3 RUF(I)=DEVICE(I)
0013      RETURN
0014      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61
SIPATA	000012	5
SVARS	000006	3
STEMPS	000002	1
		RW,I,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

OPT8.LP/LI:1=OPT8/MOTR

FORTRAN IV-PLUS V02-V1F 11-SEP-60 21:16:21

```

0040      004      TYPE 106
0041      106      FORMAT(1HS,M1 FILE NO.=(I3) ' )
0042      0042      READ(5,101,FMT=004,ERR=004)NFILE
0043      101      FORMAT(I3)
0044      0044      GO TO 14
0045      155      CONTINUE
          C155
          TYPE 107
          C107
          C      FORMAT(1HS,'INPUT FILE NAME= ' )
          CALL FILEN(2,EXTN)
          CALL ASSIGN(2,'(5,100)WHICFA-MIN',17)
          NFILE=1
0046      0047      14      RETURN
0048      0048      999      CALL EXIT
0049      0050      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC001	00030	108
SP001	00042	17
SI001	00100	32
SVARS	000214	70
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000004	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 015564 3514

NO FPP INSTRUCTIONS GENERATED

```

0001      SUBROUTINE FILEN(UNIT,FXT)
C      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
C      FORM THE TTY DEVICE 5
C      DEFAULT DEVICE
C      UNLESS SPECIFIED IN INPUT STRING
C      UNIT=UNIT NUMBER
C      FXT = LOGICAL+1 BUFFER OF EXTENSION
C
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL+1 INSTR,DOT,MLNK,FXT
0004      DIMENSION INSTR(40)
0005      DIMENSION EXT(3)
0006      DATA MLNK,DOT/' ','.'/
C
C      INPUT FILE
0007      READ (5,99,FND=999,FMT=152)(INSTR(I),I=1,40)
0008      FORMAT(40A1)
C      CHECK FOR END OF LINE
0009      DO 1600 I=40,1,-1
0010      J=1
0011      IF (INSTR(I).NE.MLNK) GO TO 1601
0012      IF (.F. 151)
0013      FORMAT(1HS,'>')
0014      GO TO 152
0015      DO 1602 I=1,J
0016      IF (INSTR(I).NE.MLNK) GO TO 1602
C
C
C      MARK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
C
0017      DO 1603 K=1,J-1
0018      INSTR(K)=INSTR(K+1)
0019      INSTR(J)=MLNK
0020      J=J-1
0021      GO TO 1601
0022      CONTINUE
0023      DO 103 I=1,J
0024      IF (INSTR(I).EQ.DOT) GO TO 25
0025      INSTR(J+1)=DOT
0026      INSTR(J+2)=FXT(1)
0027      INSTR(J+3)=EXT(2)
0028      INSTR(J+4)=EXT(3)
0029      J=J+4
0030      CALL SCAN(INSTR,J)
0031      CALL ASSIGN(UNIT,INSTR,J)
0032      RETURN
0033      CALL EXIT
0034      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1 000477	157	P+I,COD,LOC
SIDATA 000044	15	R+D,COD,LOC

PONTMAN IV-0105 V02-51P
DPTBA.FIN /06/MN

21:16:25 11-SEP-80

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SVARS 000060 24 W=D,CUN,I,CL
STEMPS 000004 2 W=D,CUN,I,CL

TOTAL SPACE ALLOCATED = 000622 201

NO PPP INSTRUCTIONS GENERATED

PROGRAM IN-PROCESS
OPTIMIZER

01:16:27 11-SEP-80

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```

0001      SUBROUTINE SCANC(P0F,LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*8 HOF,DEVICE
0004      DIMENSION HOF(1),DEVICE(1)
0005      DATA DEVICE/'S','Y','O',' ','/
0006      DO 1 I=1,LTH
0007      IF (HOF(I).EQ.0)DEVICE(4)=HOF(I)
0008      LTH=LTH+4
0009      DO 2 I=LTH,5,-1
0010      HOF(I)=HOF(I-4)
0011      DO 3 I=1,4
0012      HOF(I)=DEVICE(I)
0013      RETURN
0014      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61
SIDATA	000012	5
SVARS	000006	3
STEMPS	000002	1

TOTAL SPACE ALLOCATED = 000214 70

NO FOR INSTRUCTIONS GENERATED

OPTIM,LP/LI:1=OPTIM/DE/OUTR

```

FORTRAN IV-PLUS V02-51P      13:17:26      10-AUG-80      PAGE 1
PRMAP.FTN      ZTR:HUUCKS/MR

C      PRMAP.FTN      ORIGINATED:29-MAY-79
C      UPDATED:04-JUN-79
C      THIS PROGRAM INTERPRETS ASCII TEXT FILES FOR THE MAP
C      ASSEMBLER AND ALIGNS ALL FIELDS W/ SPACES AND REMOVES
C      ALL TABS. ALL FILES ARE ASSUMED TO BE 120 COLUMNS WIDE.
C      OUTPUT LINE FORMAT:
C      COLUMNS 1,2...7      LABEL
C      COLUMNS 8      BLANK
C      COLUMNS 9,10...55      SOURCE CODE
C      COLUMNS 56      BLANK
C      COLUMNS 57,58...120      COMMENTS
C
0001      HYTF BLANK,TAB,HYPHEN,STAR,CR,DUMMY,SCOLON
0002      HYTF ALAST
0003      HYTF AC(200),L(200)
0004      LOGICAL*1 IN(3),OUT(3),OUTPUT
0005      DATA IN/'T','X','Y',/OUT/'M','A','P',/
0006      DATA BLANK,TAB,HYPHEN,STAR,CR,SCOLON/040,011,047,052,015,073/
C
0007      TYPE 100
0008      CALL FILEM(2,IN)
0009      TYPE 101
0010      CALL FILEM(3,OUT)
0011      LINF=0
C
C      READ EACH SOURCE "LINE"
C
0012      LINF=LINF+1
0013      OUTPUT=.TRUE.
0014      READ(2,102,END=200,ERR=300)(A(1),I=1,120)
C
C      "BLANK" OUT OUTPUT LINE
C
0015      DO 10 I=1,120
0016      L(I)=BLANK
C
C      SPECIAL LINE OPERATORS
C
0017      IF(A(1).EQ.HYPHEN)GO TO 70
0018      IF(A(1).EQ.STAR)GO TO 70
0019      IF(A(1).EQ.SCOLON)GO TO 70
C
C      LABEL FIELD
C
0020      I=1
0021      IF(A(1).EQ.TAB)GO TO 12
0022      L(I)=A(I)
0023      I=I+1
0024      IF(I.GT.120)GO TO 80
0025      GO TO 11
0026      NEXT=I+1
C
C      COMMAND FIELD
C
0027      J=0
0028      I=NEXT

```

FORTRAN IV-PLANS VOZ-SAP 13117:26 10-AUG-80

PHMAP.FIN /TRHJGKNS/WH

```

0029 21 IF(A(1),FO,TAB)GO TO 22
0030 L(9+J)=A(1)
0031 J=J+1
0032 IF(J.GT.11)GO TO 40
0033 I=I+1
0034 IF(I.GT.120)GO TO 40
0035 GO TO 21
0036 NEXT=1+1
22 COMMENT F1F1D
C
C
0037 30 K=0
0038 I=I+1
0039 IF(A(1),FO,TAB)GO TO 40
0040 IF(ALAST,FO,HLANK,AND,A(1),FO,HLANK)GO TO 40
0041 IF(A(1),FO,CR)GO TO 40
0042 ALAST=A(1)
0043 OUTPUT=.TRUE.
0044 L(57+K)=A(1)
0045 K=K+1
0046 IF(K.LE.23)GO TO 32
0047 WRITE(3,106)(L(I),I=1,120)
0048 DO 33 I=1,120
0049 L(I)=HLANK
0050 L(I)=SCOLON
0051 K=0
0052 OUTPUT=.FALSE.
0053 ALAST=HLANK
0054 I=I+1
0055 IF(I.GT.120)GO TO 40
0056 GO TO 31
C
C COPY LINE "EN TOTD"
C
0057 70 DO 71 I=1,120
0058 L(I)=A(1)
C
C GENERATE CORRECTED OUTPUT LINE
C
0059 40 IF(OUTPUT,FO,.TRUE.)WRITE(3,106)(L(I),I=1,120)
0060 GO TO 1
C
C NORMAL EXIT
C
0061 200 CALL CLOSE(2)
0062 CALL CLOSE(3)
0063 TYPE 103,LINE
0064 STOP
C
C ABNORMAL EXIT
C
0065 300 CALL CLOSE(2)
0066 CALL CLOSE(3)
0067 TYPE 104,LINE
0068 GO TO 2
C

```

```

C
C
0069 C LINE STRUCTURE ERROR
0070 C
0071 CALL CLOSE(2)
0072 CALL CLOSE(3)
0073 TYPE 105,LINE
0074 STOP
C
C
0075 C FORMAT DECLARATIONS
0076 C
0077 FORMAT(1HS,'INPUT FILENAME= ')
0078 FORMAT(1HS,'OUTPUT FILENAME= ')
0079 FORMAT(120A1)
0080 FORMAT(1X,'NORMAL EXIT AFTER ',13,' LINES!')
0081 FORMAT(1X,'***WARNING*** FILE ERROR IN LINE ',13/)
0082 FORMAT(1X,'***WARNING*** LINE STRUCTURE ERROR IN LINE ',13/)
0083 FORMAT(120A1)
0084 FORMAT(1X,1005)
0085 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001374	342
SPDATA	000010	4
SIDATA	000304	98
SVARS	000654	214

TOTAL SPACE ALLOCATED = 002564 698

NO FPP INSTRUCTIONS GENERATED

PREMAP,LP/1:1=PREMAP

```

0001      SUBROUTINE FILEN(UNIT,FXT)
C
C      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
C      FORM THE TTY DEVICE &
C      DEFAULT DEVICE
C      UNLESS SPECIFIED IN INPUT STRING
C      UNIT=UNIT NUMBER
C      FXT = LOGICAL*1 BUFFER OF EXTENSION
C
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*1 INSTR,DOT,BLANK,FXT
0004      DIMENSION INSTR(40)
0005      DIMENSION EXT(3)
0006      DATA BLANK,DOT/ ' ','.'/
C
C      INPUT FILE
0007      READ (5,99)(INSTR(I),I=1,40)
0008      FORMAT(40A1)
C      CHECK FOR END OF LINE
0009      DO 1600 I=40,1,-1
0010      J=1
0011      IF (INSTR(I).NE.BLANK)GO TO 1601
0012      TYPE 151
0013      FORMAT(1H$,'>')
0014      GO TO 152
0015      DO 1602 I=1,J
0016      IF (INSTR(I).NE.BLANK) GO TO 1602
C
C      BLANK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
C
0017      DO 1603 K=1,J-1
0018      INSTR(K)=INSTR(K+1)
0019      INSTR(J)=BLANK
0020      J=J-1
0021      GO TO 1601
0022      CONTINUE
0023      DO 1603 I=1,J
0024      IF (INSTR(I).EQ.DOT) GO TO 25
0025      INSTR(J+1)=DOT
0026      INSTR(J+2)=EXT(1)
0027      INSTR(J+3)=EXT(2)
0028      INSTR(J+4)=EXT(3)
0029      J=J+4
0030      CALL SCAN(INSTR,J)
0031      CALL ASSIGN(UNIT,INSTR,J)
0032      RETURN
0033      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000444	146 PW,1,CON,LCI
SIDATA	000042	17 PW,D,CON,LCI

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13:28:11

FORTRAN IV-PLUS V02-SIF
FILEN.FTH /MP

SVARS 000060 24 PW,D,CON,I,CL
STMP'S 000004 2 HW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000572 1W9

NO FPP INSTRUCTIONS GENERATED

10-AUG-80

13:28:14

FORTAN IV-PLUS V02-51E

FILEN.FTM /WH

```

C
0001 SUBROUTINE SCAN(RUF,LTH)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 RUF,DEVICE
0004 DIMENSION RUF(1),DEVICE(4)
0005 DATA DEVICE/'S','Y','O','I':/
0006 DO 1 I=1,LTH
0007 IF(RUF(I).EQ.DEVICE(4))RETURN
0008 LTH=LTH+4
0009 DO 2 I=LTH,S,-1
0010 RUF(I)=RUF(I-4)
0011 DO 3 I=1,4
0012 RUF(I)=DEVICE(I)
0013 RETURN
0014 END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61 RW,I,CON,LCL
SIDATA	000012	5 RW,D,CON,LCL
SVARS	000006	3 RW,D,CON,LCL
STEMPS	000002	1 RW,D,CON,LCL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

FILEN.LP/II:1=FILEN/MOTR

END

DATE
FILMED

12-80

DTIC

AD-A091 663

GTE PRODUCTS CORP NEEDHAM HEIGHTS MA COMMUNICATION S--ETC F/G 5/8
SPEECH OPTIMIZATION AT 9600 BITS/SECOND, VOLUME 2, REAL-TIME 50--ETC (U)
SEP 80 A J GOLDBERG, L COSELL, S KWON DCA100-78-C-0064

UNCLASSIFIED

NL

9 of 9

AD-A091 663



END
DATE
FILMED
12-80
DTIC


```

C
C      ASSIGN BITS TO OCT ELEMENTS
C
C      ANALYSIS:MODUL=1,SYNTHESIS:MODUL=2
C
0028      INSTR=1
0029      IF(MODUL.EQ.1)STATUS=MPCDRA(MIRIT3)
0030      IF(MODUL.EQ.2)STATUS=MPCDRA(MIRIT1)
0031      IF(STATUS.NE.0)GO TO 10
C
C      RESULTS
C
0032      IF(MODUL.EQ.1)CALL MPRDR(MIRIT3,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0033      IF(MODUL.EQ.2)CALL MPRDR(MIRIT1,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0034      IF(STATUS.NE.0)GO TO 10
0035      WRITE(6,911)(I,IRIT(I),I=1,LTH)
0036      FORMAT(1X,6(1X,'IRIT(',I3,')='',I3,2X))
0037      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
0038      10      TYPE 100,INSTR,STATUS
0039      CALL EXIT
0040      100      FORMAT(1X,'***MAP ERROR*** "RITA" INSTR=',I3,' HAS STATUS=',I6/)
0041      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000334	110
SDATA	000160	56
SVARS	000054	22
SECTION	000002	1
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVPA	000152	53
OVPP	002022	521
OVPR	005004	1282
OVZ2	001002	257
OVRL	000016	7
OVRO	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025574 5566

NO FPP INSTRUCTIONS GENERATED

RITA,IP/LI:1=RITA/DE/NOTR

FORTRAN IV-PLUS V02-51E 13:30:52 09-AUG-80 PAGE 2
 /DF/MH
 0028 DATA RUS1,RUS2,BUS3/64,12H,192/
 C
 C QUANTIZE DCT(.) W/ IRIT(.) ACCORDING TO IORDR(.)
 C
 0029 INSTR=1
 0030 STATUS=MPFSTO(OTDCTM,MINIT3,TMP1,TMP2)
 0031 IF(STATUS.NE.O)GO TO 10
 C
 C RESULTS
 C
 0032 DO 21 I=1,LTH
 0033 IORDR(I)=IORDR(I)+1
 0034 CALL MPDRH(OTDCTM,NOUT(I),HYF2,CNVYES,NOUT(LTH))
 0035 DO 22 I=1,LTH
 0036 J=IORDR(I)
 0037 OTDCT(J)=NOUT(I)
 0038 NOUT(I)=0
 0039 WRITE(6,912)(I,OTDCT(I),I=1,LTH)
 0040 FORMAT(1X,4(IX,'OTDCT(',I3,')=' ,I3,2X))
 0041 CALL MPDRH(MINIT3,IRIT(I),RYF2,CNVYES,IRIT(LTH))
 0042 WRITE(6,913)(I,IRIT(I),I=1,LTH)
 0043 FORMAT(1X,4(IX,'MINIT3(',I3,')=' ,I3,2X))
 0044 RETURN
 C
 C DIAGNOSTIC TRAP AREA
 C
 0045 TYPE 100, INSTR, STATUS
 0046 CALL EXIT
 0047 100 FORMAT(1X,'***MAP ERROR*** "OTDCT" INSTR=',I3,' HAS STATUS=',I6/)
 0048 FND

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000446	147 RW,1,CON,I,CL
\$IDATA	000216	71 RW,D,CON,I,CL
\$VARS	000056	23 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GH
MTAPE1	000012	5 RW,D,OVR,GH
MTAPE2	000012	5 RW,D,OVR,GH
NVPI	002020	520 RW,D,OVR,GH
NVR0	002006	515 RW,D,OVR,GH
NVRF	006016	1543 RW,D,OVR,GH
NVRA	000152	53 RW,D,OVR,GH
NVRP	002022	521 RW,D,OVR,GH
NVRA	005004	1282 RW,D,OVR,GH
NVR2	001002	257 RW,D,OVR,GH
NVRL	000016	7 RW,D,OVR,GH
NVR0	000012	5 RW,D,OVR,GH
NVR0T	001032	264 RW,D,OVR,GH
NVRDT	002124	554 RW,D,OVR,GH
MAPDFF	000200	64 RW,D,OVR,GH

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ORCT.PTN /DE/MR

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PAGE 3

TOTAL SPACE ALLOCATED = 031122 6441

NO FPP INSTRUCTIONS GENERATED

ORCT.LP/LI:1=ORCT/DE/NOTR


```

0027 D WRITE(6,699)ICOUNT
0028 D699 FORMAT(1H1,40(1H>),'RECEIVE FRAME # ',16,40(1H<))
      C
      C PASS DATA W/ NO ERRORS
      C
0029 D INSTR=1
0030 D STATUS=MPDR(AQDCT,IAQDCT(1),BYTE2,CNVYES,IAQDCT(LTH))
0031 D IF(STATUS.NE.0)GO TO 10
0032 D INSTR=2
0033 D STATUS=MPDR(AQPR,IAQPR(1),BYTE2,CNVYES,IAQPR(LPARM+1))
0034 D IF(STATUS.NE.0)GO TO 10
0035 D WRITE(6,699)
0036 D699 FORMAT(1X,*** MODEM OUTPUT ***')
0037 D WRITE(6,900)(IAQPR(I),I=1,LPARM)
0038 D900 FORMAT(1X,6(1X,'AQPR(',12,')=' ,16,2X))
0039 D WRITE(6,901)(IAQDCT(I),I=1,LTH)
0040 D901 FORMAT(1X,6(1X,'AQDCT(',13,')=' ,16,2X))
      C
      C ADD TRANSMISSION DELAY
      C
0041 D INSTR=3
0042 D STATUS=MPDR(AQDCT,IAQDCT(1),BYTE2,CNVYES,IAQDCT(LTH))
0043 D IF(STATUS.NE.0)GO TO 10
0044 D INSTR=4
0045 D STATUS=MPDR(AQPR,IAQPR(1),BYTE2,CNVYES,IAQPR(LPARM+1))
0046 D IF(STATUS.NE.0)GO TO 10
0047 D WRITE(6,799)
0048 D799 FORMAT(1X,*** MODEM INPUT ***')
0049 D WRITE(6,800)(IAQPR(I),I=1,LPARM)
0050 D800 FORMAT(1X,6(1X,'AQPR(',12,')=' ,16,2X))
0051 D WRITE(6,801)(IAQDCT(I),I=1,LTH)
0052 D801 FORMAT(1X,6(1X,'AQDCT(',13,')=' ,16,2X))
0053 D RETURN
      C
      C DIAGNOSTIC TRAP AREA
      C
0054 D TYPE 100,INSTR,STATUS
0055 D CALL EXIT
0056 D100 FORMAT(1X,****MAP ERRORS*** "CHANL" INSTR=' ,13,' HAS STATUS=' ,16/)
0057 D END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000644	710 RW,I,CUN,LCL
\$IDATA	000452	149 RW,D,CUN,LCL
\$VARS	000054	22 RW,D,CUN,LCL
MTAPF0	002260	600 RW,D,OVR,GAL
MTAPF1	000012	5 RW,D,OVR,GAL
MTAPF2	000012	5 RW,D,OVR,GAL
CHANFL	001034	270 RW,D,OVR,GAL
NVR1	002020	520 RW,D,OVR,GAL
NVR0	002006	515 RW,D,OVR,GAL
NVRF	006016	1541 RW,D,OVR,GAL

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13:30:59

FORTAN IV-PLUS V02-SIE
CHANL.FTM /DF/WR

NVRA	000152	53	RW,D,NVR,CRI
NVRP	002022	521	RW,D,NVR,CRI
NVRA	005004	1282	RW,D,NVR,CRI
NVR2	001002	257	RW,D,NVR,CRI
NVRL	000016	7	RW,D,NVR,CRI
NVRD	000012	5	RW,D,NVR,CRI
MAPDEF	000200	64	RW,D,NVR,CRI

TOTAL SPACE ALLOCATED = 027430 6028

NO FPP INSTRUCTIONS GENERATED

CHANL.IP/LI:1=CHANL/DF/NOTR


```

0028      DATA RUS1,RUS2,RUS3/64,12H,192/
C
C      SIDEHAND DEQUANTIZATION LAGS SIDEHAND DESERIALIZED
C      BY 1 FRAME DUE TO CSFU IN BACKGROUND
C
0029      INSTR=1
0030      STATUS=VMOV2(ORPRM)
0031      IF(STATUS.NF.0)GO TO 10
C
C      DEQUANTIZE PARCOR(J),J=1,...,LPCN,G,M,VUV
C
0032      INSTR=2
0033      STATUS=MPDRP(PARC,ORPRM,A1)
0034      IF(STATUS.NF.0)GO TO 10
C
C      RESULTS
C
0035      CALL MPDRH(ORPRM,INIT(1),BYTE2,CNVYES,IRIT(LPARM+1))
0036      WRITE(6,927)(J,IRIT(J),J=1,LPARM)
0037      FORMAT(1X,4(1X,'ORPRM','12,')='16,2X))
0038      CALL MPDRH(PARC,DRPAR(1),BYTE4,CNVYES,DRPAR(LPCN))
0039      WRITE(6,928)(J,DRPAR(J),J=1,LPCN)
0040      FORMAT(1X,4(1X,'DRK','12,')='15,8,2X))
0041      CALL MPDRST(RR,DRDC,1,CNVYES)
0042      CALL MPDRST(R9,DRVAR,1,CNVYES)
0043      CALL MPDRST(90,DRG,1,CNVYES)
0044      CALL MPDRST(91,DRM,1,CNVYES)
0045      CALL MPDRST(104,DRV,1,CNVYES)
0046      WRITE(6,925)DRDC,DRVAR
0047      FORMAT(1X,'DRDC(DR)=','F15.8,' AND DRVAR(89)='F15.8)
0048      WRITE(6,929)DRM,DRG,DRV
0049      FORMAT(1X,'DRM='F15.8,' DRG='F15.8,' AND DRV='F15.8)
0050      CALL MPDRH(A1,DRA(1),BYTE4,CNVYES,DRA(LPCN))
0051      WRITE(6,931)(J,DRA(J),J=1,LPCN)
0052      FORMAT(1X,4(1X,'DRA','12,')='15,8,2X))
0053      CALL MPDRST(60,ENG,1,CNVYES)
0054      WRITE(6,932)ENG
0055      FORMAT(1X,'ENG='F15.8)
0056      CALL MPDRH(ORDCTM,ORDCT(1),BYTE2,CNVYES,ORDCT(LTH))
0057      WRITE(6,940)(I,ORDCT(I),I=1,LTH)
0058      FORMAT(1X,4(1X,'ORDCTM','13,')='16,2X))
0059      CALL MPDRH(MIRIT2,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0060      WRITE(6,941)(I,IRIT(I),I=1,LTH)
0061      FORMAT(1X,4(1X,'MIRIT2','13,')='16,2X))
0062      CALL MPDRH(IORDR2,IORDR(1),BYTE2,CNVYES,IORDR(LTH))
0063      WRITE(6,942)(I,IORDR(I),I=1,LTH)
0064      FORMAT(1X,4(1X,'IORDR2','13,')='16,2X))
0065      CALL MPDRH(DCTIT2,DCTI(1),BYTE4,CNVYES,DCTI(LTH))
0066      WRITE(6,943)(I,DCTI(I),I=1,LTH)
0067      FORMAT(1X,4(1X,'DCTI','13,')='15,8,2X))
0068      CALL MPDRST(100,DRDC,1,CNVYES)
0069      CALL MPDRST(101,DRVAR,1,CNVYES)
0070      WRITE(6,944)DRDC,DRVAR
0071      FORMAT(1X,'DRDC(100)='F15.8,' AND VAR(101)='F15.8)
0072      RETURN
C

```

```

C      DIAGNOSTIC TRAP AREA
C
0073      10      TYPE 100, INSTR, STATUS
0074      CALL EXIT
0075      100
0076      FORMAT(1X, '***MAP ERROR*** "DPRM" INSTR=', I4, ' HAS STATUS=', I6 /)
      END

```

PHONGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCCODEF1	001547	433
SBCPDATA	000044	1H
SBCIDATA	001036	271
SBCVARS	000046	23
TAPAF0	002760	600
TAPAF1	000012	5
TAPAF2	000012	5
VVRT	002020	520
VVRN	002006	515
VVRV	006016	1543
VVRA	000152	53
VVRP	002027	521
VVRM	005004	1282
VVR2	001002	257
VVRL	000016	7
VVRD	000012	5
VVRDT	002124	554
VVRQT	001032	269
VADPEF	000200	64

TOTAL SPACE ALLOCATED = 033102 6945

УПАКМ, [P/I, I:1=I)PAM/DE/NCTR

[illegible]

(OR)P# DCT1(,) MAX-TO-MIN W/ ORDERING INFO IN ORDER(,)

FORTMAN IV-PLUS V02-S1E 11:31:17 09-AUG-80 PAGE 2
 SSRT.FTN /DE/WR
 C SORT TMP2 TO DCTM2
 C SORT DCTM1 TO TMP4
 C INSTR=1
 D STATUS=MPSSRT(IORDRM)
 D IF(STATUS.NE.0)GO TO 10
 C
 C RESULTS
 C
 D CALL MPRDR(DCTM2,DCT2(1),HYTE4,CNVYES,DCT2(LTH))
 D CALL MPRDR(IORDRM,IORDR(1),HYTE2,CNVYES,IORDR(LTH))
 D WRITE(6,910)(I,IORDR(I),I=1,LTH)
 D910 FORMAT(1X,6(1X,'IORDR(',I3,')=' ,I3,2X))
 D WRITE(6,909)(I,DCT2(I),I=1,LTH)
 D909 FORMAT(1X,4(1X,'DCT2(',I3,')=' ,E15.8,2X))
 D CALL MPRDR(TMP4,DCT1(1),HYTE4,CNVYES,DCT1(LTH))
 D WRITE(6,911)(I,DCT1(I),I=1,LTH)
 D911 FORMAT(1X,4(1X,'DCT1(',I3,')=' ,E15.8,2X))
 D RETURN
 C
 C DIAGNOSTIC TRAP AREA
 C
 D TYPE 100,INSTR,STATUS
 D900 CALL EXIT
 D901 FORMAT(1X,'***MAP ERROR*** "SSRT" INSTR=',I3,' HAS STATUS=',I6,/)

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCONF1	000470	156 RW,I,CNN,LCL
SDATA	000256	87 RW,D,CNN,LCL
SVARS	000054	72 RW,D,CNN,LCL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
OVR1	002020	520 RW,D,OVR,GHL
OVR0	002006	515 RW,D,OVR,GHL
OVRF	006016	1543 RW,D,OVR,GHL
OVR4	000152	53 RW,D,OVR,GHL
OVRP	002022	521 RW,D,OVR,GHL
OVRB	005004	1282 RW,D,OVR,GHL
OVR2	001002	257 RW,D,OVR,GHL
OVRU	000016	7 RW,D,OVR,GHL
OVRD	000012	5 RW,D,OVR,GHL
MAPDEF	000200	64 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 026024 5642

NO PPP INSTRUCTIONS GENERATED

SSRT,LP/L1:1=SSRT/DE/NUIT

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 DDCT.FTN /DV/WH

```

C      DEQUANTIZATION OF MAINRAND LACS MAINRAND DESERIALIZED
C      BY 1 FRAME DUE TO CSPI IN THE BACKGROUND
C
0027  D      INSTR=1
C      GIVE CSPI HIT ASSIGNMENT FOR UNPACKING OF MAINRAND
0028  C      STATUS=MM(VVCHIT)
0029  D      IF(STATUS.NE.0)GO TO 10
C      DEQUANTIZE DCT(1)
C
0030  C      INSTR=2
0031  C      STATUS=MPFST(DRDCTM,ORDCTM,TMP1,IORDR2)
0032  D      IF(STATUS.NE.0)GO TO 10
C      RESULTS
C
0033  C      CALL MPDRH(DRDCTM,DTDCT(1),BYTE4,CNVYES,DTDCT(LTH))
0034  D      WRITE(6,934)((I,DTDCT(I),I=1,LTH)
0035  D      FORMAT(/1X,4(1X,'DRDCTM','13,')=','F15.8,2X))
0036  C      CALL MPDRH(MIRIT,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0037  D      WRITE(6,935)((I,IRIT(I),I=1,LTH)
0038  D      FORMAT(/1X,4(1X,'MIRIT','13,')=','F15.8,2X))
0039  C      CALL MPDRH(DCT1,DCT1(1),BYTE4,CNVYES,DCT1(LTH))
0040  D      WRITE(6,936)((I,DCT1(I),I=1,LTH)
0041  D      FORMAT(/1X,4(1X,'DCT1','13,')=','F15.8,2X))
0042  C      CALL MPDRH(IORDR1,IORDR(1),BYTE2,CNVYES,IORDR(LTH))
0043  D      WRITE(6,937)((I,IORDR(I),I=1,LTH)
0044  D      FORMAT(/1X,4(1X,'IORDR1','13,')=','F15.8,2X))
0045  C      CALL MPDRH(MIRIT,IRIT(1),BYTE2,CNVYES,IRIT(LTH))
0046  D      WRITE(6,938)((I,IRIT(I),I=1,LTH)
0047  D      FORMAT(/1X,4(1X,'MIRIT','13,')=','F15.8,2X))
0048  C      CALL MPST(102,DRDC,I,CNVYES)
0049  D      CALL MPST(103,DRVAR,I,CNVYES)
0050  D      WRITE(6,940)DRDC,DRVAR
0051  D      FORMAT(1X,'DRDC(102)=','F15.8,2X,' AND DRVAR(103)=','F15.8)
0052  D      RETURN
C
C      DIAGNOSTIC TRAP AREA
C
0053  C      TYPE 100, INSTR, STATUS
0054  C      CALL EXIT
0055  C      FORMAT(1X,'***MAP ERROR*** "DDCT" INSTR=',I4,' HAS STATUS=',I6/)
0056  C      FND
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDDP1	001056	270
SPDATA	000014	6
SIDATA	000506	163
SVARS	000064	26
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5

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11:31:24

FORTMAN IV-PLUS V02-51F
DDCT.FTN /DE/WH

OVPI	002020	520	RW,D,OVR,GRL
OVRO	002006	515	RW,D,OVR,GRL
OVRF	006016	1543	RW,D,OVR,GRL
OVRA	000152	53	RW,D,OVR,GRL
OVRR	002022	521	RW,D,OVR,GRL
OVRR	005004	1242	RW,D,OVR,GRL
OVRT	001002	257	RW,D,OVR,GRL
OVRL	000016	7	RW,D,OVR,GRL
OVRO	000012	5	RW,D,OVR,GRL
OVRO	002124	554	RW,D,OVR,GRL
MAPDEF	000200	64	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 031012 6405

DDCT.LP/L1:1=DDCT/DE/NOTR


```

C
0026 D INSTR=1
0027 STATUS=MPIDCM(X2,DNDCTM,CUSZ)
0028 IF (STATUS.NE.0) GO TO 10
0029 CALL MPRDH(X2,XR(1),RYT4,CNVVFS,XR(LTH2))
0030 WRITE(6,914)(1,XR(1),I=1,LTH2)
0031 INSTR=2
0032 STATUS=EFTIN(X2,1,X2,VSHKT,WORK)
0033 IF (STATUS.NE.0) GO TO 10
C
C RESULTS
C
0034 CALL MPRDH(X2,XR(1),RYT4,CNVVFS,XR(LTH))
0035 WRITE(6,914)(1,XR(1),I=1,LTH)
0036 FORMAT(1X,A(1X,'X',13,1)=',E15.8,2X))
0037 RETURN
C
C DIAGNOSTIC TRAP AREA
C
0038 10 TYPE 100,INSTR,STATUS
0039 CALL EXIT
0040 100 FORMAT(1X,'***MAP ERROR*** "UCTR" INSTR=',13,1 HAS STATUS=',16/)
0041 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCORE1	000406	131
SPDATA	000004	2
SINATA	000172	61
SVAPS	000054	22
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVRP	002022	521
OVRB	005004	1282
OVR2	001002	257
OVR1	000016	7
OVRD	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025662 5593

CTR.LP/LI:1=UCTP/DF/NOTR

[illegible]

FORTRAN IV-PLUS V02-51F
VARDC.FTN /DF/WH

```

0027 STATUS=MPVDNM(NOUTM,YOM,X2)
0028 IF (STATUS.NE.0)GO TO 10
0029 C
0030 C RESULTS
0031 C
0032 CALL MPRST(100,DCRIAS,1,CNVVFS)
0033 WRITE(6,900)DCRIAS
0034 FORMAT(1X,'DCRIAS=',F15.8)
0035 CALL MPRST(101,VAR,1,CNVVFS)
0036 WRITE(6,901)VAR
0037 FORMAT(1X,'VAR=',F15.8)
0038 RETURN
0039 C
0040 C DIAGNOSTIC TRAP AREA
0041 C
0042 C
0043 TYPE 101,INSTR,STATUS
0044 CALL EXIT
0045 FORMAT(1X,'***MAP ERROR*** VARDC INSTR=',I3,' HAS STATUS=',I6/)
0046 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000200	64
SPDATA	000014	6
SIDATA	000160	56
SVARS	000052	21
MTAPF0	002260	600
MTAPE1	000012	5
MTAPF2	000012	5
OVR1	002020	520
OVR0	002006	515
OVRP	006016	1543
OVRB	000152	51
OVRP	002022	521
OVRB	005004	1282
OVR2	001002	257
OVR1	000016	7
OVR0	000012	5
MAPDEF	000200	64

TOTAL SPACE ALLOCATED = 025450 5524

NO FPP INSTRUCTIONS GENERATED

VARDC.LP/L11:=VARDC/DE/MOTR

SC00P1	000466	155	R*,I,CON,LCL
SPDATA	000020	8	R*,D,CON,LCL
SIDATA	000132	45	R*,D,CON,LCL
SVARS	000056	23	R*,D,CON,LCL
ST*MS	000002	1	R*,D,CON,LCL
ATAP*0	002260	600	R*,D,CON,LCL
ATAP*1	000012	5	R*,D,CON,LCL
ATAP*2	000012	5	R*,D,CON,LCL
OVRI	002020	520	R*,D,CON,LCL
OVRO	002006	515	R*,D,CON,LCL
OVFF	006016	1543	R*,D,CON,LCL
OVRA	000152	53	R*,D,CON,LCL
OVPP	002016	514	R*,D,CON,LCL
OVKH	005004	1282	R*,D,CON,LCL
OVW2	001002	257	R*,D,CON,LCL
OVRI	000016	7	R*,D,CON,LCL
OVRO	000012	5	R*,D,CON,LCL
WAPD*4	000200	64	R*,D,CON,LCL

TOTAL SPACE ALLOCATED = 025716 5607

NO FPP INSTRUCTIONS GENERATED

OUTPUT,LP/1:1=OUTPUT/DE/NOTR


```

FORTRAN IV-PLUS 002-SIP 21:16:16 11-SEP-80 PAGE 2
SMP.FIN

0024 DATA LUNG,SHORT,CNVYFS,CNVNUP/0,1,1,0/
0029 DATA RUS1,RUS2,RUS3/64,128,192/

C
C ADD 3 FRAMES DELAY IN INPUT FOR SMP CALCULATION
C
0030 I=516=0
0031 STATUS=APPROX(SN,NIND(1),WTF2,CNVYFS,NIND(NTPUS))
0032 STATUS=APPROX(N2,TOROR(1),WTF2,CNVYFS,TOROR(NTPUS))
0033 STATUS=APPROX(SN,TOROR(1),WTF2,CNVYFS,TOROR(NTPUS))
0034 STATUS=APPROX(N2,TOROR(1),WTF2,CNVYFS,TOROR(NTPUS))
0035 STATUS=APPROX(N2,TOROR(1),WTF2,CNVYFS,TOROR(NTPUS))
0036 STATUS=APPROX(N2,NIND(1),WTF2,CNVYFS,NIND(NTPUS))
0037 IF(STATUS.NE.0)GO TO 10
0038 K=0 DELAY N & VIV FOR CONSISTENCY
0039 M4=M3
0040 M2=M1
0041 M1=M
0042 IV4=IV3
0043 IV3=IV2
0044 IV2=IV1
0045 IV1=1+IX(VI)

C
C COMPUTE S/N RATIO
C
0046 SUM1=0.0
0047 SUM2=0.0
0048 NIN(1) MATCHES NOUT(NP+1), PRGD NTPUS=EP MATCHES!
0049 DO 3605 I=1,NIND(NP)
0050 SOURCE=FLOAT(NIND(1))
0051 SIGNAT=FLOAT(NOUT(NP+1))
0052 NUISF=SIGNAT-SOURCE
0053 SUM1=SUM1+SOURCE**2
0054 SUM2=SUM2+NUISF**2
0055 SN=0.0
0056 PRGD=NICOUNT-START+1
0057 IF(ERNUM.LT.4)GO TO 200
0058 SN=10.0*ALOG10(SUM1/SUM2)
0059 IF(ERNUM.EQ.4)CSN=SN
0060 CSN=((ERNUM-1.)/ERNUM)*CSN*(1./ERNUM)**50
0061 *WTF(3,255)ICOUNT,SN,CSN,M4,IV4
0062 *WTF(5,255)ICOUNT,SN,CSN,M4,IV4
0063 FORMAT(IX,'PRGD= ',I3,IX,'SN= ',F6.2,IX,'CSN= ',F6.2,IX,
0064 1,' FICH= ',I3,IX,' VIV= ',I2)
0065 CONTINUE
0066 RETURN

C
C DIAGNOSTIC TRAP AREA
C
0067 TYPE 100,INSTR,STATUS
0068 CALL EXIT
0069 FORMAT(IX,'***MAP FROM*** "SNR" INSTR= ',I3,' HAS STATUS= ',I6/)
0070 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000220	232
SDATA	000162	51
SVARS	000110	36
STMPDS	000002	1
STAPE0	002260	600
STAPE1	000012	5
STAPE2	000012	5
DELAY	000020	4
OVRI	002020	520
OVRO	002006	515
OVRF	006016	1543
OVRA	000152	53
OVVP	002022	521
OVRR	005004	1282
OVW2	001002	251
OVVL	000016	7
OVRO	000012	5
WAPDEF	000200	64

TOTAL SPACE ALLOCATED = 026236 5711

SNR,LP/11:1=SNR/0F/NOTR

```

0001 SUBROUTINE TAPE2(10)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 APPEND
0004 COMMON/MTAPE0/NIN(300),NOUT(300)
0005 COMMON/MTAPE1/MSKIP,IST,NTUT1,NTUPS,MTOTO
0006 COMMON/MTAPE2/NEPD,NFPP,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NRF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISM(2),IOATT,IUSUC,IFALN,IORWD
0010 COMMON/MTAPE6/APPEND
0011 DATA IOATT,IUSUC,IFALN/0001400,1,-34/
0012 DATA IORWD,IOWLR,IFVER,IOSPF,IOHLR/02400,0400,-4,02440,01000
0013 DATA IOSPF,IEFDF,IOFDF,IOFDF/02440,-10,03000/
0014 DATA MASK/0377/
0015 DATA MT0/0,2048,0,0,0,0/
0016 DATA MT1/0,2048,0,0,0,0/
0017 GO TO (700,800,900,1100,1000,1200,1300,1400),IO
0018 CALL OPT1
0019 RETURN
0020 CALL OPT2
0021 RETURN
0022 CALL OPT3
0023 RETURN
0024 CALL OPT4
0025 RETURN
0026 CALL OPT5
0027 RETURN
0028 CALL OPT6
0029 RETURN
0030 CALL OPT7
0031 RETURN
0032 CALL OPT8
0033 CALL OPT3
0034 IF (APPEND) CALL OPT4
0035 RETURN
0036 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000226	75 RW,I,CON,I,CL
SPDATA	000072	9 RW,D,CON,I,CL
SIDATA	000002	1 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GMI
MTAPE1	000012	5 RW,D,OVR,GMI
MTAPE2	000012	5 RW,D,OVR,GMI
MTAPE3	017260	2648 RW,D,OVR,GMI
MTAPE4	000004	4 RW,D,OVR,GMI
MTAPE5	000064	26 RW,D,OVR,GMI
MTAPE6	000002	1 RW,D,OVR,GMI

TOTAL SPACE ALLOCATED = 015130 3472

FORTHAN IV-PLUS V02-51F
TAPE2.FTN /MP

11:41:24

10-AUG-80

PAGE 2

NO FPP INSTRUCTIONS GENERATED

TAPE2.I/P/L1:1=TAPE2/N0TH

FORTRAN IV-PLUS V02-51K 11:41:33 10-AUG-80
OPT1.ATM /WR

0051 6000 TYPE 6001
0052 6001 FORMAT(IX,10000INPUT FILE ERROR****/)
0053 MPRM=1
0054 6002 RETURN
0055 END

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCNDF1	000646	211 RW,1,CON,I,CL
SPOATA	000014	6 RW,D,CON,I,CL
SIDATA	000062	25 RW,D,CON,I,CL
SVARS	000212	69 RW,D,CON,I,CL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
MTAPE3	012260	2648 RW,D,OVR,GHL
MTAPE4	000004	2 RW,D,OVR,GHL
MTAPE5	000064	26 RW,D,OVR,GHL
MTAPE6	000002	1 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 016034 3598

NO PPV INSTRUCTIONS GENERATED

OPT1.6P/LI:1=OPT1/NOTR


```

0001 SUBROUTINE OPT2
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),NOUT(300)
0005 COMMON/MTAPE1/MSKIP,IST,MTOT1,NTUPS,MTOTO
0006 COMMON/MTAPE2/MEND,MERR,MFILE,MINS,NOUTS
0007 COMMON/MTAPE3/MNF(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,IREG
0009 COMMON/MTAPE5/MASK,ISM(2),IDATT,IOSUC,IEALN,IOKWD
0010 1,IOLEN,IEVER,IOSPF,IEFUF,IOFUF,IOFUA,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IEALN/0001400,1,-34/
0014 DATA IOKWD,IOLEN,IEVER,IOSPF,IOFUF/02400,0400,-4,02440,01000/
0015 DATA IOSPF,IEFUF,IOFUF/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MT1/0,2048,0,0,0,0/
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PROGRAM SECTIONS

NAME SIZE ATTRIBUTES

FORTRAN IV-PLUS V02-SIE
OPT2.FTN /WR

11:41:41 10-AUG-80

PAGE 2

SCODE1	000432	141	RW,I,CON,I,CL
SPDATA	000014	6	RW,D,CON,I,CL
SIDATA	000062	25	RW,D,CON,I,CL
SVANS	000212	69	RW,D,CON,I,CL
MTAPE0	002260	600	RW,D,OVR,GRL
MTAPE1	000012	5	RW,D,OVR,GRL
MTAPE2	000012	5	RW,D,OVR,GRL
MTAPE3	012260	2648	RW,D,OVR,GRL
MTAPE4	000004	2	RW,D,OVR,GRL
MTAPE5	000064	26	RW,D,OVR,GRL
MTAPE6	000002	1	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 015620 352R

NO FPP INSTRUCTIONS GENERATED

OPT2.LP/I.1:1=OPT7/NOTR

```

0001 SUBROUTINE OPT3
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/TAPE0/NTIN(300),NOUT(300)
0005 COMMON/TAPE1/NSKIP,IST,NTOT1,NTOPS,NTOTO
0006 COMMON/TAPE2/NEND,NERR,NFILE,NINS,NOUTS
0007 COMMON/TAPE3/NRF(1324),NHUF(1324)
0008 COMMON/TAPE4/IST,IRFC
0009 COMMON/TAPE5/MASK,ISW(2),IOATT,IOSUC,IFALN,IORWD
0010 1,IORLH,IEVER,IOSPF,IEKOF,IORLH,MT0(6),MT1(6),DSW
0011 COMMON/TAPE6/APPEND
0012 DIMENSION ICAPD(64)
0013 DATA IOATT,IOSUC,IFALN/0001400,1,-34/
0014 DATA IORWD,IORLH,IEVER,IOSPF,IORLH/02400,0400,-4,02440,01000
0015 DATA IOSPF,IEKOF,IEKOF/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MT1/0,2048,0,0,0,0/
0019
0018 C INITIALIZE(=3)
0019 C
0020 C
0021 CONTINUE
0022 NEND=0
0023 IF((NINS+NOUTS).GT.1)GO TO 901
0024 UNIT=0
0025 DO 902 LUN=2,3
0026 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 902
0027 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 902
0028 C>>>>>>MT: ATTACH
0029 CALL ASNLUN(LUN,MT,UNIT,DSW)
0030 IF(DSW.NE.1)GO TO 6000
0031 CALL WT0IOIOATT,LUN,1,0,ISW(1),0,DSW)
0032 IF(IOSUC.EQ.IAND(MASK,ISW(1)))GO TO 902
0033 IF(IAND(IFALN,MASK).NE.IAND(MASK,ISW(1)))GO TO 6000
0034 UNIT=UNIT+1
0035 DO 903 LUN=2,3
0036 IF(NINS.NE.0.AND.LUN.FO.2)GO TO 903
0037 IF(NOUTS.NE.0.AND.LUN.FO.3)GO TO 903
0038 C>>>>>>MT: REWIND
0039 CALL WT0IOIORWD,LUN,1,0,ISW(1),0,DSW)
0040 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6001
0041 CONTINUE
0042 IF(NFILE.EQ.0)GO TO 995
0043 IF(NINS.NE.0)GO TO 913
0044 C>>>>>>MT: FILE SKIP
0045 DO 907 I=1,NFILE-1
0046 CALL GETADH(MT0,NRF(301))
0047 CALL WT0IOIORLH,2,1,0,ISW(1),MT0,DSW)
0048 IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6003
0049 MT0(1)=1
0050 CALL WT0IOIOSPF,2,1,0,ISW(1),MT0,DSW)
0051 DO 912 J=1,NSKIP
0052 IF(NINS.EQ.0)GO TO 910
0053 C>>>>>>DISK INPUT
0054 DO 914 I=1,16

```

```

0049 READ(2,FMD=905,ERR=6002)(ICARD(JJ),JJ=1,64)
0050 K=64*(J-1)+300
0051 DO 914 JJ=1,64
0052 914 NMF(K+JJ)=ICARD(JJ)
0053 GO TO 912
C>>>>>TAPF INPUT
0054 910 CALL GETADR(MTO,NMF(301))
0055 CALL WTG10(IORLH,2,1,0,ISW(1),MTO,DSW)
0056 IF(IUSUC,FQ,IAND(MASK,ISW(1)))GO TO 912
0057 IF(IAND(TEVER,MASK),FQ,IAND(MASK,ISW(1)))GO TO 6002
0058 IF(IAND(TEVER,MASK),NF,IAND(MASK,ISW(1)))GO TO 912
0059 905 NEND=1
0060 912 CONTINUE
0061 995 IFEG=1
0062 1ST=1ST+300
0063 1ST=1ST-NTUPS
0064 RETURN
0065 6000 TYPE 100
0066 GO TO 6010
0067 6001 TYPE 101
0068 GO TO 6010
0069 6002 TYPE 102
0070 GO TO 6010
0071 6003 TYPE 103
0072 6010 NFR=1
0073 RETURN
0074 100 FORMAT(1X,'*** MT: ATTACH FAILURE! ***')
0075 101 FORMAT(1X,'*** MT: REMIND FAILURE! ***')
0076 102 FORMAT(1X,'*** INPUT FILE "NSKIP" FAILURE! ***')
0077 103 FORMAT(1X,'*** MT: "NFIL" FAILURE! ***')
0078 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDPF1	001212	325
SPDATA	000020	R
SIDATA	000332	109
SVARS	000214	70
STEMPS	000004	2
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000007	1

TOTAL SPACE ALLOCATED = 016662 1801

NO FPP INSTRUCTIONS GENERATED

OPT3.LP/1.1:1=OPT3/NOTR

```

0001 SUBROUTINE OPT4
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL:1 APPEND
0004 COMMON/MTAPE0/NTN(300),NOUT(300)
0005 COMMON/MTAPE1/NSKIP,IST,NTUT1,NTUPS,NTUTD
0006 COMMON/MTAPE2/NFND,NFPR,NFIF,NINS,NOUTS
0007 COMMON/MTAPE3/NHFC(1324),NRUF(1324)
0008 COMMON/MTAPE4/IST,INFC
0009 COMMON/MTAPE5/MASK,ISM(2),IOATT,IOSUC,IFALN,IORWD
0010 1.IOWLH,IFVER,IOSPF,IFEOF,IOFOR,IORLR,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IFALN/0001800,1,-34/
0014 DATA IORWD,IOWLH,IFVER,IOSPF,IOFOR/02400,0400,-4,02440,01000
0015 DATA IOSPF,IFEOF,IOFOR/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
    DATA MT1/0,2048,0,0,0,0/

C
C OUTPUT FILE SKIP(=4)
C
0018 CONTINUE
0019 NFPR=0
0020 NFND=0
0021 IREG=1
0022 CALL GETADR(MT1(1),NRUF(1))
0023 CALL MT0IO(IORLR,3,1,0,ISM(1),MT1,DSW)
0024 IF(IOSUC.EQ.IAND(MASK,ISM(1)))GO TO 1
0025 IF(IAND(IFEOF,MASK).NE.IAND(MASK,ISM(1)))GO TO 6000
0026 CALL GETADR(MT1(1),NRUF(1))
0027 CALL MT0IO(IORLR,3,1,0,ISM(1),MT1,DSW)
0028 IF(IOSUC.EQ.IAND(MASK,ISM(1)))GO TO 1
0029 IF(IAND(IFEOF,MASK).NE.IAND(MASK,ISM(1)))GO TO 6000
0030 MT1(1)=-1
0031 CALL MT0IO(IOSPF,3,1,0,ISM(1),MT1,DSW)
0032 RETURN
0033 TYPE 100
0034 RETURN
0035 100 FORMAT(IX,'*** OUTPUT "APPEND" FAILURE! ***'/)
0036 END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCONF1	000274	74
SDATA	000014	6
SINATA	000114	38
SVARS	000200	64
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	032260	2648
MTAPE4	000004	7
MTAPE5	000004	26

FORTMAN IV-PLUS V02-SIF
OPT4.FTN /WR

11:41:55 10-AUG-80

PAGE 2

NTAPE% 000002 1 RW.D.OVP.GHI.

TOTAL SPACE ALLOCATED = 015432 1469

NO FPP INSTRUCTIONS GENERATED

OPT4.LP/1.1:1=OPT4/NOTR

```

SUBROUTINE DPTS
  IMPLICIT INTEGER(A-Z)
  LOGICAL:91 APPEND
  COMMON/MTAPE0/MIN(300),MUNIT(300)
  COMMON/MTAPE1/NSKIP,IST,NTOT1,NTUPS,MTOTO
  COMMON/MTAPE2/MEND,NEPP,NFILE,NINIS,MUNIT5
  COMMON/MTAPE3/NHF(1324),NSHF(1324)

```

```

END-OF-FILE(=5)
CONTINUE
NERR=0
NEND=0
IF(NOUTS.EQ.0)GO TO 1001
DISK EOF
CALL CLOSE(3)
GO TO 1003
TAPE EOF
DO 1002 I=1,2
CALL WTQIO(IDXOF,3,1,0,ISW(1))
IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
WTI(1)=-1
CALL WTQIO(IDSPF,3,1,0,ISW(1),MT1,DSW)
IF(IOSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
IFEG=1
RETURN
TYPE 100
NERR=1
RETURN
FORMAT(IX,'*** MT: EOF FAILURE! ***')
END

```

PROGRAM SECTIONS		ATTRIBUTES
NAME	SIZE	

SCUDF1	000704	66	RM.D.CON,I.CL
SPDATA	000016	6	RM.D.CON,I.CL
SIDATA	000076	11	RM.D.CON,I.CL
SVARS	000202	65	RM.D.CON,I.CL
MTAF0	002260	600	RM.D.OVP,GHL
MTAF1	000012	5	RM.D.OVP,GHL
MTAF2	000012	5	RM.D.OVP,GHL
MTAF3	012760	2644	RM.D.OVP,GHL

FORTRAN IV-PLUS V02-51P
OPTS.FTN /MR

11:42:01 10-AUG-80

PAGE 2

MTAPE4	000004	2	RM,D,OVR,GAL
MTAPE5	000004	26	RM,D,OVR,GAL
MTAPE6	000002	1	RM,D,OVR,GAL

TOTAL SPACE ALLOCATED = 015376 3455

NO FPP INSTRUCTIONS GENERATED

OPTS.IP/LI:1=OPTS/NOTR


```

0001 SUBROUTINE OPT6
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL CAL91 APPEND
0004 COMMON/MTAPE/ININ(300),NOUT(300)
0005 COMMON/MTAPE/NSKIP,IST,NTOTL,NTIUS,NTOTO
0006 COMMON/MTAPE2/INEND,NREF,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NREF(1324),NINH(1324)
0008 COMMON/MTAPE4/IST,INFG
0009 COMMON/MTAPE5/MASK,ISW(2),IUATT,IUSUC,IFAIN,IORWD
0010 1,IORH,IFVPR,IUSPF,IFORF,IORH,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IUATT,IUSUC,IFAIN/0001400,1,-34/
0014 DATA IORWD,IORH,IFVPR,IUSPF,IORH/02400,0400,-4,02440,01000/
0015 DATA IUSPF,IFORF,IORH/02440,-10,03000/
0016 DATA MASK/0.577/
0017 DATA MT0/0,2040,0,0,0,0/
0018 DATA MT1/0,2040,0,0,0,0/
0019
0018 REMIND/SEARCH INPUT ONLY(=6)
0019
0018 CONTINUE
0019 NEND=0
0020 NREF=0
0021 IF(NINS.FO.0)...0 TO 1222
0022 IF(NINS.FO.0)GO TO 1204
0023 REMIND=2
0024 GO TO 1204
0025
0025 >>>>>MT: REMIND
0026 1222 CALL WTOIO(IORWD,2,1,0,ISW(1),0,DSW)
0027 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6002
0028 IF(NFILE.FE.1)GO TO 1204
0029 DO 1205 I=1,NFILE-1
0030 CALL GETADR(MT0,NREF(301))
0031 CALL WTOIO(IORH,2,1,0,ISW(1),MT0,DSW)
0032 IF(IUSUC.NE.IAND(MASK,ISW(1)))GO TO 6000
0033 MT0(1)=1
0034 1205 CALL WTOIO(IUSPF,2,1,0,ISW(1),MT0,DSW)
0035 1204 DO 1210 J=1,NSKIP
0036 IF(NINS.FO.0)GO TO 1201
0037 >>>>>>DISK SEARCH
0038 DO 1207 I=1,16
0039 READ(2,END=1202,ERR=6001)(ICARD(IJ),IJ=1,64)
0040 K=64*(I-1)+300
0041 DO 1207 JJ=1,64
0042 NREF(K+JJ)=ICARD(IJ)
0043 GO TO 1210
0044 >>>>>>TAPE SEARCH
0045 1201 CALL GETADR(MT0,NREF(301))
0046 CALL WTOIO(IORH,2,1,0,ISW(1),MT0,DSW)
0047 IF(IUSUC.FO.IAND(MASK,ISW(1)))GO TO 1210
0048 IF(IAND(IFVPR,MASK).FO.IAND(MASK,ISW(1)))GO TO 6000
0049 IF(IAND(IFORF,MASK).NE.IAND(MASK,ISW(1)))GO TO 1210
0050 NEND=1
0051 GO TO 6000
0052 CONTINUE
0053
0054 1210

```

```

0049      IST=IST*100
0050      IST=IST-NTHPS
0051      RETURN
0052      TYPE 100
0053      GO TO 6010
0054      TYPE 101
0055      GO TO 6010
0056      TYPE 102
0057      NTHPS=1
0058      RETURN
0059      100  FORMAT(IX,'*** INPUT FILE "ASKIP" FAILURE! ***')
0060      101  FORMAT(IX,'*** INPUT FILE "NFILE" FAILURE! ***')
0061      102  FORMAT(IX,'*** MT: REWIND FAILURE! ***')
0062      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000712	229
SPDATA	000014	6
SIDATA	000246	83
SVARS	000210	68
STMPSP	000004	2
MTAPE0	002260	600
MTAPE1	000012	5
MTAPE2	000012	5
MTAPE3	012260	2648
MTAPE4	000004	2
MTAPE5	000064	26
MTAPE6	000002	1

TOTAL SPACE ALLOCATED = 016266 3675

NO FPP INSTRUCTIONS GENERATED

OPT6.LP/L1:1=OPT6/MOTR

FORTMAN IV-PLUS V02-S1P
OPT7.FIN /MH

```

0001 SUBROUTINE OPT7
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*1 APPEND
0004 COMMON/MTAPE0/MIN(300),ROUT(300)
0005 COMMON/MTAPE1/MSKIP,1ST,NTUT1,NTUPS,NTOTO
0006 COMMON/MTAPE2/WEND,NEFH,NELLY,MINS,NOUTS
0007 COMMON/MTAPE3/NHF(1324),MHUF(1324)
0008 COMMON/MTAPE4/LST,INFG
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IEALN,IOHWD
0010 1,IOHFR,IEVER,IOSPF,IEFOP,IOEOP,IOHFR,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION ICARD(64)
0013 DATA IOATT,IOSUC,IEALN/0001400,1,-34/
0014 DATA IOHWD,IOHFR,IEVER,IOSPF,IOHFR/02400,0400,-4,02440,01000 /
0015 DATA IOSPF,IEFOP,IOEOP/02440,-10,03000/
0016 DATA MASK/0377/
0017 DATA MT0/0,2048,0,0,0,0/
0018 DATA MT1/0,2048,0,0,0,0/
0019
0018 C CLOSE INPUT FILE
0019 C
0018 1300 CONTINUE
0019 NEND=0
0020 NFR=0
0021 IF(MINS.EQ.1)CALL CLOSE(2)
0022 RETURN
0023 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODF1	000012	13 RW,I,CON,LCL
SPDATA	000004	2 RW,D,CON,LCL
SIDATA	000004	2 RW,D,CON,LCL
SVARS	000200	64 RW,D,CON,LCL
MTAPE0	002260	600 RW,D,OVR,GHL
MTAPE1	000012	5 RW,D,OVR,GHL
MTAPE2	000012	5 RW,D,OVR,GHL
MTAPE3	012260	2648 RW,D,OVR,GHL
MTAPE4	000004	2 RW,D,OVR,GHL
MTAPE5	000064	26 RW,D,OVR,GHL
MTAPE6	000002	1 RW,D,OVR,GHL

TOTAL SPACE ALLOCATED = 015120 3368

NO FPP INSTRUCTIONS GENERATED

OPT7,1P/1,1:1=OPT7/NOTR

```

0001 SUBROUTINE OPTN
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL A1 EXTIN,EXTOUT,APPEND
0004 COMMON/MTAPE0/MIN(100),NOUT(100)
0005 COMMON/MTAPE1/MSKIP,IST,MTOT1,NTUPS,NTOT0
0006 COMMON/MTAPE2/END,NERR,NFILE,NINS,NOUTS
0007 COMMON/MTAPE3/NPF(1324),NNUF(1324)
0008 COMMON/MTAPE4/IST,IRFG
0009 COMMON/MTAPE5/MASK,ISW(2),IOATT,IOSUC,IEAIN,IORWD
0010 1,IOUW,IEVER,IOSPE,IEFDF,IEDEL,IEUW,MT0(6),MT1(6),DSW
0011 COMMON/MTAPE6/APPEND
0012 DIMENSION EXTIN(3),EXTOUT(3)
0013 DIMENSION ICARD(64)
0014 DATA YES,NO,'Y','N','/
0015 DATA EXTIN/1,'N','P'//
0016 DATA EXTOUT/0,'U','T'//
0017 DATA END,NERR,NFILE/0,0,0/
0018 DATA MSKIP,IST/1,1/
0019 DATA IOATT,IOSUC,IEAIN/0001400,1,-34/
0020 DATA IORWD,IEUW,IEVER,IOSPE,IOUW,MT0400,0400,-4,02440,01000
0021 DATA IOSPE,IEFDF,IEDEL/02440,-10,03000/
0022 DATA MASK/0377/
0023 DATA MT0/0,2048,0,0,0,0/
0024 DATA MT1/0,2048,0,0,0,0/

C
C GFT FILE INFO(=R)
C
0024 APPEND=.FALSE.
0025 TYPE 100
0026 FORMAT(/,1HS,'IS THE INPUT ON MAG. TAPE(Y/N)? ')
0027 READ(5,102,END=999,FRR=900)ANSER
0028 FORMAT(A1)
0029 NINS=1
0030 IF(ANSER.FO.NO)NINS=1
0031 NOUTS=1
0032 TYPE 103
0033 FORMAT(1HS,'IS THE OUTPUT GOING TO MAG TAPE(Y/N)? ')
0034 READ(5,102,END=999,FRR=901)ANSER
0035 IF(ANSER.FO.NO)NOUTS=1
0036 IF(NOUTS.FO.1)GO TO 5
0037 TYPE 104
0038 FORMAT(1HS,'APPEND DATA? ')
0039 READ (5,102,FND=999,FRR=902)ANSER
0040 IF(ANSER.FO.YES)APPEND=.TRUE.
0041 IF(NOUTS.FO.0)GO TO 151
C
0042 RSX11 SUPPORTED FILE
0043 TYPE 105
0044 FORMAT(1HS,'OUTPUT FILE NAME= ')
0045 CALL FILEN(1,EXTOUT)
C
C BEGINNING OF INPUT
C
0045 IF(MINS.FO.1)GO TO 155
0046 TYPE 106
0047 FORMAT(1HS,'MT FILE NO.=(13) ')
0048 READ(5,101,FND=999,FRR=904)NFILE

```

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FORTHAN IV-PLUS V02-SIF
OPTA.FTN

```

0049 101  FORMAT(13)
0050      GO TO 14
0051 155  TYPE 107
0052 107  FORMAT(1HS,'INPUT FILE NAME= ')
0053      CALL FILEN(2,EXTN)
0054      NFILE=1
0055 14    RETURN
0056 999   CALL EXIT
0057      END
    
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES	
SCODE1	000524	170	RW,I,CUN,I,CL
SPDATA	000010	4	RW,D,CUN,I,CL
SIDATA	000300	96	RW,D,CUN,I,CL
SVARS	000214	70	RW,D,CUN,I,CL
MTAPE0	002260	600	RW,D,OVR,GRL
MTAPE1	000012	5	RW,D,OVR,GRL
MTAPE2	000012	5	RW,D,OVR,GRL
MTAPE3	012260	2648	RW,D,OVR,GRL
MTAPE4	000004	2	RW,D,OVR,GRL
MTAPE5	000064	26	RW,D,OVR,GRL
MTAPE6	000002	1	RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 016126 3627

NO FPP INSTRUCTIONS GENERATED

FORTHAM IV-PLUS V02-51E
OPTD.FIN /WN

11:42:22 10-AUG-80

PAGE 4

SVARS 000060 24
STEMPS 000004 2

RM.D.COM,LCI
RM.D.COM,LCI

TOTAL SPACE ALLOCATED = 000622 201

NO FPP INSTRUCTIONS GENERATED

FORTRAN IV-PLUS V02-51F /NR
OPTR.FTN

```

C
0001      SUBROUTINE SCAN(RUF,LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL*8 RUF,DEVICE
0004      DIMENSION RUF(1),DEVICE(4)
0005      DATA DEVICE/'S','V','O','.'/
0006      DO 1 I=1,LTH
0007      1 IF(RUF(I).EQ.DEVICE(4))RETURN
0008      LTH=LTH+4
0009      DO 2 I=LTH,5,-1
0010      2 RUF(I)=RUF(I-4)
0011      DO 3 I=1,4
0012      3 RUF(I)=DEVICE(I)
0013      RETURN
0014      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCNDEF	000172	61
SIDATA	000012	5
SVARS	000006	3
STEPS	000002	1

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

OPTR,LP/LI:1=OPTN/NOTR

FORTRAN IV-PLUS V02-516 21:16:21 11-SEP-80
OPEN,IN /06/88

```

0040 904 TYPE 106
0041 106 FORMATTIMS,M1 FILE NO.=(13) ' )
0042 HEAD(S,101),END=999,FILE=004)NFILE
0043 101 FORMAT(13)
0044 GO TO 14
0045 CONTINUE
C155 TYPE 107
C107 FORMATTIMS,'INPUT FILE NAME= ' )
C CALL FILEMC2,EXTIN)
C CALL ASSIGN(2,'15,1001VOICHA.WIN',17)
0046 NFILE=1
0047 RETURN
0048 14 RETURN
0049 999 CALL EXIT
0050 END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SC0041	000330	108 RW,I,CON,LCL
SPDATA	000042	17 RW,D,CON,LCL
SIDATA	000100	32 RW,D,CON,LCL
SVARS	000214	70 RW,D,CON,LCL
MTAPE0	002260	600 RW,D,OVR,GRL
MTAPE1	000012	5 RW,D,OVR,GRL
MTAPE2	000012	5 RW,D,OVR,GRL
MTAPE3	012260	2648 RW,D,OVR,GRL
MTAPE4	000004	2 RW,D,OVR,GRL
MTAPE5	000064	26 RW,D,OVR,GRL
MTAPE6	000002	1 RW,D,OVR,GRL

TOTAL SPACE ALLOCATED = 015564 3514

NO FPP INSTRUCTIONS GENERATED

```

0001      SUMROUTINE FUEN(UNIT,EXT)
0002      THIS SUMROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
0003      FROM THE TTY DEVICE 5
0004      DEFAULT DEVICE
0005      UNLESS SPECIFIED IN INPUT STRING
0006      UNIT=UNIT NUMBER
0007      EXT = LOGICAL+1 BUFFER OF EXTENSION
0008
0009      IMPLICIT INTEGER(A-Z)
0010      LOGICAL+1 INSTR,DUT,HUNK,EXT
0011      DIMENSION INSTR(40)
0012      DIMENSION EXT(3)
0013      DATA HUNK,DUT/ ' ',' ' /
0014
0015      INPUT FILE
0016      READ (5,99,FND=999,FRR=152)(INSTR(I),I=1,40)
0017      FORMAT(40A1)
0018      CHECK FOR END OF LINE
0019      DO 1600 I=40,1,-1
0020      J=1
0021      IF (INSTR(I).NE.HUNK)GO TO 1601
0022      TYPE 151
0023      FORMAT(1HS,' ')
0024      GO TO 152
0025      DO 1602 I=1,J
0026      IF (INSTR(I).NE.HUNK) GO TO 1602
0027
0028      MAKE DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
0029      DO 1603 K=1,J-1
0030      INSTR(K)=INSTR(K+1)
0031      INSTR(J)=HUNK
0032      J=J-1
0033      GO TO 1601
0034      CONTINUE
0035      DO 103 I=1,J
0036      IF (INSTR(I).EQ.DUT) GO TO 25
0037      INSTR(J+1)=DUT
0038      INSTR(J+2)=EXT(1)
0039      INSTR(J+3)=EXT(2)
0040      INSTR(J+4)=EXT(3)
0041      J=J+4
0042      CALL SCAN(INSTR,J)
0043      CALL ASSIGN(UNIT,INSTR,J)
0044      RETURN
0045      CALL EXIT
0046      END
  
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCOMP1	000372	157 P*,I,COR,BCL
SIDATA	000044	18 R*,O,COR,BCL

PONTMAN IV-0105 002-514
OPT0A.FIN /06/88

21:16:25 11-SEP-80

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SVAPS 000060 24 M,D,CIN,ICL
STFAPS 000004 2 M,D,CIN,ICL

TOTAL SPACE ALLOCATED = 000622 201

NO PPP INSTRUCTIONS GENERATED

```

0001      SUBROUTINE SCAM(PHF, LTH)
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL L1, RUF, DEVICE
0004      DIMENSION RUF(1), DEVICE(4)
0005      DATA DEVICE/'S', 'Y', 'O', '2' /
0006      DO 1 1=1, LTH
0007      IF RUF(1).EQ. DEVICE(4) THEN RETURN
0008      LTH=LTH+4
0009      DO 2 1=LTH, S, -3
0010      RUF(1)=RUF(1-4)
0011      DO 3 1=1, 4
0012      RUF(1)=DEVICE(1)
0013      RETURN
0014      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCDEF1 000172	61	RW, L, COM, LCL
SIDATA 000012	5	RW, D, COM, LCL
SVARS 000006	3	RW, D, COM, LCL
STEMPS 000007	1	RW, D, COM, LCL

TOTAL SPACE ALLOCATED = 000214 70

NO FOR INSTRUCTIONS GENERATED

OPTRA.LP/L1:1=OPTRA/DE/MOTR

```

FOURMAN IV-PLUS V02-51F
PREMAP.FTN /TR:HLUCFS/WR 13:17:26 10-AUG-80 PAGE 1

C PREMAP.FTN ORIGINATED:29-MAY-79
C UPDATED:04-JUN-79
C THIS PROGRAM INTERPRETS ASCII TEXT FILES FOR THE MAP
C ASSEMBLER AND ALIGNS ALL FIELDS W/ SPACES AND REMOVES
C ALL TABS. ALL FILES ARE ASSUMED TO BE 120 COLUMNS WIDE.
C OUTPUT LINE FORMAT:
C COLUMNS 1,2...7 LABEL
C COLUMNS 8 BLANK
C COLUMNS 9,10...55 SOURCE CODE
C COLUMNS 56 BLANK
C COLUMNS 57,58...120 COMMENTS
C
0001 HYFF BLANK,TAB,HYPHEN,STAR,CR,DUMMY,SCOLON
0002 HYFF ALIAS
0003 HYFF A(200),I(200)
0004 LOGICAL%1 IN(3),OUT(3),OUTPUT
0005 DATA IN/'T','X','Y','Z','A','P'/'
0006 DATA BLANK,TAB,HYPHEN,STAR,CR,SCOLON/040,011,047,052,015,073/
C
0007 TYPE 100
0008 CALL FILEN(2,IN)
0009 TYPE 101
0010 CALL FILEN(3,OUT)
0011 LINE=0
C
C HEAD EACH SOURCE "LINE"
C
0012 LINE=LINE+1
0013 OUTPUT=.TRUE.
0014 HEAD(2,102,END=200,ERR=300)(A(1),1=1,120)
C
C "BLANK" OUT OUTPUT LINE
C
0015 DO 10 I=1,120
0016 I(I)=BLANK
C
C SPECIAL LINE OPERATORS
C
0017 IF(A(1).EQ.HYPHEN)GO TO 70
0018 IF(A(1).EQ.STAR)GO TO 70
0019 IF(A(1).EQ.SCOLON)GO TO 70
C
C LABEL FIELD
C
0020 I=1
0021 IF(A(1).EQ.TAB)GO TO 12
0022 I(I)=A(1)
0023 I=I+1
0024 IF(I.GT.120)GO TO 80
0025 GO TO 11
0026 NEXT=I+1
C
C COMMAND FIELD
C
0027 J=0
0028 I=NEXT

```

10-AUG-80

13117:26

FORTRAN IV-PLUS V02-SIF
PREMAP.FTN /TR:BLOCKS/WR

```

0029 21 IF(A(1).EQ.TAB)GO TO 22
0030 L(9,1)=A(1)
0031 J=9+1
0032 IF(J.GT.11)GO TO 40
0033 I=1+1
0034 IF(I.GT.120)GO TO 40
0035 GO TO 21
0036 NEXT=1+1
22 C
C COMMENT FIELD
C
0037 30 K=0
0038 I=NEXT
0039 IF(A(1).EQ.TAB)GO TO 40
0040 IF(ALAST.EQ.BLANK.AND.A(1).EQ.BLANK)GO TO 40
0041 IF(A(1).EQ.CR)GO TO 40
0042 ALAST=A(1)
0043 OUTPUT=.TRUE.
0044 L(57+K)=A(1)
0045 K=K+1
0046 IF(K.LE.23)GO TO 42
0047 WRITE(3,106)(L(I),I=1,120)
0048 DO 33 I=1,120
0049 L(I)=BLANK
0050 L(I)=SCOLON
0051 K=0
0052 OUTPUT=.FALSE.
0053 ALAST=BLANK
0054 I=1+1
0055 IF(I.GT.120)GO TO 40
0056 GO TO 31
C
C COPY LINE "EN TOTO"
C
0057 70 DO 71 I=1,120
0058 L(I)=A(1)
C
C GENERATE CORRECTED OUTPUT LINE
C
0059 40 IF(OUTPUT.EQ..TRUE.)WRITE(3,106)(L(I),I=1,120)
0060 GO TO 1
C
C NORMAL EXIT
C
0061 CALL CLOSE(2)
0062 CALL CLOSE(3)
0063 TYPE 103,LINE
0064 STOP
C
C ABNORMAL EXIT
C
0065 CALL CLOSE(2)
0066 CALL CLOSE(3)
0067 TYPE 104,LINE
0068 GO TO 2
C

```

FORTMAN IV-PLUS V02-SIF
PREMAP.FTN /TR:RLOC/S/WP

```

C      LINE STRUCTURE ERROR
C
0069      CALL CLOSE(2)
0070      CALL CLOSE(3)
0071      TYPE 105,LINE
0072      STOP
C
C      FORMAT DECLARATIONS
C
0073      FORMAT(1H,'INPUT FILENAME= ')
0074      FORMAT(1H,'OUTPUT FILENAME= ')
0075      FORMAT(120A1)
0076      FORMAT(1X,'NORMAL EXIT AFTER ',13,' LINES!')
0077      FORMAT(1X,'***WARNING*** FILE ERROR IN LINE ',13/)
0078      FORMAT(1X,'***WARNING*** LINE STRUCTURE ERROR IN LINE ',13/)
0079      FORMAT(120A1)
0080      FORMAT(1X,1005)
0081      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	001374	342
SPDATA	000030	4
SIDATA	000304	98
SVARS	000654	214
		RW,I,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL
		RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 002564 698

NO FPP INSTRUCTIONS GENERATED

PREMAP,LP/LI:1=PREMAP


```

0001      SUBROUTINE FILNAM(UNIT,EXT)
C
C      THIS SUBROUTINE ACCEPTS THE NAME OF THE INPUT OR OUTPUT FILE
C      FROM THE TTY DEVICE &
C      DEFAULT DEVICE
C      UNLESS SPECIFIED IN INPUT STRING
C      UNIT=UNIT NUMBER
C      EXT = LOGICAL(4) BUFFER OF EXTENSION
C
0002      IMPLICIT INTEGER(A-Z)
0003      LOGICAL(4) INSTR,DOT,BLNK,EXT
0004      DIMENSION INSTR(40)
0005      DIMENSION EXT(3)
0006      DATA BLNK,DOT/ ' ','.'/
C
C      INPUT FILE
0007      READ (5,99)(INSTR(I),I=1,40)
0008      FORMAT(40A1)
C      CHECK FOR END OF LINE
0009      DO 1600 I=40,1,-1
0010      J=1
0011      IF(INSTR(I).NE.BLNK)GO TO 1601
0012      TYPE 151
0013      FORMAT(1HS,'>')
0014      GO TO 152
0015      DO 1602 I=1,J
0016      IF(INSTR(I).NE.BLNK) GO TO 1602
C
C      BLANK DISCOVERED-COLLAPSE LINE BY ONE AND DECREASE CHARACTER COUNT
C
0017      DO 1603 K=I,J-1
0018      INSTR(K)=INSTR(K+1)
0019      INSTR(J)=BLNK
0020      J=J-1
0021      GO TO 1601
0022      CONTINUE
0023      DO 1602 I=1,J
0024      IF(INSTR(I).EQ.DOT) GO TO 25
0025      INSTR(J+1)=DOT
0026      INSTR(J+2)=EXT(1)
0027      INSTR(J+3)=EXT(2)
0028      INSTR(J+4)=EXT(3)
0029      J=J+4
0030      CALL SCAM(INSTR,J)
0031      CALL ASSIGN(UNIT,INSTR,J)
0032      RETURN
0033      END

```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000444	146 PW,1,CON,ICL
SIDATA	000042	17 PW,D,CON,ICL

10-AUG-80

13:28:11

FORTRAN JV-PLUS V02-SIE
FILEN.FTN /#P

SVARS 000060 24 PW.D,CON,I,CL
STEPS 000004 2 HW.D,CON,I,CL

TOTAL SPACE ALLOCATED = 000572 149

NO FPP INSTRUCTIONS GENERATED

10-AUG-80

13:28:14

FORTAN IV-PLUS V02-SIE

FILEN.FTN

```
C
0001 SUBROUTINE SCAN(HUF,LTH)
0002 IMPLICIT INTEGER(A-Z)
0003 LOGICAL*4 HUF,DEVICE
0004 DIMENSION HUF(1),DEVICE(4)
0005 DATA DEVICE/'S','Y','O','/'
0006 DO 1 I=1,LTH
0007 IF(HUF(I).EQ.DEVICE(4))RETURN
0008 LTH=LTH+4
0009 DO 2 I=LTH,5,-1
0010 HUF(I)=HUF(I-4)
0011 DO 3 I=1,4
0012 HUF(I)=DEVICE(I)
0013 RETURN
0014 END
```

PROGRAM SECTIONS

NAME	SIZE	ATTRIBUTES
SCODE1	000172	61 RW,I,CON,I,CL
SIDATA	000012	5 RW,D,CON,I,CL
SVARS	000006	3 RW,D,CON,I,CL
STEMPS	000002	1 RW,D,CON,I,CL

TOTAL SPACE ALLOCATED = 000214 70

NO FPP INSTRUCTIONS GENERATED

FILEN,LP/LI:1=FILEN/NOTR

END

DATE
FILMED

12-80

DTIC